SIDDHARTHA

College Code - TP

INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE & Affiliated to JNTUH.)

Vinobha Nagar, Ibrahimpatnam, Ranga Reddy Dist – 501 506, Telangana, INDIA.

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ELECTRONICS AND COMMUNICATION ENGINEERING

2016-2017

S. No	Regulations	Number of Courses	Year of Study
1	R16	08	I year I & II Semesters
2	R15	25	II & III and IV year I & II Semesters

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Ranga Reddy District-501 506.

With effect from 02/08/2016

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

B.Tech COURSE STRUCTURE (2016-17)
(Common for EEE, ECE, CSE, EIE, BME, IT, ETE, ECM, ICE)

I YEAR I SEMESTER

S. No	Course Code	Course Title	L	T	P	Credits
1	MA101BS	Mathematics-I	3	1	0	3
2	CH102BS	Engineering Chemistry	4	0	0	4
3	PH103BS	Engineering Physics-I	3	0	0	3
4	EN104HS	Professional Communication in English	3	0	0	3
5	ME105ES	Engineering Mechanics	3	0	0	3
6	EE106ES	Basic Electrical and Electronics Engineering	4	0	0	4
7	EN107HS	English Language Communication Skills Lab	0	0	3	2
8	ME108ES	Engineering Workshop	0	0	3	2
9	*EA109MC	NSS	0	0	0	0
		Total Credits	**20**	**1**	**6**	**24**

I YEAR II SEMESTER

S. No	Course Code	Course Title	L	T	P	Credits
1	PH201BS	Engineering Physics-II	3	0	0	3
2	MA202BS	Mathematics-II	4	1	0	4
3	MA203BS	Mathematics-III	4	1	0	4
4	CS204ES	Computer Programming in C	3	0	0	3
5	ME205ES	Engineering Graphics	2	0	4	4
6	CH206BS	Engineering Chemistry Lab	0	0	3	2
7	PH207BS	Engineering Physics Lab	0	0	3	2
8	CS208ES	Computer Programming in C Lab	0	0	3	2
9	*EA209MC	NCC/NSO	0	0	0	0
		Total Credits	**16**	**2**	**13**	**24**

*Mandatory Course.

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PROFESSIONAL COMMUNICATION IN ENGLISH

B.Tech. I Year I Sem.
Course Code: **EN104HS/EN204HS**

L	T/P/D	C
3	0/0/0	3

INTRODUCTION

In view of the growing importance of English as a tool for global communication and the consequent emphasis on training students to acquire language skills, the syllabus of English has been designed to develop linguistic and communicative competencies of Engineering students.

In English classes, the focus should be on the skills development in the areas of vocabulary, grammar, reading and writing. For this, the teachers should use the prescribed text book for detailed study. The students should be encouraged to read the texts/poems silently leading to reading comprehension. Reading comprehension passages are given for practice in the class. The time should be utilized for working out the exercises given after each excerpt, and also for supplementing the exercises with authentic materials of a similar kind, for example, from newspaper articles, advertisements, promotional material, etc. *The focus in this syllabus is on skill development, fostering ideas and practice of language skills.*

Course Objectives:

The course will help students to:

a. Improve the language proficiency of students in English with an emphasis on Vocabulary, Grammar, Reading and Writing skills.

b. Equip students to study academic subjects more effectively using the theoretical and Practical components of English syllabus.

c. Develop study skills and communication skills in formal and informal situations.

Course Outcomes:

Students will be able to:

1. Use English Language effectively in spoken and written forms.
2. Comprehend the given texts and respond appropriately.
3. Communicate confidently in formal and informal contexts.

SYLLABUS

Reading Skills:

Objectives:

1. To develop an awareness in students about the significance of silent reading and comprehension.
2. To develop students' ability to guess meanings of words from the context and grasp the overall message of the text, draw inferences, etc., by way of:

- Skimming and Scanning the text
- Intensive and Extensive Reading
- Reading for Pleasure
- Identifying the topic sentence

- Inferring lexical and contextual meaning
- Recognizing Coherence/Sequencing of Sentences

NOTE: The students will be trained in reading skills using the prescribed texts for detailed study. They will be tested in reading comprehension of different 'unseen' passages which may be taken from authentic texts, such as magazines/newspaper articles.

Writing Skills:

Objectives:

1. To develop an awareness in the students about writing as an exact and formal skill
2. To create an awareness in students about the components of different forms of writing, beginning with the lower order ones through;
   - Writing of sentences
   - Use of appropriate vocabulary
   - Paragraph writing
   - Coherence and cohesiveness
   - Narration / description
   - Note Making
   - Formal and informal letter writing
   - Describing graphs using expressions of comparison

In order to improve the proficiency of the students in the acquisition of language skills mentioned above, the following text and course contents, divided into Five Units, are prescribed:

Text Books:

1. ***"Fluency in English – A Course book for Engineering Students"*** by Board of Editors: **Hyderabad: Orient BlackSwan Pvt. Ltd. 2016. Print.**
2. Raman, Meenakshi and Sharma, Sangeeta. ***"Technical Communication- Principles and Practice"*. Third Edition. New Delhi: Oxford University Press. 2015. Print.**

The course content / study material is divided into **Five Units.**

<u>Note</u>: ***Listening and speaking skills are covered in the syllabus of ELCS Lab.***

UNIT –I:
Chapter entitled ***'Presidential Address' by Dr. A.P.J. Kalam*** from ***"Fluency in English– A Course book for Engineering Students"*** published by Orient BlackSwan, Hyderabad.

Vocabulary: Word Formation -- Root Words --The Use of Prefixes and Suffixes– Collocations– Exercises for Practice.

Grammar: Punctuation – Parts of Speech- Articles -Exercises for Practice.

Reading: ***Double Angels*** by David Scott-Reading and Its Importance- Techniques for Effective Reading- Signal Words- Exercises for Practice

Writing: Writing Sentences- Techniques for Effective Writing-- Paragraph Writing- Types, Structure and Features of a Paragraph-Coherence and Cohesiveness: Logical, Lexical and Grammatical Devices - Exercises for Practice

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UNIT –II:

Chapter entitled ***Satya Nadella: Email to Employees on his First Day as CEO*** from ***"Fluency in English– A Course book for Engineering Students"*** Published by Orient BlackSwan, Hyderabad.

Vocabulary: Synonyms and Antonyms – Homonyms, Homophones, Homographs- Exercises for Practice (Chapter 17 ***'Technical Communication- Principles and Practice'. Third Edition*** published by Oxford University Press may also be followed.)

Grammar: Verbs-Transitive, Intransitive and Non-finite Verbs – Mood and Tense—Gerund – Words with Appropriate Prepositions – Phrasal Verbs - Exercises for Practice

Reading: Sub-skills of Reading- Skimming, Scanning, Extensive Reading and Intensive Reading - ***The Road Not Taken*** by **Robert Frost** -- Exercises for Practice

Writing: Letter Writing –Format, Styles, Parts, Language to be used in Formal Letters- Letter of Apology – Letter of Complaint-Letter of Inquiry with Reply – Letter of Requisition — Exercises for Practice

UNIT –III:

From the book entitled ***'Technical Communication- Principles and Practice'. Third Edition*** published by Oxford University Press.

Vocabulary: Introduction- A Brief History of Words – Using the Dictionary and Thesaurus– Changing Words from One Form to Another – Confusables (From Chapter 17 entitled '*Grammar and Vocabulary Development*')

Grammar: Tenses: Present Tense- Past Tense- Future Tense- Active Voice – Passive Voice- Conditional Sentences – Adjective and Degrees of Comparison. (From Chapter 17 entitled '*Grammar and Vocabulary Development*')

Reading: Improving Comprehension Skills – Techniques for Good Comprehension- Skimming and Scanning- Non-verbal Signals – Structure of the Text – Structure of Paragraphs – Punctuation – Author's viewpoint (Inference) – Reader Anticipation: Determining the Meaning of Words – Summarizing- Typical Reading Comprehension Questions. (From Chapter 10 entitled '*Reading Comprehension*')

Writing: Introduction- Letter Writing-Writing the Cover Letter- Cover Letters Accompanying Resumes- Emails. (From Chapter 15 entitled '*Formal Letters, Memos, and Email*')

UNIT –IV:

Chapter entitled ***'Good Manners' by J.C. Hill*** from ***Fluency in English – A Course book for Engineering Students"*** published by Orient Blackswan, Hyderabad.

Vocabulary: Idiomatic Expressions –One- word Substitutes --- Exercises for Practice (Chapter 17 ***'Technical Communication- Principles and Practice'. Third Edition*** published by Oxford University Press may also be followed.)

Grammar: Sequence of Tenses- Concord (Subject in Agreement with the Verb) – Exercises for Practice

Reading: *'If'* poem by **Rudyard Kipling**--Tips for Writing a Review --- Author's Viewpoint – Reader's Anticipation-- Herein the Students will be required to Read and Submit a Review of a Book (Literary or Non-literary) of their choice – Exercises for Practice.

Writing: Information Transfer-Bar Charts-Flow Charts-Tree Diagrams etc., -- Exercises for Practice.
Introduction - Steps to Effective Precis Writing – Guidelines- Samples (Chapter 12 entitled '*The Art of Condensation*' from ***Technical Communication- Principles and Practice. Third Edition*** published by Oxford University Press)

UNIT –V:

Chapter entitled ***'Father Dear Father'*** **by Raj Kinger** from ***Fluency in English – A Course book for Engineering Students"*** Published by Orient BlackSwan, Hyderabad

Vocabulary: Foreign Words—Words borrowed from other Languages- Exercises for Practice

Grammar: Direct and Indirect Speech- Question Tags- Exercises for Practice

Reading: Predicting the Content- Understanding the Gist – SQ3R Reading Technique- Study Skills – Note Making - Understanding Discourse Coherence – Sequencing Sentences. (From Chapter 10 entitled **'Reading Comprehension'** - ***Technical Communication- Principles and Practice. Third Edition*** published by Oxford University Press.)

Writing: Technical Reports- Introduction – Characteristics of a Report – Categories of Reports –Formats- Prewriting – Structure of Reports (Manuscript Format) - Types of Reports - Writing the Report. (From Chapter 13 entitled **'Technical Reports'** - ***Technical Communication- Principles and Practice. Third Edition*** published by Oxford University Press.)

- **Exercises from both the texts not prescribed shall be used for classroom tasks.**

References

1 Green, David. *Contemporary English Grammar –Structures and Composition.* MacMillan India. 2014 (Print)
2. Rizvi, M. Ashraf. *Effective Technical Communication*. Tata Mc Graw –Hill. 2015 (Print).

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2. Engineering Mechanics/ Irving Shames, G. Krishna Mohan Rao / Prentice Hall
3. Foundations and applications of Engineering Mechanics by HD Ram and AK Chouhan, Cambridge publications.

References:

1. A Text of Engineering Mechanics /YVD Rao/ K. Govinda Rajulu/ M. Manzoor Hussain / Academic Publishing Company
2. Engineering Mechanics / Bhattacharyya/ Oxford.

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ENGLISH LANGUAGE COMMUNICATION SKILLS (ELCS) LAB

B.Tech. I Year I Sem. **L T/P/D C**
Course Code: **EN107HS/EN207HS** **0 0/3/0 2**

The **English Language Communication Skills (ELCS) Lab** focuses on the production and practice of sounds of language and familiarizes the students with the use of English in everyday situations both in formal and informal contexts.

Course Objectives:

- To facilitate computer-assisted multi-media instruction enabling individualized and independent language learning
- To sensitize the students to the nuances of English speech sounds, word accent, intonation and rhythm
- To bring about a consistent accent and intelligibility in students' pronunciation of English by providing an opportunity for practice in speaking
- To improve the fluency of students in spoken English and neutralize their mother tongue influence
- To train students to use language appropriately for public speaking, group discussions and interviews

Course Outcomes:

Students will be able to attain:

- Better understanding of nuances of English language through audio- visual experience and group activities
- Neutralization of accent for intelligibility
- Speaking skills with clarity and confidence which in turn enhances their employability skills.

Syllabus: English Language Communication Skills Lab (ELCS) shall have two parts:

- **Computer Assisted Language Learning (CALL) Lab**
- **Interactive Communication Skills (ICS) Lab**

Listening Skills:

Objectives

- To enable students develop their listening skills so that they may appreciate the role in the LSRW skills approach to language and improve their pronunciation
- To equip students with necessary training in listening, so that they can comprehend the speech of people of different backgrounds and regions.

Students should be given practice in listening to the sounds of the language, to be able to recognize them and find the distinction between different sounds, to be able to mark stress and recognize and use the right intonation in sentences.

- Listening for general content
- Listening to fill up information
- Intensive listening
- Listening for specific information

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Speaking Skills:

Objectives

- To involve students in speaking activities in various contexts
- To enable students express themselves fluently and appropriately in social and professional contexts :
  - Oral practice
  - Describing objects/situations/people
  - Role play – Individual/Group activities
  - Just A Minute (JAM) Sessions.

The following course content is prescribed for the **English Language Communication Skills Lab.**

Exercise – I

CALL Lab:

Understand: Listening Skill- Its importance – Purpose- Process- Types- Barriers- Effective Listening.

Practice: Introduction to Phonetics – Speech Sounds – Vowels and Consonants – Minimal Pairs- Consonant Clusters- Past Tense Marker and Plural Marker.

Testing Exercises

ICS Lab:

Understand: Spoken vs. Written language- Formal and Informal English.

Practice: Ice-Breaking Activity and JAM Session- Situational Dialogues – Greetings – Taking Leave – Introducing Oneself and Others.

Exercise – II

CALL Lab:

Understand: Structure of Syllables – Word Stress– Weak Forms and Strong Forms – Sentence Stress – Intonation.

Practice: Basic Rules of Word Accent - Stress Shift - Weak Forms and Strong Forms- Sentence Stress – Intonation.

Testing Exercises

ICS Lab:

Understand: Features of Good Conversation – Strategies for Effective Communication.

Practice: Situational Dialogues – Role-Play- Expressions in Various Situations –Making Requests and Seeking Permissions - Telephone Etiquette.

Exercise - III

CALL Lab:

Understand: Errors in Pronunciation-the Influence of Mother Tongue (MTI).

Practice: Common Indian Variants in Pronunciation – Differences between British and American Pronunciation.

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Testing Exercises

ICS Lab:

Understand: Descriptions- Narrations- Giving Directions and Guidelines.

Practice: Giving Instructions – Seeking Clarifications – Asking for and Giving Directions – Thanking and Responding – Agreeing and Disagreeing – Seeking and Giving Advice – Making Suggestions.

Exercise – IV

CALL Lab:

Understand: Listening for General Details.

Practice: Listening Comprehension Tests.

Testing Exercises

ICS Lab:

Understand: Public Speaking – Exposure to Structured Talks - Non-verbal Communication- Presentation Skills.

Practice: Making a Short Speech – Extempore- Making a Presentation.

Exercise – V

CALL Lab:

Understand: Listening for Specific Details.

Practice: Listening Comprehension Tests.

Testing Exercises

ICS Lab:

Understand: Group Discussion- Interview Skills.

Practice: Group Discussion- Mock Interviews.

Minimum Requirement of infrastructural facilities for ELCS Lab:

1. **Computer Assisted Language Learning (CALL) Lab:**

   The Computer Assisted Language Learning Lab has to accommodate 40 students with 40 systems, with one Master Console, LAN facility and English language learning software for self- study by students.

   System Requirement (Hardware component):

   Computer network with LAN facility (minimum 40 systems with multimedia) with the following specifications:

   Computers with Suitable Configuration

   High Fidelity Headphones

2. **Interactive Communication Skills (ICS) Lab:**

   The Interactive Communication Skills Lab: A Spacious room with movable chairs and audio-visual aids with a Public Address System, a T. V. or LCD, a digital stereo –audio and video system and camcorder etc.

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Lab Manuals:

1) A book entitled ***"ELCS Lab Manual – A Workbook for CALL and ICS Lab Activities"*** by Board of Editors: Hyderabad: Orient BlackSwan Pvt. Ltd. 2016. Print.
2) Hart, Steve; Nair, Aravind R.; Bhambhani, Veena. ***"EMBARK- English for undergraduates"*** Delhi: Cambridge University Press. 2016. Print.

Suggested Software:

1) Cambridge Advanced Learners' English Dictionary with CD.
2) Grammar Made Easy by Darling Kindersley.
3) Punctuation Made Easy by Darling Kindersley.
4) Oxford Advanced Learner's Compass, 8^{th} Edition.
5) English in Mind (Series 1-4), Herbert Puchta and Jeff Stranks with Meredith Levy, Cambridge.
6) English Pronunciation in Use (Elementary, Intermediate, Advanced) Cambridge University Press.
7) TOEFL and GRE (KAPLAN, AARCO and BARRONS, USA, Cracking GRE by CLIFFS).

References:

1) Jayashree Mohanraj. *Let Us Hear Them Speak*. New Delhi: Sage Texts. 2015. Print.
Hancock, M. *English Pronunciation in Use. Intermediate Cambridge*: Cambridge University Press. 2009. Print.

ENGINEERING CHEMISTRY

B.Tech. I Year I Sem. **L T/P/D C**
Course Code: **CH102BS/CH202BS** **4 0/0/0 4**

Course Objectives:

1) To bring adaptability to new developments in Engineering Chemistry and to acquire the skills required to become a perfect engineer.
2) To include the importance of water in industrial usage, significance of corrosion control to protect the structures, polymers and their controlled usage.
3) To acquire knowledge of engineering materials and about fuels and batteries.
4) To acquire required knowledge about engineering materials like cement, refractories and composites.

Course Outcomes:

Students will gain the basic knowledge of electrochemical procedures related to corrosion and its control. They can understand the basic properties of water and its usage in domestic and industrial purposes. They learn the use of fundamental principles to make predictions about the general properties of materials. They can predict potential applications of chemistry and practical utility in order to become good engineers and entrepreneurs.

UNIT-I

Water and its treatment: Introduction – hardness of water – causes of hardness – types of hardness: temporary and permanent – expression and units of hardness – Estimation of hardness of water by complexometric method. Numerical problems. Potable water and its specifications- Steps involved in the treatment of potable water - Disinfection of potable water by chlorination and Ozonization. Defluoridation – Nalgonda technique - Determination of F^- ion by ion- selective electrode method.

Boiler troubles:

Sludges, scales and Caustic embrittlement. Internal treatment of Boiler feed water – Calgon conditioning – Phosphate conditioning - Colloidal conditioning – Softening of water by ion-exchange processes. Desalination of water – Reverse osmosis. Numerical problems – Sewage water - Steps involved in treatment of sewage.

UNIT-II

Electrochemistry and Batteries:

Electrochemistry: Electrode- electrode potential, standard electrode potential, types of electrodes – Construction and functioning of Standard hydrogen electrode, calomel and glass electrode. Nernst equation - electrochemical series and its applications. Electrochemical cells: Daniel cell – cell notation, cell reaction and cell emf — Concept of concentration cells – Electrolyte concentration cell –Numerical problems.

Batteries: Cell and battery - Primary battery (dry cell, alkaline cell and Lithium cell) and Secondary battery (lead acid, Ni-Cd and lithium ion cell),

Fuel cells: Hydrogen –oxygen and methanol-oxygen fuel cells – Applications.

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UNIT-III

Polymers: Definition – Classification of polymers with examples – Types of polymerization – addition (free radical addition) and condensation polymerization with examples.

Plastics: Definition and characteristics- thermoplastic and thermosetting plastics, compounding and fabrication of plastics (compression and injection moulding). Preparation, Properties and engineering applications of PVC and Bakelite.

Fibers: Characteristics of fibers – preparation, properties and applications of Nylon-6, 6 and Dacron. Fiber reinforced plastics (FRP) – Applications.

Rubbers: Natural rubber and its vulcanization - compounding of rubber.

Elastomers: Characteristics –preparation – properties and applications of Buna-S, Butyl and Thiokol rubber.

Conducting polymers: Characteristics and Classification with examples-mechanism of conduction in trans-polyacetylene and applications of conducting polymers.

Biodegradable polymers: Concept and advantages - Polylactic acid and poly vinyl alcohol and their applications.

UNIT-IV

Fuels and Combustion: Classification- solid fuels: coal – analysis of coal – proximate and ultimate analysis and their significance. Liquid fuels – petroleum and its refining, cracking – types – moving bed catalytic cracking. Knocking – octane and cetane rating, synthetic petrol - Fischer-Tropsch's process; Gaseous fuels – composition and uses of natural gas, LPG and CNG.

Combustion: Definition, Calorific value of fuel – HCV, LCV; Calculation of air quantity required for combustion of a fuel.

UNIT-V

Cement, Refractories, Lubricants and Composites:

Cement: Portland cement, its composition, setting and hardening of Portland cement.

Special cements: White cement, water proof cement, High alumina cement and Acid resistant cement.

Refractories: Classification, characteristics of good refractories, Refractoriness, refractoriness under load, porosity and chemical inertness – applications of refractories.

Lubricants: Classification of lubricants with examples-characteristics of a good lubricants - mechanism of lubrication (thick film, thin film and extreme pressure)- properties of lubricants: viscosity, cloud point, pour point, flash point and fire point.

Composites: Introduction- Constituents of composites – advantages, classification and constituents of composites. Applications of composites.

Text books:

1) Engineering Chemistry by P.C. Jain and M. Jain, Dhanpatrai Publishing Company, New Delhi (2010)

2) Engineering Chemistry by Rama Devi, Venkata Ramana Reddy and Rath, Cengage learning, New Delhi. (2016)

Reference Books:

1) Engineering Chemistry by Shikha Agarwal, Cambridge University Press, Delhi (2015)
2) Engineering Chemistry by Shashi Chawla, Dhanpatrai and Company (P) Ltd. Delhi (2011)
3) Engineering Chemistry by Thirumala Chary and Laxminarayana, Scitech Publishers, Chennai (2016).

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CH206BS: ENGINEERING CHEMISTRY LAB

B.Tech. I Year II Sem.

L	T/P/D	C
0	0/3/0	2

LIST OF EXPERIMENTS

Volumetric Analysis:

1. Estimation of Ferrous ion by Dichrometry.
2. Estimation of hardness of water by Complexometric method using EDTA.
3. Estimation of Ferrous and Ferric ions in a given mixture by Dichrometry.
4. Estimation Ferrous ion by Permanganometry.
5. Estimation of copper by Iodomery.
6. Estimation of percentage of purity of MnO_2 in pyrolusite
7. Determination of percentage of available chlorine in bleaching powder.
8. Determination of salt concentration by ion- exchange resin.

Instrumental methods of Analysis:

1. Estimation of HCl by Conductometry.
2. Estimation of Ferrous ion by Potentiometry.
3. Determination of Ferrous iron in cement by Colorimetric method.
4. Determination of viscosity of an oil by Redwood / Oswald's Viscometer.
5. Estimation of manganese in $KMnO_4$ by Colorimetric method.
6. Estimation o f HCl and Acetic acid in a given mixture by Conductometry.
7. Estimation of HCl by Potentiometry.

Preparation of Polymers:

1. Preparation of Bakelite and urea formaldehyde resin.

Note: All the above experiments must be performed.

Text Books:

1. Vogel's Text Book of Quantitative Chemical Analysis, 5th Edition (2015)
2. G. H. Jeffery, J. Bassett, J. Mendham and R. C. Denney.
3. A Text Book on experiments and calculations in Engineering Chemistry by S.S. Dara S. Chand & Company Ltd., Delhi (2003).

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PH201BS: ENGINEERING PHYSICS - II

B.Tech. I Year II Sem.

L T/P/D C
3 0/0/0 3

Course Objectives:

- To understand the behavior of a particle quantum mechanically.
- To be able to distinguish pure and impure semi conductors and understand formation of P-N Junction.
- To understand various magnetic and dielectric properties of materials.
- To study super conductor behavior of materials.

Course Outcomes: After completion of this course the student is able to

- Realize the importance of behavior of a particle quantum mechanically.
- Learn concentration estimation of charge carriers in semi conductors.
- Learn various magnetic dielectric properties and apply them in engineering applications.
- Know the basic principles and applications of super conductors.

UNIT - I

Principles of Quantum Mechanics: Waves and particles, de-Broglie hypothesis, matter waves, Davisson and Germer experiment, Heisenberg uncertainty principle, Schrodinger time independent wave equation, physical significance of wave function, particle in 1-D potential box, electron in periodic potential, Kronig-Penny model (qualitative treatment), E-K curve, origin of energy band formation in solids.

UNIT - II

Semiconductor Physics: Fermi level in intrinsic and extrinsic semiconductors, calculation of carrier concentration in intrinsic & extrinsic semiconductors, direct and indirect band gap semiconductors, formation of PN junction, open circuit PN junction, energy diagram of PN junction diode, solar cell: I-V characteristics and applications.

UNIT - III

Dielectric Properties: Electric dipole, dipole moment, dielectric constant, polarizability, electric susceptibility, displacement vector, electronic, ionic and orientation polarizations and calculation of their polarizabilitites, internal field, Clausius-Mossotti relation, Piezoelectricity, pyroelectricity and ferroelectricity-$BaTiO_3$ structure.

UNIT - IV

Magnetic Properties & Superconductivity: Permeability, field intensity, magnetic field induction, magnetization, magnetic susceptibility, origin of magnetic moment, Bohr magneton, classification of dia, para and ferro magnetic materials on the basis of magnetic moment, hysteresis curve based on domain theory, soft and hard magnetic materials, properties of anti-ferro and ferri magnetic materials,

Superconductivity: Superconductivity phenomenon, Meissner effect, applications of superconductivity.

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UNIT - V

Introduction to nanoscience: Origin of nanoscience, nanoscale, surface to volume ratio, quantum confinement, dominance of electromagnetic forces, random molecular motion, bottom-up fabrication: Sol-gel, CVD and PVD techniques, top-down fabrication: ball mill method, characterization by XRD, SEM and TEM.

Text Books:

1. Solid State Physics, A. J. Dekkar, Macmillan publishers Ind. Ltd.,
2. Solid State Physics, Chales Kittel, Wiley student edition.
3. Fundamentals of Physics, Alan Giambattisa, BM Richardson and Robert C Richardson, Tata McGraw hill Publishers.

Reference Books:

1. Modern Engineering Physics, K. Vijaya Kumar, S. Chandralingam S. Chand & Co. Pvt. Ltd.,
2. University Physics, Francis W. Sears, Hugh D. Young, Marle Zeemansky and Roger A Freedman, Pearson Education.
3. Fundamentals of Acoustics, Kinster and Frey, John Wiley and Sons.
4. Introduction to Quantum Mechanics Leonard I. Schiff McGraw-Hill

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PH107BS/PH207BS: ENGINEERING PHYSICS LAB

B.Tech. I Year II Sem.

L	T/P/D	C
0	0/3/0	2

LIST OF EXPERIMENTS

1. Dispersive power of the material of a prism – Spectrometer.
2. Determination of wavelengths of white source – Diffraction grating.
3. Newton's Rings – Radius of curvature of Plano convex lens.
4. Melde's experiment – Transverse and longitudinal modes.
5. Charging, discharging and time constant of an R-C circuit.
6. L-C-R circuit – Resonance & Q-factor.
7. Magnetic field along the axis of current carrying coil – Stewart and Gees method and to verify Biot – Savart's law.
8. Study the characteristics of LED and LASER diode.
9. Bending losses of fibres & Evaluation of numerical aperture of a given fibre.
10. Energy gap of a material of p-n junction.
11. Torsional pendulum – Rigidity modulus.
12. Wavelength of light, resolving power and dispersive power of a diffraction grating using laser.
13. V-I characteristics of a solar cell.

Note: Minimum 10 experiments must be performed.

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CS104ES/CS204ES: COMPUTER PROGRAMMING IN C

B.Tech. I Year II Sem.

L	T/P/D	C
3	0/0/0	3

Course Objectives:

- To learn the fundamentals of computers.
- To understand the various steps in Program development.
- To learn the syntax and semantics of C Programming Language.
- To learn how to write modular and readable C Programs.
- To learn to write programs using structured programming approach in C to solve problems.

Course Outcomes:

- Demonstrate the basic knowledge of computer hardware and software.
- Ability to write algorithms for solving problems.
- Ability to draw flowcharts for solving problems.
- Ability to code a given logic in C programming language.
- Gain knowledge in using C language for solving problems.

UNIT - I

Introduction to Computers – Computer Systems, Computing Environments, Computer Languages, Creating and running programs, Program Development, algorithms and flowcharts , Number systems-Binary, Decimal, Hexadecimal and Conversions, storing integers and real numbers.

Introduction to C Language – Background, C Programs, Identifiers, Types, Variables, Constants, Input / Output, Operators(Arithmetic, relational, logical, bitwise etc.), Expressions, Precedence and Associativity, Expression Evaluation, Type conversions, Statements- Selection Statements(making decisions) – if and switch statements, Repetition statements (loops)-while, for, do-while statements, Loop examples, other statements related to looping – break, continue, goto, Simple C Program examples.

UNIT - II

Functions-Designing Structured Programs, Functions, user defined functions, inter function communication, Standard functions, Scope, Storage classes-auto, register, static, extern, scope rules, type qualifiers, recursion- recursive functions, Limitations of recursion, example C programs.

Arrays – Concepts, using arrays in C, inter function communication, array applications- linear search, binary search and bubble sort, two – dimensional arrays, multidimensional arrays, C program examples.

UNIT - III

Pointers – Introduction (Basic Concepts), Pointers for inter function communication, pointers to pointers, compatibility, Pointer Applications-Arrays and Pointers, Pointer Arithmetic and

arrays, Passing an array to a function, memory allocation functions, array of pointers, programming applications, pointers to void, pointers to functions.
Strings – Concepts, C Strings, String Input / Output functions, arrays of strings, string manipulation functions, string / data conversion, C program examples.

UNIT - IV

Enumerated, Structure and Union Types – The Type Definition (typedef), Enumerated types, Structures –Declaration, initialization, accessing structures, operations on structures, Complex structures-Nested structures, structures containing arrays, structures containing pointers, arrays of structures, structures and functions, Passing structures through pointers, self referential structures, unions, bit fields, C programming examples, command–line arguments, Preprocessor commands.

UNIT – V

Input and Output – Concept of a file, streams, text files and binary files, Differences between text and binary files, State of a file, Opening and Closing files, file input / output functions (standard library input / output functions for files), file status functions (error handling), Positioning functions (fseek ,rewind and ftell), C program examples.

Text Books:

1. Computer Science: A Structured Programming Approach Using C, B. A. Forouzan and R. F. Gilberg, Third Edition, Cengage Learning.
2. Programming in C. P. Dey and M Ghosh , Second Edition, Oxford University Press.

Reference Books:

1. The C Programming Language, B.W. Kernighan and Dennis M. Ritchie, Second Edition, Pearson education.
2. Programming with C, B. Gottfried, 3rd edition, Schaum's outlines, McGraw Hill Education (India) Pvt Ltd.
3. C From Theory to Practice, G S. Tselikis and N D. Tselikas, CRC Press.
4. Basic computation and Programming with C, Subrata Saha and S. Mukherjee, Cambridge University Press.

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CS108ES/CS208ES: COMPUTER PROGRAMMING IN C LAB

B.Tech. I Year II Sem.

L	T/P/D	C
0	0/3/0	2

Course Objective:

- To write programs in C using structured programming approach to solve the problems.

Course Outcomes:

- Ability to design and test programs to solve mathematical and scientific problems.
- Ability to write structured programs using control structures and functions.

Recommended Systems/Software Requirements:

- Intel based desktop PC
- GNU C Compiler

1. a) Write a C program to find the factorial of a positive integer.
   b) Write a C program to find the roots of a quadratic equation.

2. a) Write a C program to determine if the given number is a prime number or not.
   b) A Fibonacci sequence is defined as follows: the first and second terms in the sequence are 0 and 1. Subsequent terms are found by adding the preceding two terms in the sequence. Write a C program to generate the first n terms of the sequence.

3. a) Write a C program to construct a pyramid of numbers.
   b) Write a C program to calculate the following Sum:
   $Sum=1-x^2/2! +x^4/4!-x^6/6!+x^8/8!-x^{10}/10!$

4. a) The least common multiple (LCM) of two positive integers a and b is the smallest integer that is evenly divisible by both a and b. Write a C program that reads two integers and calls LCM (a, b) function that takes two integer arguments and returns their LCM. The LCM (a, b) function should calculate the least common multiple by calling the GCD (a, b) function and using the following relation:
   LCM (a, b) = ab / GCD (a, b)
   b) Write a C program that reads two integers n and r to compute the ncr value using the following relation:
   n_{c_r} (n, r) = n! / r! (n-r)! . Use a function for computing the factorial value of an integer.

5. a) Write C program that reads two integers x and n and calls a recursive function to compute x^n
   b) Write a C program that uses a recursive function to solve the Towers of Hanoi problem.
   c) Write a C program that reads two integers and calls a recursive function to compute n_{c_r} value.

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6. a) Write a C program to generate all the prime numbers between 1 and n, where n is a value supplied by the user using Sieve of Eratosthenes algorithm.
   b) Write a C program that uses non recursive function to search for a Key value in a given list of integers. Use linear search method.

7. a) Write a menu-driven C program that allows a user to enter n numbers and then choose between finding the smallest, largest, sum, or average. The menu and all the choices are to be functions. Use a switch statement to determine what action to take. Display an error message if an invalid choice is entered.
   b) Write a C program that uses non recursive function to search for a Key value in a given sorted list of integers. Use binary search method.

8 a) Write a C program that implements the Bubble sort method to sort a given list of integers in ascending order.
   b) Write a C program that reads two matrices and uses functions to perform the following:
      1. Addition of two matrices
      2. Multiplication of two matrices

9. a) Write a C program that uses functions to perform the following operations:
      1. to insert a sub-string into a given main string from a given position.
      2. to delete n characters from a given position in a given string.
   b) Write a C program that uses a non recursive function to determine if the given string is a palindrome or not.

10. a) Write a C program to replace a substring with another in a given line of text.
    b) Write a C program that reads 15 names each of up to 30 characters, stores them in an array, and uses an array of pointers to display them in ascending (ie. alphabetical) order.

11. a) 2's complement of a number is obtained by scanning it from right to left and complementing all the bits after the first appearance of a 1. Thus 2's complement of 11100 is 00100. Write a C program to find the 2's complement of a binary number.
    b) Write a C program to convert a positive integer to a roman numeral. Ex. 11 is converted to XI.

12. a) Write a C program to display the contents of a file to standard output device.
    b) Write a C program which copies one file to another, replacing all lowercase characters with their uppercase equivalents.

13. a) Write a C program to count the number of times a character occurs in a text file. The file name and the character are supplied as command-line arguments.
    b) Write a C program to compare two files, printing the first line where they differ.

14. a) Write a C program to change the nth character (byte) in a text file. Use fseek function.

b) Write a C program to reverse the first n characters in a file. The file name and n are specified on the command line. Use fseek function.

15. a) Write a C program to merge two files into a third file (i.e., the contents of the firs t file followed by those of the second are put in the third file).

b) Define a macro that finds the maximum of two numbers. Write a C program that uses the macro and prints the maximum of two numbers.

Reference Books:

1. Mastering C, K.R. Venugopal and S.R. Prasad, TMH Publishers.
2. Computer Programming in C, V. Rajaraman, PHI.
3. Programming in C, Stephen G. Kochan, Fourth Edition, Pearson Education.
4. C++: The complete reference, H. Schildt, TMH Publishers.

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II YEAR I SEMESTER

Code	Subject	L	T/P/D	C
A30007	Mathematics - III	4	-	4
A30405	Probability Theory and Stochastic Processes	4	-	4
A30407	Switching Theory and Logic Design	4	-	4
A30204	Electrical Circuits	4	-	4
A30404	Electronic Devices and Circuits	4	-	4
A30406	Signals and Systems	4	-	4
A30482	Electronic Devices and Circuits Lab.	-	3	2
A30481	Basic Simulation Lab.	-	3	2
	Total	**24**	**6**	**28**

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	L	T/P/D	C
II Year B.Tech. ECE-I Sem	4	-/-/-	4

(A30404) ELECTRONIC DEVICES AND CIRCUITS

Objectives:

This is a fundamental course, basic knowledge of which is required by all the circuit branch engineers. This course focuses:

- To familiarize the student with the principle of operation, analysis and design of Junction diode, BJT and FET transistors and amplifier circuits.
- To understand diode as rectifier.
- To study basic principle of filter circuits and various types.

UNIT -I:

P-N Junction Diode: Qualitative Theory of P-N Junction, P-N Junction as a Diode, Diode Equation, Volt-Ampere Characteristics, Temperature dependence of VI characteristic, Ideal versus Practical – Resistance levels (Static and Dynamic), Transition and Diffusion Capacitances, Diode Equivalent Circuits, Load Line Analysis, Breakdown Mechanisms in Semiconductor Diodes, Zener Diode Characteristics.

Special Purpose Electronic Devices: Principle of Operation and Characteristics of Tunnel Diode (with the help of Energy Band Diagram), Varactor Diode, SCR and Semiconductor Photo Diode.

UNIT-II:

Rectifiers and Filters : The P-N junction as a Rectifier, Half wave Rectifier, Full wave Rectifier, Bridge Rectifier, Harmonic components in a Rectifier Circuit, Inductor Filters, Capacitor Filters, L- Section Filters, p- Section Filters, Comparision of Filters, Voltage Regulation using Zener Diode.

UNIT-III:

Bipolar Junction Transistor and UJT: The Junction Transistor, Transistor Current Components, Transistor as an Amplifier, Transistor Construction, BJT Operation, BJT Symbol, Common Base, Common Emitter and Common Collector Configurations, Limits of Operation , BJT Specifications, BJT Hybrid Model, Determination of h-parameters from Transistor Characteristics, Comparison of CB, CE, and CC Amplifier Configurations, UJT and Characteristics.

UNIT-IV:

Transistor Biasing and Stabilization: Operating Point, The DC and AC Load lines, Need for Biasing, Fixed Bias, Collector Feedback Bias, Emitter Feedback Bias, Collector - Emitter Feedback Bias, Voltage Divider Bias,

Bias Stability, Stabilization Factors, Stabilization against variations in VBE and ß, Bias Compensation using Diodes and Transistors, Thermal Runaway, Thermal Stability, Analysis of a Transistor Amplifier Circuit using h-Parameters.

UNIT-V:

Field Effect Transistor and FET Amplifiers

Field Effect Transistor: The Junction Field Effect Transistor (Construction, principle of operation, symbol) – Pinch-off Voltage - Volt-Ampere characteristics, The JFET Small Signal Model, MOSFET (Construction, principle of operation, symbol), MOSFET Characteristics in Enhancement and Depletion modes.

FET Amplifiers: FET Common Source Amplifier, Common Drain Amplifier, Generalized FET Amplifier, Biasing FET, FET as Voltage Variable Resistor, Comparison of BJT and FET.

TEXT BOOKS:

1. Millman's Electronic Devices and Circuits – J. Millman, C.C.Halkias, and Satyabrata Jit, 2 Ed.,1998, TMH.
2. Electronic Devices and Circuits – Mohammad Rashid, Cengage Learing, 2013
3. Electronic Devices and Circuits – David A. Bell, 5 Ed, Oxford

REFERENCE BOOKS:

1. Integrated Electronics – J. Millman and Christos C. Halkias, 1991 Ed., 2008, TMH.
2. Electronic Devices and Circuits – R.L. Boylestad and Louis Nashelsky, 9 Ed., 2006, PEI/PHI.
3. Electronic Devices and Circuits – B. P. Singh, Rekha Singh, Pearson, 2Ed, 2013.
4. Electronic Devices and Circuits --K. Lal Kishore, 2 Ed., 2005, BSP.
5. Electronic Devices and Circuits – Anil K. Maini, Varsha Agarwal, 1 Ed., 2009, Wiley India Pvt. Ltd.
6. Electronic Devices and Circuits – S.Salivahanan, N.Suresh Kumar, A.Vallavaraj, 2 Ed., 2008, TMH.

Course Outcomes:

At the end of the course, the student will be able to:

- Understand and Analyse the different types of diodes, operation and its characteristics
- Design and analyse the DC bias circuitry of BJT and FET
- Design biasing circuits using diodes and transistors.
- To analyze and design diode application circuits, amplifier circuits and oscillatorsemploying BJT, FET devices.

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II Year B.Tech. ECE-I Sem **L T/P/D C**

- -/3/- 2

(A30482) ELECTRONIC DEVICES AND CIRCUITS LAB

PART A: (Only for Viva-voce Examination)

Electronic Workshop Practice (In 3 Lab Sessions):

1. Identification, Specifications, Testing of R, L, C Components (Color Codes), Potentiometers, Switches (SPDT, DPDT, and DIP), Coils, Gang Condensers, Relays, Bread Boards, PCB's
2. Identification, Specifications and Testing of Active Devices, Diodes, BJT's, Low power JFET's, MOSFET's, Power Transistors, LED's, LCD's, SCR, UJT.
3. Study and operation of

i) Multimeters (Analog and Digital)

ii) Function Generator

iii) Regulated Power Supplies

iv) CRO.

PART B: (For Laboratory Examination – Minimum of 10 experiments)

1. Forward & Reverse Bias Characteristics of PN Junction Diode.
2. Zener diode characteristics and Zener as voltage Regulator.
3. Input & Output Characteristics of Transistor in CB Configuration and h-parameter calculations.
4. Input & Output Characteristics of Transistor in CE Configuration and h-parameter calculations.
5. Half Wave Rectifier with & without filters.
6. Full Wave Rectifier with & without filters.
7. FET characteristics.
8. Design of Self-bias circuit.
9. Frequency Response of CC Amplifier.
10. Frequency Response of CE Amplifier.
11. Frequency Response of Common Source FET amplifier .
12. SCR characteristics.
13. UJT Characteristics

PART C:Equipment required for Laboratories:

1. Regulated Power supplies (RPS) -0-30 V

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2.	CRO's -0-20 MHz.	
3.	Function Generators	-0-1 MHz.
4.	Multimeters	
5.	Decade Resistance Boxes/Rheostats	
6.	Decade Capacitance Boxes	
7.	Ammeters (Analog or Digital)	-0-20 μA, 0-50μA, 0-100μA, 0-200μA, 0-10 mA.
8.	Voltmeters (Analog or Digital)	-0-50V, 0-100V, 0-250V
9.	Electronic Components	-Resistors, Capacitors, BJTs, LCDs, SCRs, UJTs, FETs, LEDs, MOSFETs, Diodes- Ge& Si type, Transistors – NPN, PNP type)

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II Year B.Tech. ECE-I Sem	L	T/P/D	C
	4	-/-/-	4

(A30406) SIGNALS AND SYSTEMS

Objectives:

This is a core subject, basic knowledge of which is required by all the engineers.

This course focuses on:

- To get an in-depth knowledge about signals, systems and analysis of the same using various transforms.

UNIT-I:

Signal Analysis and Fourier Series

Signal Analysis: Analogy between Vectors and Signals, Orthogonal Signal Space, Signal approximation using Orthogonal functions, Mean Square Error, Closed or complete set of Orthogonal functions, Orthogonality in Complex functions, Exponential and Sinusoidal signals, Concepts of Impulse function, Unit Step function, Signum function.

Fourier Series: Representation of Fourier series, Continuous time periodic signals, Properties of Fourier Series, Dirichlet's conditions, Trigonometric Fourier Series and Exponential Fourier Series, Complex Fourier spectrum.

UNIT-II:

Fourier Transforms and Sampling

Fourier Transforms: Deriving Fourier Transform from Fourier Series, Fourier Transform of arbitrary signal, Fourier Transform of standard signals, Fourier Transform of Periodic Signals, Properties of Fourier Transform, Fourier Transforms involving Impulse function and Signum function, Introduction to Hilbert Transform.

Sampling: Sampling theorem – Graphical and analytical proof for Band Limited Signals, Typers of Sampling - Impulse Sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, Effect of under sampling – Aliasing, Introduction to Band Pass sampling.

UNIT-III:

Signal Transmission Through Linear Systems: Linear System, Impulse response, Response of a Linear System, Linear Time Invariant (LTI) System, Linear Time Variant (LTV) System, Transfer function of a LTI system, Filter characteristics of Linear Systems, Distortion less transmission through a system, Signal bandwidth, System bandwidth, Ideal LPF, HPF and BPF characteristics, Causality and Paley-Wiener criterion for physical realization, Relationship between Bandwidth and Rise time.

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UNIT-IV:

Convolution and Correlation of Signals: Concept of convolution in Time domain and Frequency domain, Graphical representation of Convolution, Convolution property of Fourier Transforms, Cross Correlation and Auto Correlation of functions, Properties of Correlation function, Energy density spectrum, Parseval's Theorem, Power density spectrum, Relation between Auto Correlation function and Energy/Power spectral density function, Relation between Convolution and Correlation, Detection of periodic signals in the presence of Noise by Correlation, Extraction of signal from noise by filtering.

UNIT-V:

Laplace Transforms and Z-Transforms

Laplace Transforms: Review of Laplace Transforms (L.T), Partial fraction expansion, Inverse Laplace Transform, Concept of Region of Convergence (ROC) for Laplace Transforms, Constraints on ROC for various classes of signals, Properties of L.T, Relation between L.T and F.T of a signal, Laplace Transform of certain signals using waveform synthesis.

Z–Transforms: Fundamental difference between Continuous and Discrete time signals, Discrete time signal representation using Complex exponential and Sinusoidal components, Periodicity of Discrete time signal using complex exponential signal, Concept of Z- Transform of a Discrete Sequence, Distinction between Laplace, Fourier and Z Transforms, Region of Convergence in Z-Transform, Constraints on ROC for various classes of signals, Inverse Z-transform, Properties of Z-transforms.

TEXT BOOKS:

1. Signals, Systems & Communications - B.P. Lathi, 2013, BSP.
2. Signals and Systems - A.V. Oppenheim, A.S. Willsky and S.H. Nawab, 2 Ed., PHI.

REFERENCE BOOKS:

1. Signals & Systems - Simon Haykin and Van Veen,Wiley, 2 Ed.
2. Signals and Signals – Iyer and K. Satya Prasad, Cengage Learning
3. Signals and Systems – A.Rama Krishna Rao – 2008, TMH.
4. Introduction to Signal and System Analysis – K.Gopalan 2009, Cengage Learning.
5. Fundamentals of Signals and Systems - Michel J. Robert, 2008, MGH International Edition.
6. Signals, Systems and Transforms - C. L. Philips, J.M.Parr and Eve A.Riskin, 3 Ed., 2004, PE.

Course Outcomes:

Upon completing this course the student will be able to:

- Represent any arbitrary signals in terms of complete sets of orthogonal functions and understands the principles of impulse functions, step function and signum function.
- Express periodic signals in terms of Fourier series and express the spectrum and express the arbitrary signal (discrete) as Fourier transform to draw the spectrum.
- Understands the principle of linear system, filter characteristics of a system and its bandwidth, the concepts of auto correlation and cross correlation and power Density Spectrum.
- Can design a system for sampling a signal.
- For a given system, response can be obtained using Laplace transform, properties and ROC of L.T.
- Study the continuous and discrete signal relation and relation between F.T., L.T. & Z.T, properties, ROC of Z Transform.

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II Year B.Tech. ECE-I Sem	**L**	**T/P/D**	**C**
	-	**-/3/-**	**2**

(A30481) BASIC SIMULATION LAB

Note:

- All the experiments are to be simulated using MATLAB or equivalent software
- Minimum of 15 experiment are to be completed

List of Experiments:

1. Basic Operations on Matrices.
2. Generation of Various Signals and Sequences (Periodic and Aperiodic), such as Unit Impulse, Unit Step, Square, Saw tooth, Triangular, Sinusoidal, Ramp, Sinc.
3. Operations on Signals and Sequences such as Addition, Multiplication, Scaling, Shifting, Folding, Computation of Energy and Average Power.
4. Finding the Even and Odd parts of Signal/Sequence and Real and Imaginary parts of Signal.
5. Convolution between Signals and sequences.
6. Auto Correlation and Cross Correlation between Signals and Sequences.
7. Verification of Linearity and Time Invariance Properties of a given Continuous/Discrete System.
8. Computation of Unit sample, Unit step and Sinusoidal responses of the given LTI system and verifying its physical realiazability and stability properties.
9. Gibbs Phenomenon
10. Finding the Fourier Transform of a given signal and plotting its magnitude and phase spectrum.
11. Waveform Synthesis using Laplace Transform.
12. Locating the Zeros and Poles and plotting the Pole-Zero maps in S-plane and Z-Plane for the given transfer function.
13. Generation of Gaussian noise (Real and Complex), Computation of its mean, M.S. Value and its Skew, Kurtosis, and PSD, Probability Distribution Function.
14. Sampling Theorem Verification.
15. Removal of noise by Autocorrelation / Cross correlation.
16. Extraction of Periodic Signal masked by noise using Correlation.
17. Verification of Weiner-Khinchine Relations.
18. Checking a Random Process for Stationarity in Wide sense.

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II YEAR II SEMESTER

Code	Subject	L	T/P/D	C
A40215	Principles of Electrical Engineering	4	-	4
A40412	Electronic Circuit Analysis	4	-	4
A40415	Pulse and Digital Circuits	4	-	4
A40009	Environmental Studies	4	—	4
A40411	Electromagnetic Theory and Transmission Lines	4	-	4
A40410	Digital Design using Verilog HDL	4	-	4
A40288	Electrical Technology Lab.	-	3	2
A40484	Electronic Circuits and Pulse Circuits Lab.	-	3	2
	Total	**24**	**6**	**28**

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II Year B.Tech. ECE-II Sem	L	T/P/D	C
	4	-/-/-	4

(A40412) ELECTRONIC CIRCUIT ANALYSIS

Course Objective:

- To familiarize the student with the analysis and design of basic transistor amplifier circuits and their frequency response characteristics, feedback amplifiers, oscillators, large signal amplifiers and tuned amplifiers

UNIT -I:

Single Stage and Multi Stage Amplifiers

Single Stage Amplifiers: Classification of Amplifiers – Distortion in Amplifiers, Analysis of CE, CC, and CB Configurations with simplified Hybrid Model, Analysis of CE amplifier with Emitter Resistance and Emitter follower, Miller's Theorem and its dual, Design of Single Stage RC Coupled Amplifier using BJT.

Multi Stage Amplifiers: Analysis of Cascaded RC Coupled BJT amplifiers, Cascode Amplifier, Darlington Pair, Different Coupling Schemes used in Amplifiers - RC Coupled Amplifier, Transformer Coupled Amplifier, Direct Coupled Amplifier.

UNIT –II:

BJT Amplifiers and MOS Amplifiers

BJT Amplifiers - Frequency Response: Logarithms, Decibels, General frequency considerations, Frequency response of BJT Amplifier, Analysis at Low and High frequencies, Effect of coupling and bypass Capacitors, The Hybrid- pi (p) - Common Emitter Transistor Model, CE Short Circuit Current Gain, Current Gain with Resistive Load, Single Stage CE Transistor Amplifier Response, Gain-Bandwidth Product, Emitter follower at higher frequencies.

MOS Amplifiers [3]: Basic concepts, MOS Small signal model, Common source amplifier with Resistive load.

UNIT –III:

Feedback Amplifiers and Oscillators

Feedback Amplifiers: Concepts of Feedback, Classification of Feedback Amplifiers, General characteristics of Negative Feedback Amplifiers, Effect of Feedback on Amplifier Characteristics, Voltage Series, Voltage Shunt, Current Series and Current Shunt Feedback Configurations, Illustrative Problems.

Oscillators: Classification of Oscillators, Conditions for Oscillations, RC Phase Shift Oscillator, Generalized analysis of LC oscillators - Hartley, and

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Colpitts Oscillators, Wien-Bridge & Crystal Oscillators, Stability of Oscillators.

UNIT –IV:

Large Signal Amplifiers : Classification, Class A Large Signal Amplifiers, Transformer Coupled Class A Audio Power Amplifier, Efficiency of Class A Amplifier, Class B Amplifier, Efficiency of Class B Amplifier, Class-B Push-Pull Amplifier, Complementary Symmetry Class B Push-Pull Amplifier, Distortion in Power Amplifiers, Thermal Stability and Heat Sinks.

UNIT –V:

Tuned Amplifiers: Introduction, Q-Factor, Small Signal Tuned Amplifiers, Effect of Cascading Single Tuned Amplifiers on Bandwidth, Effect of Cascading Double Tuned Amplifiers on Bandwidth, Stagger Tuned Amplifiers, Stability of Tuned Amplifiers.

TEXT BOOKS:

1. Integrated Electronics - Jacob Millman and Christos C Halkias, 1991 Ed., 2008, TMH.
2. Electronic Devices and Circuits, B. P. Singh, Rekha Singh, Pearson, 2013.
3. Design of Analog CMOS Integrated Circuits – Behzad Razavi, 2008, TMH.

REFERENCE BOOKS:

1. ElectronicCircuit Analysis – Rashid, Cengage Learning, 2013
2. Electronic Devices and Circuit Theory - Robert L.Boylestad, Louis Nashelsky, 9 Ed., 2008 PE.
3. Microelectric Circuits – Sedra and Smith – 5 Ed., 2009, Oxford University Press.
4. Electronic Circuit Analysis – K. Lal Kishore, 2004, BSP.
5. Electronic Devices and Circuits - S. Salivahanan, N.Suresh Kumar, A Vallavaraj, 2 Ed., 2009, TMH.

Course Outcomes:

Upon completion of the subject, students will be able to:

- Design and analyse the DC bias circuitry of BJT and FET.
- Analyse the different types of amplifiers, operation and its characteristics
- Design circuits like amplifiers, oscillators using the transistors diodes and oscillators.

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II Year B.Tech. ECE-II Sem	L	T/P/D	C
	4	-/-/-	4

(A40415) PULSE AND DIGITAL CIRCUITS

Objectives:

The main objectives are:

- To explain the complete response of R-C and R-L-C transient circuits.
- To explain clippers, clampers, switching characteristics of transistors and sampling gates.
- To construct various multivibrators using transistors, design of sweep circuits and sampling gates.
- To discuss and realize logic gates using diodes and transistors.

UNIT-I:

Linear Wave Shaping: High pass and low pass RC circuits and their response for Sinusoidal, Step, Pulse, Square, & Ramp inputs, High pass RC network as Differentiator, Low pass RC circuit as an Integrator, Attenuators and its application as a CRO Probe, RL and RLC Circuits and their response for Step Input, Ringing Circuit.

UNIT-II:

Non-Linear Wave Shaping: Diode clippers, Transistor clippers, Clipping at two independent levels, Comparators, Applications of Voltage comparators. Clamping Operation, Clamping circuit taking Source and Diode resistances into account, Clamping Circuit Theorem, Practical Clamping Circuits, Effect of Diode Characteristics on Clamping Voltage, Synchronized Clamping.

UNIT-III:

Switching Characteristics of Devices : Diode as a Switch, Piecewise Linear Diode Characteristics, Diode Switching times, Transistor as a Switch, Break down voltages, Transistor in Saturation, Temperature variation of Saturation Parameters, Transistor-switching times, Silicon-controlled-switch circuits, Sampling Gates : Basic operating principles of Sampling Gates, Unidirectional and Bi-directional Sampling Gates, Four Diode Sampling Gate, Reduction of pedestal in Gate Circuits

UNIT–IV:

Multivibrators: Analysis and Design of Bistable, Monostable, Astable Multivibrators and Schmitt trigger using Transistors, Time Base Generators: General features of a Time base Signal, Methods of Generating Time Base Waveform, Miller and Bootstrap

Time Base Generators-Basic Principles, Transistor Miller Time Base generator, Transistor Bootstrap Time Base Generator, Transistor Current Time Base Generators, Methods of Linearity improvement.

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UNIT-V:

Synchronization and Frequency Division: Pulse Synchronization of Relaxation Devices, Frequency division in Sweep Circuit, Stability of Relaxation Devices, Astable Relaxation Circuits, Monostable Relaxation Circuits, Synchronization of a Sweep Circuit with Symmetrical Signals, Sine wave frequency division with a Sweep Circuit, A Sinusoidal Divider using Regeneration and Modulation.

Realization of Logic Gates Using Diodes & Transistors: AND, OR and NOT Gates using Diodes and Transistors, DCTL, RTL, DTL, TTL and CML Logic Families and its Comparison.

TEXT BOOKS:

1. Millman's Pulse, Digital and Switching Waveforms –J. Millman, H. Taub and Mothiki S. Prakash Rao, 2 Ed., 2008, TMH.
2. Solid State Pulse Circuits –David A. Bell, 4 Ed., 2002 PHI.

REFERENCE BOOKS:

1. Pulse and Digital Circuits – A. Anand Kumar, 2005, PHI.
2. Fundamentals of Pulse and Digital Circuits- Ronald J. Tocci, 3 Ed., 2008.
3. Pulse and Digital Circuits – Motheki S. Prakash Rao, 2006, TMH.
4. Wave Generation and Shaping - L. Strauss.

Outcomes:

At the end of the course, the student will be able to:

- Understand the applications of diode as integrator, differentiator, clippers, clampler circuits..
- Learn various switching devices such as diode, transistor, SCR.
- Difference between logic gates and sampling gates
- Design mutivibrators for various applications, synchronization techniques and sweep circuits.
- Realizing logic gates using diodes and transistors.

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(A40484) ELECTRONIC CIRCUITS AND PULSE CIRCUITS LAB

List of Experiments (16 experiments to be done):

PART –I: ELCTRONIC CIRCUITS

Minimum eight experiments to be conducted:

I) Design and Simulation in Simulation Laboratory using any Simulation Software (Minimum 6 Experiments):

1. Common Emitter Amplifier
2. Common Source Amplifier
3. Two Stage RC Coupled Amplifier
4. Current shunt and Voltage Series Feedback Amplifier
5. Cascode Amplifier
6. Wien Bridge Oscillator using Transistors
7. RC Phase Shift Oscillator using Transistors
8. Class A Power Amplifier (Transformer less)
9. Class B Complementary Symmetry Amplifier
10. Common Base (BJT) / Common Gate (JFET) Amplifier.

II) Testing in the Hardware Laboratory (Minimum 2 Experiments)

1. Class A Power Amplifier (with transformer load)
2. Class C Power Amplifier
3. Single Tuned Voltage Amplifier
4. Hartley & Colpitt's Oscillators
5. Darlington Pair
6. MOS Common Source Amplifier

Equipment required for the Laboratory:

1. For software simulation of Electronic circuits
   - i) Computer Systems with latest specifications
   - ii) Connected in LAN (Optional)
   - iii) Operating system (Windows XP)
   - iv) Suitable Simulations software
2. For Hardware simulations of Electronic Circuits
   - i) Regulated Power Supply (0-30V)
   - ii) CRO's

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- iii) Functions Generators
- iv) Multimeters
- v) Components

3. Win XP/ Linux etc.

PART –II: PULSE CIRCUITS

Minimum eight experiments to be conducted:

1. Linear Wave Shaping
   - a. RC Low Pass Circuit for different time constants
   - b RC High Pass Circuit for different time constants
2. Non-linear wave shaping

a. Transfer characteristics and response of Clippers:
   - i) Positive and Negative Clippers
   - ii) Clipping at two independent levels

b The steady state output waveform of clampers for a square wave input
   - i) Positive and Negative Clampers
   - ii) Clamping at reference voltage

3. Comparison Operation of Comparators
4. Switching characteristics of a transistor
5 Design a Bistable Multivibrator and draw its waveforms
6 Design an Astable Multivibrator and draw its waveforms
7. Design a Monostable Multivibrator and draw its waveforms
8. Response of Schmitt Trigger circuit for loop gain less than and greater than one
9. UJT relaxation oscillator
10. The output- voltage waveform of Boot strap sweep circuit
11. The output- voltage waveform of Miller sweep circuit

Equipment required for Laboratories:

Regulated Power Supply	- 0 – 30 V
CRO	- 0 – 20 M Hz.
Function Generators	- 0 – 1 M Hz
Components	
Multi Meters	

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(A40288) ELECTRICAL TECHNOLOGY LAB

PART –A:

1. Verification of KVL and KCL.
2. Serial and Parallel Resonance.
3. Time response of first order RC/RL network for periodic non-sinusoidal inputs – time constant and steady state error determination.
4. Two port network parameters – Z-Y Parameters, chain matrix and analytical verification.
5. Two port network parameters – ABCD and h- Parameters
6. Verification of Superposition and Reciprocity theorems.
7. Verification of maximum power transfer theorem.
8. Verification of Thevenin's and Norton's theorems.

PART –B:

1. Magnetization characteristics of D.C. Shunt generator.
2. Swinburne's Test on DC shunt machine.
3. Brake test on DC shunt motor.
4. OC & SC tests on Single-phase transformer.
5. Load Test on Single Phase Transformer.

Note: Any 12 of the above experiments are to be conducted.

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(A40215) PRINCIPLES OF ELECTRICAL ENGINEERING

Objectives:

This course introduces the basic concepts of transient analysis of the circuits, the basic two-port network parameters and the design analysis of filters and attenuators and their use in circuit theory. The emphasis of this course is laid on the basic operation of the DC machines and transformers which includes DC generators and motors, single-phase transformers.

UNIT –I:

Transient Analysis (First and Second Order Circuits): Transient Response of RL , RC Series, RLC Circuits for DC excitations, Initial Conditions, Solution using Differential Equations approach and Laplace Transform Method.

UNIT –II:

Two Port Networks: Impedance Parameters, Admittance Parameters, Hybrid Parameters, Transmission (ABCD) Parameters, Conversion of one Parameter to another, Conditions for Reciprocity and Symmetry, Interconnection of Two Port networks in Series, Parallel and Cascaded configurations, Image Parameters, Illustrative problems.

UNIT –III:

Filters and Symmetrical Attenuators: Classification of Filters, Filter Networks, Classification of Pass band and Stop band, Characteristic Impedance in the Pass and Stop Bands, Constant-k Low Pass Filter, High Pass Filter, m-derived T-Section, Band Pass filter and Band Elimination filter, Illustrative Problems. Symmetrical Attenuators – T-Type Attenuator, p–Type Attenuator, Bridged T type Attenuator, Lattice Attenuator.

UNIT –IV:

DC Machines: Principle of Operation of DC Machines, EMF equation, Types of Generators, Magnetization and Load Characteristics of DC Generators. DC Motors, Types of DC Motors, Characteristics of DC Motors, Losses and Efficiency, Swinburne's Test, Speed Control of DC Shunt Motor, Flux and Armature Voltage control methods.

UNIT –V:

Transformers and Their Performance: Principle of Operation of Single Phase transformer, Types, Constructional Features, Phasor Diagram on No Load and Load, Equivalent Circuit, Losses and Efficiency of Transformer and Regulation, OC and SC Tests (Simple Problems). Synchros, Stepper Motors.

TEXT BOOKS:

1. Electric Circuits - A. Chakrabarhty, Dhanipat Rai & Sons.
2. Basic concepts of Electrical Engineering - PS Subramanyam, BS Publications

REFERENCE BOOKS:

1. Engineering circuit analysis - William Hayt and Jack E. Kemmerly, Mc Graw Hill Company, 7th Edition.
2. Basic Electrical Engineering - S.N. Singh, PHI.
3. Electrical Circuits - David A.Bell, Oxford University Press.
4. Electric Circuit Analysis - K.S.Suresh Kumar, Pearson Education.

Outcome:

After going through this course the student gets a thorough knowledge on transient analysis of circuits, filters, attenuators , the operation of DC machines and transformers, with which he/she can able to apply the above conceptual things to real-world problems and applications.

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III YEAR I SEMESTER

Code	Subject	L	T/P/D	C
A50217	Control Systems Engineering	4	-	4
A50516	Computer Organization and Operating Systems	4	-	4
A50418	Antennas and Wave Propagation	4	-	4
A50422	Electronic Measurements and Instrumentation	4	-	4
A50408	Analog Communications	4	-	4
A50425	Linear and Digital IC Applications	4	-	4
A50482	Analog Communications Lab	-	3	2
A50488	IC Applications and HDL Simulation Lab	-	3	2
	Total	24	6	28

III YEAR II SEMESTER

Code	Subject	L	T/P/D	C
A60010	Managerial Economics and Financial Analysis	4	-	4
A600 18 A601 17 A600 17	**Open Elective:** Human Values and Professional Ethics Disaster Management Intellectual Property Rights	4	-	4
A60420	Digital Communications	4	-	4
A60432	VLSI Design	4	-	4
A60430	Microprocessors and Microcontrollers	4	-	4
A60421	Digital Signal Processing	4	-	4
A60494	Microprocessors and Microcontrollers Lab	-	3	2
A60493	Digital Signal Processing Lab	-	3	2
	Total	24	6	28

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components and its Properties, Modeling of Noise Sources, Average Noise Bandwidth, Effective Noise Temperature, Average Noise Figures, Average Noise Figure of cascaded networks.

Noise in DSB and SSB System Noise in AM System, Noise in Angle Modulation System, Noise Triangle in Angle Modulation System, Pre-emphasis and de-emphasis

UNIT –V:

Receivers: Radio Receiver - Receiver Types - Tuned radio frequency receiver, Superhetrodyne receiver, RF section and Characteristics - Frequency changing and tracking, Intermediate frequency, AGC, FM Receiver, Comparison with AM Receiver, Amplitude limiting.

Pulse Modulation: Types of Pulse modulation, PAM (Single polarity, double polarity) PWM: Generation and demodulation of PWM, PPM, Generation and demodulation of PPM, Time Divison Multiplexing.

TEXTBOOKS:

1. Communication Systems–Simon Haykin, 2 Ed, Wiley Publications.
2. Communication Systems – B.P. Lathi, BS Publication , 2004.

REFERENCE BOOKS:

1. Electronic Communications – Dennis Roddy and John Coolean, 4th Edition,PEA, 2004
2. Electronic Communication Systems – Modulation and Transmission - Robert J. Schoenbeck, 2nd Edition, PHI.
3. Analog and Digital Communication – K. Sam Shanmugam, Willey ,2005
4. Electronics & Communication System – George Kennedy and Bernard Davis , TMH 2004.
5. Principles of Communication Systems – H Taub & D. Schilling, Gautam Sahe, TMH, 2007 , 3rd Edition

Course Outcomes:

Upon completion of the subject, students will be able to:

- Conceptually understand the baseband signal & system.
- Identify various elements, processes, and parameters in telecommunication systems, and describe their functions, effects, and interrelationship.
- Design procedure of AM Transmission & Reception, analyze, measure, and evaluate the performance of a telecommunication system against given criteria.
- Understand basic knowledge of FM Transmission & Reception
- Understand various types of SSB Transmission & Reception.
- Design typical telecommunication systems that consist of basic and essential building blocks.

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(A50425) LINEAR AND DIGITAL IC APPLICATIONS

Course Objectives:

The main objectives of the course are:

- To introduce the basic building blocks of linear integrated circuits.
- To teach the linear and non - linear applications of operational amplifiers.
- To introduce the theory and applications of analog multipliers and PLL.
- To teach the theory of ADC and DAC.
- To introduce the concepts of waveform generation and introduce some special function ICs.
- To understand and implement the working of basic digital circuits.

UNIT -I:

Operational Amplifier: Ideal and Practical Op-Amp, Op-Amp Characteristics, DC and AC Characteristics, Features of 741 Op-Amp, Modes of Operation - Inverting, Non-Inverting, Differential, Instrumentation Amplifier, AC Amplifier, Differentiators and Integrators, Comparators, Schmitt Trigger, Introduction to Voltage Regulators, Features of 723 Regulator, Three Terminal Voltage Regulators.

UNIT -II:

Op-Amp, IC-555 & IC 565 Applications: Introduction to Active Filters, Characteristics of Band pass, Band reject and All Pass Filters, Analysis of 1st order LPF & HPF Butterworth Filters, Waveform Generators – Triangular, Sawtooth, Square Wave, IC555 Timer - Functional Diagram, Monostable and Astable Operations, Applications, IC565 PLL - Block Schematic, Description of Individual Blocks, Applications.

UNIT -III:

Data Converters : Introduction, Basic DAC techniques, Different types of DACs-Weighted resistor DAC, R-2R ladder DAC, Inverted R-2R DAC, Different Types of ADCs - Parallel Comparator Type ADC, Counter Type ADC, Successive Approximation ADC and Dual Slope ADC, DAC and ADC Specifications.

UNIT -IV:

Digital Integrated Circuits: Classification of Integrated Circuits, Comparison of Various Logic Families, CMOS Transmission Gate, IC interfacing- TTL

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(A50487) ANALOG COMMUNICATIONS LAB

Note:

Minimum 12 experiments should be conducted:

All these experiments are to be simulated first either using MATLAB, Comsim or any other simulation package and then to be realized in hardware

1. Amplitude modulation and demodulation.
2. DSB-SC Modulator & Detector
3. SSB-SC Modulator & Detector (Phase Shift Method)
4. Frequency modulation and demodulation.
5. Study of spectrum analyzer and analysis of AM and FM Signals
6. Pre-emphasis & de-emphasis.
7. Time Division Multiplexing & De multiplexing
8. Frequency Division Multiplexing & De multiplexing
9. Verification of Sampling Theorem
10. Pulse Amplitude Modulation & Demodulation
11. Pulse Width Modulation & Demodulation
12. Pulse Position Modulation & Demodulation
13. Frequency Synthesizer.
14. AGC Characteristics.
15. PLL as FM Demodulator

Equipment required for the Laboratory:

1. RPS - 0 – 30 V
2. CRO - 0 – 20 M Hz.
3. Function Generators - 0 – 1 M Hz
4. RF Generators - 0 – 1000 M Hz./0 – 100 M Hz.
5. Multimeters
6. Lab Experimental kits for Analog Communication
7. Components
8. Radio Receiver/TV Receiver Demo kits or Trainees.
9. Spectrum Analyzer - 60 M Hz.
10. Any one simulation package

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Driving CMOS & CMOS Driving TTL, Combinational Logic ICs – Specifications and Applications of TTL-74XX & CMOS 40XX Series ICs - Code Converters, Decoders, Demultiplexers, LED & LCD Decoders with Drivers , Encoders, Priority Encoders, Multiplexers, Demultiplexers, Priority Generators/Checkers, Parallel Binary Adder/Subtractor, Magnitude Comparators.

UNIT -V:

Sequential Logic IC's and Memories: Familiarity with commonly available 74XX & CMOS 40XX Series ICs – All Types of Flip-flops, Synchronous Counters, Decade Counters, Shift Registers.

Memories - ROM Architecture, Types of ROMS & Applications, RAM Architecture, Static & Dynamic RAMs.

TEXT BOOKS:

1. Op-Amps & Linear ICs – Ramakanth A. Gayakwad, PHI, 2003.
2. Linear Integrated Circuits –D. Roy Chowdhury, New Age International (p) Ltd, 2nd Ed., 2003.
3. Digital Fundamentals – Floyd and Jain, Pearson Education, 8th Edition, 2005.

REFERENCE BOOKS:

1. Op Amps and Linear Integrated Circuits-Concepts and Applications James M. Fiore, Cengage Learning/ Jaico, 2009.
2. Operational Amplifiers with Linear Integrated Circuits by K.Lal Kishore – Pearson, 2009.
3. Linear Integrated Circuits and Applications – Salivahana, TMH.
4. Modern Digital Electronics – RP Jain – 4/e – TMH, 2010.
5. Digital Design Principles and Practices – John. F. Wakerly 3/e, 2005.
6. Operational Amplifiers with Linear Integrated Circuits, 4/e William D.Stanley, Pearson Education India, 2009.

Course Outcomes:

On completion of this course, the students will have:

- A thorough understanding of operational amplifiers with linear integrated circuits.
- Understanding of the different families of digital integrated circuits and their characteristics.
- Also students will be able to design circuits using operational amplifiers for various applications.

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(A50487) ANALOG COMMUNICATIONS LAB

Note:

Minimum 12 experiments should be conducted:

All these experiments are to be simulated first either using MATLAB, Comsim or any other simulation package and then to be realized in hardware

1. Amplitude modulation and demodulation.
2. DSB-SC Modulator & Detector
3. SSB-SC Modulator & Detector (Phase Shift Method)
4. Frequency modulation and demodulation.
5. Study of spectrum analyzer and analysis of AM and FM Signals
6. Pre-emphasis & de-emphasis.
7. Time Division Multiplexing & De multiplexing
8. Frequency Division Multiplexing & De multiplexing
9. Verification of Sampling Theorem
10. Pulse Amplitude Modulation & Demodulation
11. Pulse Width Modulation & Demodulation
12. Pulse Position Modulation & Demodulation
13. Frequency Synthesizer.
14. AGC Characteristics.
15. PLL as FM Demodulator

Equipment required for the Laboratory:

1. RPS - 0 – 30 V
2. CRO - 0 – 20 M Hz.
3. Function Generators - 0 – 1 M Hz
4. RF Generators - 0 – 1000 M Hz./0 – 100 M Hz.
5. Multimeters
6. Lab Experimental kits for Analog Communication
7. Components
8. Radio Receiver/TV Receiver Demo kits or Trainees.
9. Spectrum Analyzer - 60 M Hz.
10. Any one simulation package

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(A50488) IC APPLICATIONS AND HDL SIMULATION LAB

Note: To perform any sixteen experiments (choosing at least seven from each part).

Part-I: Linear IC Experiments

1. OP AMP Applications – Adder, Subtractor, Comparators.
2. Integrator and Differentiator Circuits using IC 741.
3. Active Filter Applications – LPF, HPF (first order)
4. IC 741 Waveform Generators – Sine, Squarewave and Triangular waves.
5. IC 555 Timer – Monostable and Astable Multivibrator Circuits.
6. Schmitt Trigger Circuits – using IC 741
7. IC 565 – PLL Applications.
8. Voltage Regulator using IC 723, Three Terminal Voltage Regulators –7805, 7809, 7912.

EQUIPMENT REQUIRED:

1 20 MHz/ 40 MHz/60 MHz Oscilloscope.

2 1 MHz Function Generator (Sine, Square, Triangular and TTL).

3 Regulated Power Supply.

4 Multimeter / Volt Meter.

Part – II: HDL Simulation programs:

Programming can be done using any complier. Down load the programs on FPGA/CPLD boards and performance testing may be done using pattern generator/logic analyzer apart from verification by simulation using Cadence / Mentor Graphics / Synopsys /Equivalentfront end CAD tools.

1 HDL code to realize all the logic gates

2 Design of 2-to-4 decoder

3 Design of 8-to-3 encoder (without and with Priority)

4 Design of 8-to-1 multiplexer and 1x8 demultiplexer.

5 Design of 4 bit binary to gray code converter

6 Design of 4 bit comparator

7 Design of Full adder using 3 modeling styles

8 Design of flip flops: SR, D, JK, T

9 Design of 4-bit binary, BCD counters (synchronous/ asynchronous reset)

10 Finite State Machine Design

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2. Kenneth. J. Ayala, The 8051 Microcontroller , 3rd Ed., Cengage Learning.

REFERENCE BOOKS:

1. Advanced Microprocessors and Peripherals – A. K. Ray and K.M. Bhurchandani, TMH, 2nd Edition 2006.
2. The 8051Microcontrollers, Architecture and Programming and Applications -K.Uma Rao, Andhe Pallavi, Pearson, 2009.
3. Micro Computer System 8086/8088 Family Architecture, Programming and Design - Liu and GA Gibson, PHI, 2nd Ed.
4. Microcontrollers and Application - Ajay. V. Deshmukh, TMGH, 2005.
5. The 8085 Microprocessor: Architecture, programming and Interfacing – K.Uday Kumar, B.S.Umashankar, 2008, Pearson

Course Outcome:

Upon completion of the course:

- The student will learn the internal organization of popular 8086/8051 microprocessors/microcontrollers.
- The student will learn hardware and software interaction and integration.
- The students will learn the design of microprocessors/ microcontrollers-based systems.

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(A60421) DIGITAL SIGNAL PROCESSING

Objectives:

This course is an essential course that provides design techniques for processing all type of signals in various fields. The main objectives are:

- To provide background and fundamental material for the analysis and processing of digital signals.
- To familiarize the relationships between continuous-time and discrete-time signals and systems.
- To study fundamentals of time, frequency and Z-plane analysis and to discuss the inter-relationships of these analytic method.
- To study the designs and structures of digital (IIR and FIR) filters from analysis to synthesis for a given specifications.
- The impetus is to introduce a few real-world signal processing applications.
- To acquaint in FFT algorithms, Multi-rate signal processing techniques and finite word length effects.

UNIT -I:

Introduction: Introduction to Digital Signal Processing: Discrete Time Signals & Sequences, Linear Shift Invariant Systems, Stability, and Causality, Linear Constant Coefficient Difference Equations, Frequency Domain Representation of Discrete Time Signals and Systems

Realization of Digital Filters: Applications of Z – Transforms, Solution of Difference Equations of Digital Filters, System Function, Stability Criterion, Frequency Response of Stable Systems, Realization of Digital Filters – Direct, Canonic, Cascade and Parallel Forms.

UNIT -II:

Discrete Fourier series: DFS Representation of Periodic Sequences, Properties of Discrete Fourier Series, Discrete Fourier Transforms: Properties of DFT, Linear Convolution of Sequences using DFT, Computation of DFT: Over-Lap Add Method, Over-Lap Save Method, Relation between DTFT, DFS, DFT and Z-Transform.

Fast Fourier Transforms: Fast Fourier Transforms (FFT) - Radix-2 Decimation-in-Time and Decimation-in-Frequency FFT Algorithms, Inverse FFT, and FFT with General Radix-N.

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(A60494) MICROPROCESSORS AND MICROCONTROLLERS LAB

Note: Minimum of 12 experiments are to be conducted.

The Following programs/experiments are to be written for assembler and to be executed the same with 8086 and 8051 kits.

List of Experiments:

1. Programs for 16 bit arithmetic operations for 8086 (using Various Addressing Modes).
2. Program for sorting an array for 8086.
3. Program for searching for a number or character in a string for 8086.
4. Program for string manipulations for 8086.
5. Program for digital clock design using 8086.
6. Interfacing ADC and DAC to 8086.
7. Parallel communication between two microprocessors using 8255.
8. Serial communication between two microprocessor kits using 8251.
9. Interfacing to 8086 and programming to control stepper motor.
10. Programming using arithmetic, logical and bit manipulation instructions of 8051.
11. Program and verify Timer/ Counter in 8051.
12. Program and verify Interrupt handling in 8051
13. UART Operation in 8051.
14. Communication between 8051 kit and PC.
15. Interfacing LCD to 8051.
16. Interfacing Matrix/ Keyboard to 8051.
17. Data Transfer from Peripheral to Memory through DMA controller 8237 / 8257.

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UNIT-III:

IIR Digital Filters: Analog filter approximations – Butterworth and Chebyshev, Design of IIR Digital Filters from Analog Filters, Step and Impulse Invariant Techniques, Bilinear Transformation Method, Spectral Transformations.

UNIT-IV:

FIR Digital Filters: Characteristics of FIR Digital Filters, Frequency Response, Design of FIR Filters: Fourier Method, Digital Filters using Window Techniques, Frequency Sampling Technique, Comparison of IIR & FIR filters.

UNIT-V:

Multirate Digital Signal Processing: Introduction, Down Sampling, Decimation, Upsampling, Interpolation, Sampling Rate Conversion.

Finite Word Length Effects: Limit cycles, Overflow Oscillations, Round-off Noise in IIR Digital Filters, Computational Output Round Off Noise, Methods to Prevent Overflow, Trade Off Between Round Off and Overflow Noise, Dead Band Effects.

TEXT BOOKS:

1. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G. Manolakis, Pearson Education / PHI, 2007.
2. Discrete Time Signal Processing – A. V. Oppenheim and R.W. Schaffer, PHI, 2009
3. Fundamentals of Digital Signal Processing – Loney Ludeman, John Wiley, 2009

REFERENCE BOOKS:

1. Digital Signal Processing – Fundamentals and Applications – Li Tan, Elsevier, 2008
2. Fundamentals of Digital Signal Processing using MATLAB – Robert J. Schilling, Sandra L. Harris, Thomson, 2007
3. Digital Signal Processing – S.Salivahanan, A.Vallavaraj and C.Gnanapriya, TMH, 2009
4. Discrete Systems and Digital Signal Processing with MATLAB – Taan S. ElAli, CRC press, 2009.
5. Digital Signal Processing - A Practical approach, Emmanuel C. Ifeachor and Barrie W. Jervis, 2nd Edition, Pearson Education, 2009.
6. Digital Signal Processing - Nagoor Khani, TMG, 2012

Course Outcomes:

On completion of this subject, the student should be able to:

- Perform time, frequency and Z -transform analysis on signals and systems.

- Understand the inter-relationship between DFT and various transforms.
- Understand the significance of various filter structures and effects of roundoff errors.
- Design a digital filter for a given specification.
- Understand the fast computation of DFT and appreciate the FFT processing.
- Understand the tradeoffs between normal and multi rate DSP techniques and finite length word effects.

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III Year B.Tech. ECE-II Sem	L	T/P/D	C
	-	-/3/-	2

(A60494) MICROPROCESSORS AND MICROCONTROLLERS LAB

Note: Minimum of 12 experiments are to be conducted.

The Following programs/experiments are to be written for assembler and to be executed the same with 8086 and 8051 kits.

List of Experiments:

1 Programs for 16 bit arithmetic operations for 8086 (using Various Addressing Modes).
2 Program for sorting an array for 8086.
3 Program for searching for a number or character in a string for 8086.
4 Program for string manipulations for 8086.
5 Program for digital clock design using 8086.
6 Interfacing ADC and DAC to 8086.
7 Parallel communication between two microprocessors using 8255.
8 Serial communication between two microprocessor kits using 8251.
9 Interfacing to 8086 and programming to control stepper motor.
10 Programming using arithmetic, logical and bit manipulation instructions of 8051.
11 Program and verify Timer/ Counter in 8051.
12 Program and verify Interrupt handling in 8051
13 UART Operation in 8051.
14 Communication between 8051 kit and PC.
15 Interfacing LCD to 8051.
16 Interfacing Matrix/ Keyboard to 8051.
17 Data Transfer from Peripheral to Memory through DMA controller 8237 / 8257.

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III Year B.Tech. ECE-II Sem	**L**	**T/P/D**	**C**
	-	**-/3/-**	**2**

(A60493) DIGITAL SIGNAL PROCESSING LAB

Note:

- Minimum of 12 experiments are to be conducted.
- The programs shall be implemented in software (Using MATLAB / Lab view / C programming/OCTAVE Equivalent) and hardware (Using TI / Analog devices / Motorola / Equivalent DSP processors).

List of Experiments:

1 Generation of Sinusoidal waveform / signal based on recursive difference equations
2 To find DFT / IDFT of given DT signal
3 To find frequency response of a given system given in (Transfer Function/ Differential equation form).
4 Implementation of FFT of given sequence
5 Determination of Power Spectrum of a given signal(s).
6 Implementation of LP FIR filter for a given sequence
7 Implementation of HP FIR filter for a given sequence
8 Implementation of LP IIR filter for a given sequence
9 Implementation of HP IIR filter for a given sequence
10 Generation of Sinusoidal signal through filtering
11 Generation of DTMF signals
12 Implementation of Decimation Process
13 Implementation of Interpolation Process
14 Implementation of I/D sampling rate converters
15 Audio application such as to plot a time and frequency display of microphone plus a cosine using DSP. Read a .wav file and match with their respective spectrograms.
16 Noise removal: Add noise above 3 KHz and then remove, interference suppression using 400 Hz tone.
17 Impulse response of first order and second order systems.

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IVYEAR I SEMESTER

Code	Subject	L	T/P/D	C
A70014	Management Science	4	-	4
A70442	Microwave Engineering	4	-	4
A70515	Computer Networks	4	-	4
A70434	Cellular and Mobile Communications	4	-	4
	Elective -I:	4	-	4
A70436	Digital Image Processing			
A70443	Multimedia and Signal Coding			
A70505	Object Oriented Programming through Java			
	Elective -II:	4	-	4
A70447	Television Engineering			
A70444	Optical Communications			
A70440	Embedded Systems Design			
A70086	Advanced Communication Skills Lab	-	3	2
A70499	Microwave Engineering and Digital Communications Lab	-	3	2
	Total	24	6	28

IV YEAR II SEMESTER

Code	Subject	L	T/P/D	C
	Elective -III:	4	-	4
A80452	Satellite Communications			
A81102	Biomédical Instrumentation			
A80527	Artificial Neural Networks			
	Elective -IV:	4	-	4
A80431	Telecommunication Switching Systems and Networks			
A80450	Radar Systems			
A80449	Network Security			
	Elective -V:	4	-	4
A80454	Wireless Communications and Networks			
A80437	Digital Signal Processors and Architectures			
A80451	RF Circuit Design			
A80087	Industry Oriented Mini Project	-	-	2
A80089	Seminar	-	6	2
A80088	Major Project Work	-	15	10
A80090	Comprehensive Viva	-	-	2
	Total	12	21	28

Note: All End Examinations (Theory and Practical) are of three hours duration.
T-Tutorial L – Theory P – Practical D-Drawing C – Credits

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IV Year B.Tech. ECE-I Sem	L	T/P/D	C
	4	-/-/-	4

(A70442) MICROWAVE ENGINEERING

Course Objectives:

The objectives of the course are:

- To develop the knowledge on transmission lines for microwaves, cavity resonators and wave guide components and applications.
- To enable the students understand and analyze the operation of Microwave tubes like klystron, magnetron, travelling wave tube, etc.,
- To familiarize with microwave solid state devices.
- To understand the scattering matrix parameters and its use.
- To introduce the student the microwave test bench for measure different parameters like attenuation, VSWR, etc.,

UNIT-I:

Microwave Transmission Lines - I: Introduction, Microwave Spectrum and Bands, Applications of Microwaves. Rectangular Waveguides – Solution of Wave Equations in Rectangular Coordinates, TE/TM mode analysis, Expressions for Fields, Characteristic Equation and Cut-off Frequencies, Filter Characteristics, Dominant and Degenerate Modes, Sketches of TE and TM mode fields in the cross-section, Mode Characteristics – Phase and Group Velocities, Wavelengths and Impedance Relations, Illustrative Problems.

Rectangular Guides: Power Transmission and Power Losses, Impossibility of TEM Mode, Micro strip Lines– Introduction, Zo Relations, Effective Dielectric Constant, Losses, Q factor.

UNIT-II:

Cavity Resonators– Introduction, Rectangular Cavities, Dominant Modes and Resonant Frequencies, Q Factor and Coupling Coefficients, Illustrative Problems

Waveguide Components and Applications: Coupling Mechanisms – Probe, Loop, Aperture types. Waveguide Discontinuities – Waveguide Windows, Tuning Screws and Posts, Matched Loads. Waveguide Attenuators – Different Types, Resistive Card and Rotary Vane Attenuators; Waveguide Phase Shifters – Types, Dielectric and Rotary Vane Phase Shifters, Waveguide Multiport Junctions – E plane and H plane Tees, Magic Tee. Directional Couplers – 2 Hole, Bethe Hole types, Illustrative Problems

Ferrites– Composition and Characteristics, Faraday Rotation, Ferrite

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Components – Gyrator, Isolator, Circulator.

UNIT-III:

Microwave Tubes: Limitations and Losses of conventional Tubes at Microwave Frequencies, Microwave Tubes – O Type and M Type Classifications, O-type Tubes : 2 Cavity Klystrons – Structure, Reentrant Cavities, Velocity Modulation Process and Applegate Diagram, Bunching Process and Small Signal Theory – Expressions for O/P Power and Efficiency. Reflex Klystrons – Structure, Velocity Modulation and Applegate Diagram, Mathematical Theory of Bunching, Power Output, Efficiency, Oscillating Modes and O/P Characteristics, Effect of Repeller Voltage on Power O/P, Illustrative Problems.

Helix TTS: Significance, Types and Characteristics of Slow Wave Structures; Structure of TW T and Amplification Process (qualitative treatment), Suppression of Oscillations, Gain Considerations.

UNIT-IV:

M-Type Tubes:

Introduction, Cross-field Effects, Magnetrons – Different Types, Cylindrical Traveling Wave Magnetron – Hull Cut-off and Hartree Conditions, Modes of Resonance and PI-Mode Operation, Separation of PI-Mode, o/p characteristics, Illustrative Problems

Microwave Solid State Devices: Introduction, Classification, Applications. TEDs – Introduction, Gunn Diodes – Principle, RWH Theory, Characteristics, Basic Modes of Operation - Gunn Oscillation Modes, LSA Mode, Introduction to Avalanche Transit Time Devices.

UNIT-V:

Microwave Measurements: Scattering Matrix– Significance, Formulation and Properties, S Matrix Calculations for – 2 port Junctions, E plane and H plane Tees, Magic Tee, Circulator and Isolator, Illustrative Problems.

Description of Microwave Bench – Different Blocks and their Features, Errors and Precautions, Microwave Power Measurement, Bolometers Measurement of Attenuation, Frequency Standing Wave Measurements – Measurement of Low and High VSWR, Cavity Q, Impedance Measurements.

TEXT BOOKS:

1. Microwave Devices and Circuits – Samuel Y. Liao, Pearson, 3rd Edition, 2003.
2. Microwave Principles – Herbert J. Reich, J.G. Skalnik, P.F. Ordung and H.L. Krauss, CBS Publishers and Distributors, New Delhi, 2004.

REFERENCE BOOKS:

1. Foundations for Microwave Engineering – R.E. Collin, IEEE Press, John Wiley, 2ndEdition, 2002.

2. Microwave Circuits and Passive Devices – M.L. Sisodia and G.S.Raghuvanshi, W iley Eastern Ltd.,New Age International Publishers Ltd., 1995.
3. Microwave Engineering Passive Circuits – Peter A. Rizzi, PHI, 1999.
4. Electronic and Radio Engineering – F.E. Terman, McGraw-Hill, 4th Ed., 1955.
5. Microwave Engineering – A. Das and S.K. Das, TMH, 2nd Ed., 2009.
6. Microwave Engineering - G. S. Raghuvanshi and K. Satya Prasad, Cengage Learning, 2012.

Course Outcomes:

Upon completion of the course, the students will be able to:

- Understand the significance of microwaves and microwave transmission lines.
- Analyze the characteristics of microwave tubes and compare them.
- Be able to list and explain the various microwave solid state devices.
- Can set up a microwave bench for measuring microwave parameters.

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IV Year B.Tech. ECE-I Sem	L	T/P/D	C
	-	-/3/-	2

(A70499) MICROWAVE ENGINEERING AND DIGITAL COMMUNICATIONS LAB

Note: Minimum 12 Experiments to be conducted

Part – A: Microwave Engineering Lab (Any 6 Experiments):

1. Reflex Klystron Characteristics
2. Gunn Diode Characteristics
3. Directional Coupler Characteristics
4. VSWR Measurement
5. Measurement of Waveguide Parameters
6. Measurement of Impedance of a given Load
7. Measurement of Scattering parameters of a Magic Tee
8. Measurement of Scattering parameters of a Circulator
9. Attenuation Measurement
10. Microwave Frequency Measurement

Part – B: Digital Communication Lab (Any 6 Experiments):

1. PCM Generation and Detection
2. Differential Pulse Code Modulation
3. Delta Modulation
4. Time Division Multiplexing of 2 Band Limited Signals
5. Frequency shift keying: Generation and Detection
6. Phase Shift Keying: Generation and Detection
7. Amplitude Shift Keying: Generation and Detection
8. Study of the spectral characteristics of PAM, QAM
9. DPSK :Generation and Detection
10. QPSK : Generation and Detection

Equipment required for the Laboratory:

Microwave Engineering Lab:

1. Microwave Bench set up with Klystron Power Supply
2. Microwave Bench set up with Gunn Power Supply
3. Micro Ammeter
4. VSWR meter

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5. Microwave Components

Digital Communication Lab:

1. RPS: 0-30V
2. CRO: 0-20MHz
3. Function Generators: 0-1MHz
4. RF Generators: 0-100MHz
5. Experimental Kits /Modules

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Building Vocabulary - Starting a conversation – responding appropriately and relevantly – using the right body language – Role Play in different situations & Discourse Skills- using visuals - Synonyms and antonyms, word roots, one-word substitutes, prefixes and suffixes, study of word origin, business vocabulary, analogy, idioms and phrases, collocations & usage of vocabulary.

2. Activities on Reading Comprehension –General Vs Local comprehension, reading for facts, guessing meanings from context, scanning, skimming, inferring meaning, critical reading & effective googling.
3. Activities on Writing Skills – Structure and presentation of different types of writing – letter writing/Resume writing/ e-correspondence/ Technical report writing/ Portfolio writing – planning for writing – improving one's writing.
4. Activities on Presentation Skills – Oral presentations (individual and group) through JAM sessions/seminars/PPTs and written presentations through posters/projects/reports/ e-mails/assignments etc.
5. Activities on Group Discussion and Interview Skills – Dynamics of group discussion, intervention, summarizing, modulation of voice, body language, relevance, fluency and organization of ideas and rubrics for evaluation- Concept and process, pre-interview planning, opening strategies, answering strategies, interview through tele-conference & video-conference and Mock Interviews.

Minimum Requirement:

The Advanced Communication Skills (ACS) Laboratory shall have the following infra-structural facilities to accommodate at least 35 students in the lab:

- Spacious room with appropriate acoustics.
- Round Tables with movable chairs
- Audio-visual aids
- LCD Projector
- Public Address system
- P – IV Processor, Hard Disk – 80 GB, RAM–512 MB Minimum, Speed – 2.8 GHZ
- T. V, a digital stereo & Camcorder
- Headphones of High quality

Prescribed Lab Manual: A book titled A Course Book of Advanced Communication Skills (ACS) Lab published by Universities Press, Hyderabad.

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Suggested Software:

The software consisting of the prescribed topics elaborated above should be procured and used.

- Oxford Advanced Learner's Compass, 7th Edition
- DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice.
- Lingua TOEFL CBT Insider, by Dreamtech
- TOEFL & GRE(KAPLAN, AARCO & BARRONS, USA, Cracking GRE by CLIFFS)
- The following software from 'train2success.com'
  - Ø Preparing for being Interviewed
  - Ø Positive Thinking
  - Ø Interviewing Skills
  - Ø Telephone Skills
  - Ø Time Management

Books Recommended:

1. Technical Communication by Meenakshi Raman & Sangeeta Sharma, Oxford University Press 2009.
2. Advanced Communication Skills Laboratory Manual by Sudha Rani, D, Pearson Education 2011.
3. Technical Communication by Paul V. Anderson. 2007. Cengage Learning pvt. Ltd. New Delhi.
4. Business and Professional Communication: Keys for Workplace Excellence. Kelly M. Quintanilla & Shawn T. Wahl. Sage South Asia Edition. Sage Publications. 2011.
5. The Basics of Communication: A Relational Perspective. Steve Duck & David T. McMahan. Sage South Asia Edition. Sage Publications. 2012.
6. English Vocabulary in Use series, Cambridge University Press 2008.
7. Management Shapers Series by Universities Press(India)Pvt Ltd., Himayatnagar, Hyderabad 2008.
8. Handbook for Technical Communication by David A. McMurrey & Joanne Buckley. 2012. Cengage Learning.
9. Communication Skills by Leena Sen, PHI Learning Pvt Ltd., New Delhi, 2009.
10. Handbook for Technical Writing by David A McMurrey & Joanne Buckely CENGAGE Learning 2008.

ELECTRONICS AND COMMUNICATION ENGINEERING

2017-2018

S. No	Regulations	Number of Courses	Year of Study
1	R16	21	I & II year I & II Semesters
2	R15	15	III and IV year I & II Semesters

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B.Tech COURSE STRUCTURE (2016-17)
(Common for EEE, ECE, CSE, EIE, BME, IT, ETE, ECM, ICE)

I YEAR I SEMESTER

S. No	Course Code	Course Title	L	T	P	Credits
1	MA101BS	Mathematics-I	3	1	0	3
2	CH102BS	Engineering Chemistry	4	0	0	4
3	PH103BS	Engineering Physics-I	3	0	0	3
4	EN104HS	Professional Communication in English	3	0	0	3
5	ME105ES	Engineering Mechanics	3	0	0	3
6	EE106ES	Basic Electrical and Electronics Engineering	4	0	0	4
7	EN107HS	English Language Communication Skills Lab	0	0	3	2
8	ME108ES	Engineering Workshop	0	0	3	2
9	*EA109MC	NSS	0	0	0	0
		Total Credits	**20**	**1**	**6**	**24**

I YEAR II SEMESTER

S. No	Course Code	Course Title	L	T	P	Credits
1	PH201BS	Engineering Physics-II	3	0	0	3
2	MA202BS	Mathematics-II	4	1	0	4
3	MA203BS	Mathematics-III	4	1	0	4
4	CS204ES	Computer Programming in C	3	0	0	3
5	ME205ES	Engineering Graphics	2	0	4	4
6	CH206BS	Engineering Chemistry Lab	0	0	3	2
7	PH207BS	Engineering Physics Lab	0	0	3	2
8	CS208ES	Computer Programming in C Lab	0	0	3	2
9	*EA209MC	NCC/NSO	0	0	0	0
		Total Credits	**16**	**2**	**13**	**24**

*Mandatory Course.

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PROFESSIONAL COMMUNICATION IN ENGLISH

B.Tech. I Year I Sem. **L T/P/D C**
Course Code: **EN104HS/EN204HS** **3 0/0/0 3**

INTRODUCTION

In view of the growing importance of English as a tool for global communication and the consequent emphasis on training students to acquire language skills, the syllabus of English has been designed to develop linguistic and communicative competencies of Engineering students.

In English classes, the focus should be on the skills development in the areas of vocabulary, grammar, reading and writing. For this, the teachers should use the prescribed text book for detailed study. The students should be encouraged to read the texts/poems silently leading to reading comprehension. Reading comprehension passages are given for practice in the class. The time should be utilized for working out the exercises given after each excerpt, and also for supplementing the exercises with authentic materials of a similar kind, for example, from newspaper articles, advertisements, promotional material, etc. *The focus in this syllabus is on skill development, fostering ideas and practice of language skills.*

Course Objectives:

The course will help students to:

a. Improve the language proficiency of students in English with an emphasis on Vocabulary, Grammar, Reading and Writing skills.
b. Equip students to study academic subjects more effectively using the theoretical and Practical components of English syllabus.
c. Develop study skills and communication skills in formal and informal situations.

Course Outcomes:

Students will be able to:

1. Use English Language effectively in spoken and written forms.
2. Comprehend the given texts and respond appropriately.
3. Communicate confidently in formal and informal contexts.

SYLLABUS

Reading Skills:

Objectives:

1. To develop an awareness in students about the significance of silent reading and comprehension.
2. To develop students' ability to guess meanings of words from the context and grasp the overall message of the text, draw inferences, etc., by way of:

- Skimming and Scanning the text
- Intensive and Extensive Reading
- Reading for Pleasure
- Identifying the topic sentence

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- Inferring lexical and contextual meaning
- Recognizing Coherence/Sequencing of Sentences

NOTE: The students will be trained in reading skills using the prescribed texts for detailed study. They will be tested in reading comprehension of different 'unseen' passages which may be taken from authentic texts, such as magazines/newspaper articles.

Writing Skills:

Objectives:

1. To develop an awareness in the students about writing as an exact and formal skill
2. To create an awareness in students about the components of different forms of writing, beginning with the lower order ones through;
   - Writing of sentences
   - Use of appropriate vocabulary
   - Paragraph writing
   - Coherence and cohesiveness
   - Narration / description
   - Note Making
   - Formal and informal letter writing
   - Describing graphs using expressions of comparison

In order to improve the proficiency of the students in the acquisition of language skills mentioned above, the following text and course contents, divided into Five Units, are prescribed:

Text Books:

1. ***"Fluency in English – A Course book for Engineering Students"*** by Board of Editors: **Hyderabad: Orient BlackSwan Pvt. Ltd. 2016. Print.**
2. Raman, Meenakshi and Sharma, Sangeeta. ***"Technical Communication- Principles and Practice".*** **Third Edition. New Delhi: Oxford University Press. 2015. Print.**

The course content / study material is divided into **Five Units.**

<u>**Note**</u>: ***Listening and speaking skills are covered in the syllabus of ELCS Lab.***

UNIT –I:
Chapter entitled '***Presidential Address' by Dr. A.P.J. Kalam*** from ***"Fluency in English– A Course book for Engineering Students"*** published by Orient BlackSwan, Hyderabad.

Vocabulary: Word Formation -- Root Words --The Use of Prefixes and Suffixes– Collocations– Exercises for Practice.

Grammar: Punctuation – Parts of Speech- Articles -Exercises for Practice.

Reading: ***Double Angels*** by David Scott-Reading and Its Importance- Techniques for Effective Reading- Signal Words- Exercises for Practice

Writing: Writing Sentences- Techniques for Effective Writing-- Paragraph Writing- Types, Structure and Features of a Paragraph-Coherence and Cohesiveness: Logical, Lexical and Grammatical Devices - Exercises for Practice

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UNIT –II:

Chapter entitled ***Satya Nadella: Email to Employees on his First Day as CEO*** from ***"Fluency in English– A Course book for Engineering Students"*** Published by Orient BlackSwan, Hyderabad.

Vocabulary: Synonyms and Antonyms – Homonyms, Homophones, Homographs- Exercises for Practice (Chapter 17 ***'Technical Communication- Principles and Practice'. Third Edition*** published by Oxford University Press may also be followed.)

Grammar: Verbs-Transitive, Intransitive and Non-finite Verbs – Mood and Tense— Gerund – Words with Appropriate Prepositions – Phrasal Verbs - Exercises for Practice

Reading: Sub-skills of Reading- Skimming, Scanning, Extensive Reading and Intensive Reading - ***The Road Not Taken*** by **Robert Frost** -- Exercises for Practice

Writing: Letter Writing –Format, Styles, Parts, Language to be used in Formal Letters- Letter of Apology – Letter of Complaint-Letter of Inquiry with Reply – Letter of Requisition — Exercises for Practice

UNIT –III:

From the book entitled ***'Technical Communication- Principles and Practice'. Third Edition*** published by Oxford University Press.

Vocabulary: Introduction- A Brief History of Words – Using the Dictionary and Thesaurus– Changing Words from One Form to Another – Confusables (From Chapter 17 entitled '*Grammar and Vocabulary Development*')

Grammar: Tenses: Present Tense- Past Tense- Future Tense- Active Voice – Passive Voice- Conditional Sentences – Adjective and Degrees of Comparison. (From Chapter 17 entitled '*Grammar and Vocabulary Development*')

Reading: Improving Comprehension Skills – Techniques for Good Comprehension- Skimming and Scanning- Non-verbal Signals – Structure of the Text – Structure of Paragraphs – Punctuation – Author's viewpoint (Inference) – Reader Anticipation: Determining the Meaning of Words – Summarizing- Typical Reading Comprehension Questions. (From Chapter 10 entitled '*Reading Comprehension*')

Writing: Introduction- Letter Writing-Writing the Cover Letter- Cover Letters Accompanying Resumes- Emails. (From Chapter 15 entitled '*Formal Letters, Memos, and Email*')

UNIT –IV:

Chapter entitled ***'Good Manners' by J.C. Hill*** from ***Fluency in English – A Course book for Engineering Students"*** published by Orient Blackswan, Hyderabad.

Vocabulary: Idiomatic Expressions –One- word Substitutes --- Exercises for Practice (Chapter 17 ***'Technical Communication- Principles and Practice'. Third Edition*** published by Oxford University Press may also be followed.)

Grammar: Sequence of Tenses- Concord (Subject in Agreement with the Verb) – Exercises for Practice

Reading: *'If'* poem by **Rudyard Kipling**--Tips for Writing a Review --- Author's Viewpoint – Reader's Anticipation-- Herein the Students will be required to Read and Submit a Review of a Book (Literary or Non-literary) of their choice – Exercises for Practice.

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Writing: Information Transfer-Bar Charts-Flow Charts-Tree Diagrams etc., -- Exercises for Practice.
Introduction - Steps to Effective Precis Writing – Guidelines- Samples (Chapter 12 entitled '*The Art of Condensation*' from ***Technical Communication- Principles and Practice. Third Edition*** published by Oxford University Press)

UNIT –V:

Chapter entitled ***'Father Dear Father'*** **by Raj Kinger** from ***Fluency in English – A Course book for Engineering Students"*** Published by Orient BlackSwan, Hyderabad

Vocabulary: Foreign Words—Words borrowed from other Languages- Exercises for Practice

Grammar: Direct and Indirect Speech- Question Tags- Exercises for Practice

Reading: Predicting the Content- Understanding the Gist – SQ3R Reading Technique- Study Skills – Note Making - Understanding Discourse Coherence – Sequencing Sentences. (From Chapter 10 entitled **'Reading Comprehension' -** ***Technical Communication- Principles and Practice. Third Edition*** published by Oxford University Press.)

Writing: Technical Reports- Introduction – Characteristics of a Report – Categories of Reports –Formats- Prewriting – Structure of Reports (Manuscript Format) - Types of Reports - Writing the Report. (From Chapter 13 entitled **'Technical Reports' -** ***Technical Communication- Principles and Practice. Third Edition*** published by Oxford University Press.)

- **Exercises from both the texts not prescribed shall be used for classroom tasks.**

References

1 Green, David. *Contemporary English Grammar –Structures and Composition.* MacMillan India. 2014 (Print)
2. Rizvi, M. Ashraf. *Effective Technical Communication*. Tata Mc Graw –Hill. 2015 (Print).

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2. Engineering Mechanics/ Irving Shames, G. Krishna Mohan Rao / Prentice Hall
3. Foundations and applications of Engineering Mechanics by HD Ram and AK Chouhan, Cambridge publications.

References:

1. A Text of Engineering Mechanics /YVD Rao/ K. Govinda Rajulu/ M. Manzoor Hussain / Academic Publishing Company
2. Engineering Mechanics / Bhattacharyya/ Oxford.

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ENGLISH LANGUAGE COMMUNICATION SKILLS (ELCS) LAB

B.Tech. I Year I Sem. **L T/P/D C**
Course Code: **EN107HS/EN207HS** **0 0/3/0 2**

The **English Language Communication Skills (ELCS) Lab** focuses on the production and practice of sounds of language and familiarizes the students with the use of English in everyday situations both in formal and informal contexts.

Course Objectives:

- To facilitate computer-assisted multi-media instruction enabling individualized and independent language learning
- To sensitize the students to the nuances of English speech sounds, word accent, intonation and rhythm
- To bring about a consistent accent and intelligibility in students' pronunciation of English by providing an opportunity for practice in speaking
- To improve the fluency of students in spoken English and neutralize their mother tongue influence
- To train students to use language appropriately for public speaking, group discussions and interviews

Course Outcomes:

Students will be able to attain:

- Better understanding of nuances of English language through audio- visual experience and group activities
- Neutralization of accent for intelligibility
- Speaking skills with clarity and confidence which in turn enhances their employability skills.

Syllabus: English Language Communication Skills Lab (ELCS) shall have two parts:

- **Computer Assisted Language Learning (CALL) Lab**
- **Interactive Communication Skills (ICS) Lab**

Listening Skills:

Objectives

- To enable students develop their listening skills so that they may appreciate the role in the LSRW skills approach to language and improve their pronunciation
- To equip students with necessary training in listening, so that they can comprehend the speech of people of different backgrounds and regions.

Students should be given practice in listening to the sounds of the language, to be able to recognize them and find the distinction between different sounds, to be able to mark stress and recognize and use the right intonation in sentences.

- Listening for general content
- Listening to fill up information
- Intensive listening
- Listening for specific information

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Speaking Skills:

Objectives

- To involve students in speaking activities in various contexts
- To enable students express themselves fluently and appropriately in social and professional contexts :
  - • Oral practice
  - • Describing objects/situations/people
  - • Role play – Individual/Group activities
  - • Just A Minute (JAM) Sessions.

The following course content is prescribed for the **English Language Communication Skills Lab**.

Exercise – I

CALL Lab:

Understand: Listening Skill- Its importance – Purpose- Process- Types- Barriers- Effective Listening.

Practice: Introduction to Phonetics – Speech Sounds – Vowels and Consonants – Minimal Pairs- Consonant Clusters- Past Tense Marker and Plural Marker.

Testing Exercises

ICS Lab:

Understand: Spoken vs. Written language- Formal and Informal English.

Practice: Ice-Breaking Activity and JAM Session- Situational Dialogues – Greetings – Taking Leave – Introducing Oneself and Others.

Exercise – II

CALL Lab:

Understand: Structure of Syllables – Word Stress– Weak Forms and Strong Forms – Sentence Stress – Intonation.

Practice: Basic Rules of Word Accent - Stress Shift - Weak Forms and Strong Forms- Sentence Stress – Intonation.

Testing Exercises

ICS Lab:

Understand: Features of Good Conversation – Strategies for Effective Communication.

Practice: Situational Dialogues – Role-Play- Expressions in Various Situations –Making Requests and Seeking Permissions - Telephone Etiquette.

Exercise - III

CALL Lab:

Understand: Errors in Pronunciation-the Influence of Mother Tongue (MTI).

Practice: Common Indian Variants in Pronunciation – Differences between British and American Pronunciation.

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Testing Exercises

ICS Lab:

Understand: Descriptions- Narrations- Giving Directions and Guidelines.

Practice: Giving Instructions – Seeking Clarifications – Asking for and Giving Directions – Thanking and Responding – Agreeing and Disagreeing – Seeking and Giving Advice – Making Suggestions.

Exercise – IV

CALL Lab:

Understand: Listening for General Details.

Practice: Listening Comprehension Tests.

Testing Exercises

ICS Lab:

Understand: Public Speaking – Exposure to Structured Talks - Non-verbal Communication- Presentation Skills.

Practice: Making a Short Speech – Extempore- Making a Presentation.

Exercise – V

CALL Lab:

Understand: Listening for Specific Details.

Practice: Listening Comprehension Tests.

Testing Exercises

ICS Lab:

Understand: Group Discussion- Interview Skills.

Practice: Group Discussion- Mock Interviews.

Minimum Requirement of infrastructural facilities for ELCS Lab:

1. **Computer Assisted Language Learning (CALL) Lab:**

   The Computer Assisted Language Learning Lab has to accommodate 40 students with 40 systems, with one Master Console, LAN facility and English language learning software for self- study by students.

   System Requirement (Hardware component):

   Computer network with LAN facility (minimum 40 systems with multimedia) with the following specifications:

   Computers with Suitable Configuration

   High Fidelity Headphones

2. **Interactive Communication Skills (ICS) Lab:**

   The Interactive Communication Skills Lab: A Spacious room with movable chairs and audio-visual aids with a Public Address System, a T. V. or LCD, a digital stereo –audio and video system and camcorder etc.

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Lab Manuals:

1) A book entitled "***ELCS Lab Manual – A Workbook for CALL and ICS Lab Activities***" by Board of Editors: Hyderabad: Orient BlackSwan Pvt. Ltd. 2016. Print.
2) Hart, Steve; Nair, Aravind R.; Bhambhani, Veena. "***EMBARK- English for undergraduates***" Delhi: Cambridge University Press. 2016. Print.

Suggested Software:

1) Cambridge Advanced Learners' English Dictionary with CD.
2) Grammar Made Easy by Darling Kindersley.
3) Punctuation Made Easy by Darling Kindersley.
4) Oxford Advanced Learner's Compass, 8^{th} Edition.
5) English in Mind (Series 1-4), Herbert Puchta and Jeff Stranks with Meredith Levy, Cambridge.
6) English Pronunciation in Use (Elementary, Intermediate, Advanced) Cambridge University Press.
7) TOEFL and GRE (KAPLAN, AARCO and BARRONS, USA, Cracking GRE by CLIFFS).

References:

1) Jayashree Mohanraj. *Let Us Hear Them Speak*. New Delhi: Sage Texts. 2015. Print. Hancock, M. *English Pronunciation in Use. Intermediate Cambridge*: Cambridge University Press. 2009. Print.

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ENGINEERING CHEMISTRY

B.Tech. I Year I Sem.
Course Code: **CH102BS/CH202BS**

L	T/P/D	C
4	0/0/0	4

Course Objectives:

1) To bring adaptability to new developments in Engineering Chemistry and to acquire the skills required to become a perfect engineer.
2) To include the importance of water in industrial usage, significance of corrosion control to protect the structures, polymers and their controlled usage.
3) To acquire knowledge of engineering materials and about fuels and batteries.
4) To acquire required knowledge about engineering materials like cement, refractories and composites.

Course Outcomes:

Students will gain the basic knowledge of electrochemical procedures related to corrosion and its control. They can understand the basic properties of water and its usage in domestic and industrial purposes. They learn the use of fundamental principles to make predictions about the general properties of materials. They can predict potential applications of chemistry and practical utility in order to become good engineers and entrepreneurs.

UNIT-I

Water and its treatment: Introduction – hardness of water – causes of hardness – types of hardness: temporary and permanent – expression and units of hardness – Estimation of hardness of water by complexometric method. Numerical problems. Potable water and its specifications- Steps involved in the treatment of potable water - Disinfection of potable water by chlorination and Ozonization. Defluoridation – Nalgonda technique - Determination of F^- ion by ion- selective electrode method.

Boiler troubles:

Sludges, scales and Caustic embrittlement. Internal treatment of Boiler feed water – Calgon conditioning – Phosphate conditioning - Colloidal conditioning – Softening of water by ion-exchange processes. Desalination of water – Reverse osmosis. Numerical problems – Sewage water - Steps involved in treatment of sewage.

UNIT-II

Electrochemistry and Batteries:

Electrochemistry: Electrode- electrode potential, standard electrode potential, types of electrodes – Construction and functioning of Standard hydrogen electrode, calomel and glass electrode. Nernst equation - electrochemical series and its applications. Electrochemical cells: Daniel cell – cell notation, cell reaction and cell emf — Concept of concentration cells – Electrolyte concentration cell –Numerical problems.

Batteries: Cell and battery - Primary battery (dry cell, alkaline cell and Lithium cell) and Secondary battery (lead acid, Ni-Cd and lithium ion cell),

Fuel cells: Hydrogen –oxygen and methanol-oxygen fuel cells – Applications.

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UNIT-III

Polymers: Definition – Classification of polymers with examples – Types of polymerization – addition (free radical addition) and condensation polymerization with examples.

Plastics: Definition and characteristics- thermoplastic and thermosetting plastics, compounding and fabrication of plastics (compression and injection moulding). Preparation, Properties and engineering applications of PVC and Bakelite.

Fibers: Characteristics of fibers – preparation, properties and applications of Nylon-6, 6 and Dacron. Fiber reinforced plastics (FRP) – Applications.

Rubbers: Natural rubber and its vulcanization - compounding of rubber.

Elastomers: Characteristics –preparation – properties and applications of Buna-S, Butyl and Thiokol rubber.

Conducting polymers: Characteristics and Classification with examples-mechanism of conduction in trans-polyacetylene and applications of conducting polymers.

Biodegradable polymers: Concept and advantages - Polylactic acid and poly vinyl alcohol and their applications.

UNIT-IV

Fuels and Combustion: Classification- solid fuels: coal – analysis of coal – proximate and ultimate analysis and their significance. Liquid fuels – petroleum and its refining, cracking – types – moving bed catalytic cracking. Knocking – octane and cetane rating, synthetic petrol - Fischer-Tropsch's process; Gaseous fuels – composition and uses of natural gas, LPG and CNG.

Combustion: Definition, Calorific value of fuel – HCV, LCV; Calculation of air quantity required for combustion of a fuel.

UNIT-V

Cement, Refractories, Lubricants and Composites:

Cement: Portland cement, its composition, setting and hardening of Portland cement.

Special cements: White cement, water proof cement, High alumina cement and Acid resistant cement.

Refractories: Classification, characteristics of good refractories, Refractoriness, refractoriness under load, porosity and chemical inertness – applications of refractories.

Lubricants: Classification of lubricants with examples-characteristics of a good lubricants - mechanism of lubrication (thick film, thin film and extreme pressure)- properties of lubricants: viscosity, cloud point, pour point, flash point and fire point.

Composites: Introduction- Constituents of composites – advantages, classification and constituents of composites. Applications of composites.

Text books:

1) Engineering Chemistry by P.C. Jain and M. Jain, Dhanpatrai Publishing Company, New Delhi (2010)
2) Engineering Chemistry by Rama Devi, Venkata Ramana Reddy and Rath, Cengage learning, New Delhi. (2016)

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Reference Books:

1) Engineering Chemistry by Shikha Agarwal, Cambridge University Press, Delhi (2015)
2) Engineering Chemistry by Shashi Chawla, Dhanpatrai and Company (P) Ltd. Delhi (2011)
3) Engineering Chemistry by Thirumala Chary and Laxminarayana, Scitech Publishers, Chennai (2016).

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CH206BS: ENGINEERING CHEMISTRY LAB

B.Tech. I Year II Sem.

L T/P/D C
0 0/3/0 2

LIST OF EXPERIMENTS

Volumetric Analysis:

1. Estimation of Ferrous ion by Dichrometry.
2. Estimation of hardness of water by Complexometric method using EDTA.
3. Estimation of Ferrous and Ferric ions in a given mixture by Dichrometry.
4. Estimation Ferrous ion by Permanganometry.
5. Estimation of copper by Iodomery.
6. Estimation of percentage of purity of MnO_2 in pyrolusite
7. Determination of percentage of available chlorine in bleaching powder.
8. Determination of salt concentration by ion- exchange resin.

Instrumental methods of Analysis:

1. Estimation of HCl by Conductometry.
2. Estimation of Ferrous ion by Potentiometry.
3. Determination of Ferrous iron in cement by Colorimetric method.
4. Determination of viscosity of an oil by Redwood / Oswald's Viscometer.
5. Estimation of manganese in $KMnO_4$ by Colorimetric method.
6. Estimation o f HCl and Acetic acid in a given mixture by Conductometry.
7. Estimation of HCl by Potentiometry.

Preparation of Polymers:

1. Preparation of Bakelite and urea formaldehyde resin.

Note: All the above experiments must be performed.

Text Books:

1. Vogel's Text Book of Quantitative Chemical Analysis, 5th Edition (2015)
2. G. H. Jeffery, J. Bassett, J. Mendham and R. C. Denney.
3. A Text Book on experiments and calculations in Engineering Chemistry by S.S. Dara S. Chand & Company Ltd., Delhi (2003).

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PH201BS: ENGINEERING PHYSICS - II

B.Tech. I Year II Sem.

	L	T/P/D	C
	3	0/0/0	3

Course Objectives:

- To understand the behavior of a particle quantum mechanically.
- To be able to distinguish pure and impure semi conductors and understand formation of P-N Junction.
- To understand various magnetic and dielectric properties of materials.
- To study super conductor behavior of materials.

Course Outcomes: After completion of this course the student is able to

- Realize the importance of behavior of a particle quantum mechanically.
- Learn concentration estimation of charge carriers in semi conductors.
- Learn various magnetic dielectric properties and apply them in engineering applications.
- Know the basic principles and applications of super conductors.

UNIT - I

Principles of Quantum Mechanics: Waves and particles, de-Broglie hypothesis, matter waves, Davisson and Germer experiment, Heisenberg uncertainty principle, Schrodinger time independent wave equation, physical significance of wave function, particle in 1-D potential box, electron in periodic potential, Kronig-Penny model (qualitative treatment), E-K curve, origin of energy band formation in solids.

UNIT - II

Semiconductor Physics: Fermi level in intrinsic and extrinsic semiconductors, calculation of carrier concentration in intrinsic & extrinsic semiconductors, direct and indirect band gap semiconductors, formation of PN junction, open circuit PN junction, energy diagram of PN junction diode, solar cell: I-V characteristics and applications.

UNIT - III

Dielectric Properties: Electric dipole, dipole moment, dielectric constant, polarizability, electric susceptibility, displacement vector, electronic, ionic and orientation polarizations and calculation of their polarizabilitites, internal field, Clausius-Mossotti relation, Piezoelectricity, pyroelectricity and ferroelectricity-$BaTiO_3$ structure.

UNIT - IV

Magnetic Properties & Superconductivity: Permeability, field intensity, magnetic field induction, magnetization, magnetic susceptibility, origin of magnetic moment, Bohr magneton, classification of dia, para and ferro magnetic materials on the basis of magnetic moment, hysteresis curve based on domain theory, soft and hard magnetic materials, properties of anti-ferro and ferri magnetic materials,

Superconductivity: Superconductivity phenomenon, Meissner effect, applications of superconductivity.

UNIT - V

Introduction to nanoscience: Origin of nanoscience, nanoscale, surface to volume ratio, quantum confinement, dominance of electromagnetic forces, random molecular motion, bottom-up fabrication: Sol-gel, CVD and PVD techniques, top-down fabrication: ball mill method, characterization by XRD, SEM and TEM.

Text Books:

1. Solid State Physics, A. J. Dekkar, Macmillan publishers Ind. Ltd.,
2. Solid State Physics, Chales Kittel, Wiley student edition.
3. Fundamentals of Physics, Alan Giambattisa, BM Richardson and Robert C Richardson, Tata McGraw hill Publishers.

Reference Books:

1. Modern Engineering Physics, K. Vijaya Kumar, S. Chandralingam S. Chand & Co. Pvt. Ltd.,
2. University Physics, Francis W. Sears, Hugh D. Young, Marle Zeemansky and Roger A Freedman, Pearson Education.
3. Fundamentals of Acoustics, Kinster and Frey, John Wiley and Sons.
4. Introduction to Quantum Mechanics Leonard I. Schiff McGraw-Hill

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PH107BS/PH207BS: ENGINEERING PHYSICS LAB

B.Tech. I Year II Sem.

L	T/P/D	C
0	0/3/0	2

LIST OF EXPERIMENTS

1. Dispersive power of the material of a prism – Spectrometer.
2. Determination of wavelengths of white source – Diffraction grating.
3. Newton's Rings – Radius of curvature of Plano convex lens.
4. Melde's experiment – Transverse and longitudinal modes.
5. Charging, discharging and time constant of an R-C circuit.
6. L-C-R circuit – Resonance & Q-factor.
7. Magnetic field along the axis of current carrying coil – Stewart and Gees method and to verify Biot – Savart's law.
8. Study the characteristics of LED and LASER diode.
9. Bending losses of fibres & Evaluation of numerical aperture of a given fibre.
10. Energy gap of a material of p-n junction.
11. Torsional pendulum – Rigidity modulus.
12. Wavelength of light, resolving power and dispersive power of a diffraction grating using laser.
13. V-I characteristics of a solar cell.

Note: Minimum 10 experiments must be performed.

CS104ES/CS204ES: COMPUTER PROGRAMMING IN C

B.Tech. I Year II Sem.	**L**	**T/P/D**	**C**
	3	**0/0/0**	**3**

Course Objectives:

- To learn the fundamentals of computers.
- To understand the various steps in Program development.
- To learn the syntax and semantics of C Programming Language.
- To learn how to write modular and readable C Programs.
- To learn to write programs using structured programming approach in C to solve problems.

Course Outcomes:

- Demonstrate the basic knowledge of computer hardware and software.
- Ability to write algorithms for solving problems.
- Ability to draw flowcharts for solving problems.
- Ability to code a given logic in C programming language.
- Gain knowledge in using C language for solving problems.

UNIT - I

Introduction to Computers – Computer Systems, Computing Environments, Computer Languages, Creating and running programs, Program Development, algorithms and flowcharts , Number systems-Binary, Decimal, Hexadecimal and Conversions, storing integers and real numbers.

Introduction to C Language – Background, C Programs, Identifiers, Types, Variables, Constants, Input / Output, Operators(Arithmetic, relational, logical, bitwise etc.), Expressions, Precedence and Associativity, Expression Evaluation, Type conversions, Statements- Selection Statements(making decisions) – if and switch statements, Repetition statements (loops)-while, for, do-while statements, Loop examples, other statements related to looping – break, continue, goto, Simple C Program examples.

UNIT - II

Functions-Designing Structured Programs, Functions, user defined functions, inter function communication, Standard functions, Scope, Storage classes-auto, register, static, extern, scope rules, type qualifiers, recursion- recursive functions, Limitations of recursion, example C programs.

Arrays – Concepts, using arrays in C, inter function communication, array applications- linear search, binary search and bubble sort, two – dimensional arrays, multidimensional arrays, C program examples.

UNIT - III

Pointers – Introduction (Basic Concepts), Pointers for inter function communication, pointers to pointers, compatibility, Pointer Applications-Arrays and Pointers, Pointer Arithmetic and

arrays, Passing an array to a function, memory allocation functions, array of pointers, programming applications, pointers to void, pointers to functions.
Strings – Concepts, C Strings, String Input / Output functions, arrays of strings, string manipulation functions, string / data conversion, C program examples.

UNIT - IV

Enumerated, Structure and Union Types – The Type Definition (typedef), Enumerated types, Structures –Declaration, initialization, accessing structures, operations on structures, Complex structures-Nested structures, structures containing arrays, structures containing pointers, arrays of structures, structures and functions, Passing structures through pointers, self referential structures, unions, bit fields, C programming examples, command–line arguments, Preprocessor commands.

UNIT – V

Input and Output – Concept of a file, streams, text files and binary files, Differences between text and binary files, State of a file, Opening and Closing files, file input / output functions (standard library input / output functions for files), file status functions (error handling), Positioning functions (fseek ,rewind and ftell), C program examples.

Text Books:

1. Computer Science: A Structured Programming Approach Using C, B. A. Forouzan and R. F. Gilberg, Third Edition, Cengage Learning.
2. Programming in C. P. Dey and M Ghosh , Second Edition, Oxford University Press.

Reference Books:

1. The C Programming Language, B.W. Kernighan and Dennis M. Ritchie, Second Edition, Pearson education.
2. Programming with C, B. Gottfried, 3rd edition, Schaum's outlines, McGraw Hill Education (India) Pvt Ltd.
3. C From Theory to Practice, G S. Tselikis and N D. Tselikas, CRC Press.
4. Basic computation and Programming with C, Subrata Saha and S. Mukherjee, Cambridge University Press.

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CS108ES/CS208ES: COMPUTER PROGRAMMING IN C LAB

B.Tech. I Year II Sem.

L	T/P/D	C
0	0/3/0	2

Course Objective:

- To write programs in C using structured programming approach to solve the problems.

Course Outcomes:

- Ability to design and test programs to solve mathematical and scientific problems.
- Ability to write structured programs using control structures and functions.

Recommended Systems/Software Requirements:

- Intel based desktop PC
- GNU C Compiler

1. a) Write a C program to find the factorial of a positive integer.
   b) Write a C program to find the roots of a quadratic equation.

2. a) Write a C program to determine if the given number is a prime number or not.
   b) A Fibonacci sequence is defined as follows: the first and second terms in the sequence are 0 and 1. Subsequent terms are found by adding the preceding two terms in the sequence. Write a C program to generate the first n terms of the sequence.

3. a) Write a C program to construct a pyramid of numbers.
   b) Write a C program to calculate the following Sum:
   $Sum=1-x^2/2! +x^4/4!-x^6/6!+x^8/8!-x^{10}/10!$

4. a) The least common multiple (LCM) of two positive integers a and b is the smallest integer that is evenly divisible by both a and b. Write a C program that reads two integers and calls LCM (a, b) function that takes two integer arguments and returns their LCM. The LCM (a, b) function should calculate the least common multiple by calling the GCD (a, b) function and using the following relation:
   LCM (a, b) = ab / GCD (a, b)
   b) Write a C program that reads two integers n and r to compute the ncr value using the following relation:
   n_{c_r} (n, r) = n! / r! (n-r)! . Use a function for computing the factorial value of an integer.

5. a) Write C program that reads two integers x and n and calls a recursive function to compute x^n
   b) Write a C program that uses a recursive function to solve the Towers of Hanoi problem.
   c) Write a C program that reads two integers and calls a recursive function to compute n_{c_r} value.

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6. a) Write a C program to generate all the prime numbers between 1 and n, where n is a value supplied by the user using Sieve of Eratosthenes algorithm.
   b) Write a C program that uses non recursive function to search for a Key value in a given list of integers. Use linear search method.

7. a) Write a menu-driven C program that allows a user to enter n numbers and then choose between finding the smallest, largest, sum, or average. The menu and all the choices are to be functions. Use a switch statement to determine what action to take. Display an error message if an invalid choice is entered.
   b) Write a C program that uses non recursive function to search for a Key value in a given sorted list of integers. Use binary search method.

8 a) Write a C program that implements the Bubble sort method to sort a given list of integers in ascending order.
   b) Write a C program that reads two matrices and uses functions to perform the following:
      1. Addition of two matrices
      2. Multiplication of two matrices

9. a) Write a C program that uses functions to perform the following operations:
      1. to insert a sub-string into a given main string from a given position.
      2. to delete n characters from a given position in a given string.
   b) Write a C program that uses a non recursive function to determine if the given string is a palindrome or not.

10. a) Write a C program to replace a substring with another in a given line of text.
   b) Write a C program that reads 15 names each of up to 30 characters, stores them in an array, and uses an array of pointers to display them in ascending (ie. alphabetical) order.

11. a) 2's complement of a number is obtained by scanning it from right to left and complementing all the bits after the first appearance of a 1. Thus 2's complement of 11100 is 00100. Write a C program to find the 2's complement of a binary number.
   b) Write a C program to convert a positive integer to a roman numeral. Ex. 11 is converted to XI.

12. a) Write a C program to display the contents of a file to standard output device.
   b) Write a C program which copies one file to another, replacing all lowercase characters with their uppercase equivalents.

13. a) Write a C program to count the number of times a character occurs in a text file. The file name and the character are supplied as command-line arguments.
   b) Write a C program to compare two files, printing the first line where they differ.

14. a) Write a C program to change the nth character (byte) in a text file. Use fseek function.

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b) Write a C program to reverse the first n characters in a file. The file name and n are specified on the command line. Use fseek function.

15. a) Write a C program to merge two files into a third file (i.e., the contents of the firs t file followed by those of the second are put in the third file).

b) Define a macro that finds the maximum of two numbers. Write a C program that uses the macro and prints the maximum of two numbers.

Reference Books:

1. Mastering C, K.R. Venugopal and S.R. Prasad, TMH Publishers.
2. Computer Programming in C, V. Rajaraman, PHI.
3. Programming in C, Stephen G. Kochan, Fourth Edition, Pearson Education.
4. C++: The complete reference, H. Schildt, TMH Publishers.

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II YEAR I SEMESTER

Code	Subject	L	T/P/D	C
A30007	Mathematics - III	4	-	4
A30405	Probability Theory and Stochastic Processes	4	-	4
A30407	Switching Theory and Logic Design	4	-	4
A30204	Electrical Circuits	4	-	4
A30404	Electronic Devices and Circuits	4	-	4
A30406	Signals and Systems	4	-	4
A30482	Electronic Devices and Circuits Lab	-	3	2
A30481	Basic Simulation Lab	-	3	2
	Total	24	6	28

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

II Year B.Tech. ECE-I Sem	**L**	**T/P/D**	**C**
	4	**-/-/-**	**4**

(A30404) ELECTRONIC DEVICES AND CIRCUITS

Objectives:

This is a fundamental course, basic knowledge of which is required by all the circuit branch engineers. This course focuses:

- To familiarize the student with the principle of operation, analysis and design of Junction diode, BJT and FET transistors and amplifier circuits.
- To understand diode as rectifier.
- To study basic principle of filter circuits and various types.

UNIT -I:

P-N Junction Diode: Qualitative Theory of P-N Junction, P-N Junction as a Diode, Diode Equation, Volt-Ampere Characteristics, Temperature dependence of VI characteristic, Ideal versus Practical – Resistance levels (Static and Dynamic), Transition and Diffusion Capacitances, Diode Equivalent Circuits, Load Line Analysis, Breakdown Mechanisms in Semiconductor Diodes, Zener Diode Characteristics.

Special Purpose Electronic Devices: Principle of Operation and Characteristics of Tunnel Diode (with the help of Energy Band Diagram), Varactor Diode, SCR and Semiconductor Photo Diode.

UNIT-II:

Rectifiers and Filters : The P-N junction as a Rectifier, Half wave Rectifier, Full wave Rectifier, Bridge Rectifier, Harmonic components in a Rectifier Circuit, Inductor Filters, Capacitor Filters, L- Section Filters, p- Section Filters, Comparision of Filters, Voltage Regulation using Zener Diode.

UNIT-III:

Bipolar Junction Transistor and UJT: The Junction Transistor, Transistor Current Components, Transistor as an Amplifier, Transistor Construction, BJT Operation, BJT Symbol, Common Base, Common Emitter and Common Collector Configurations, Limits of Operation , BJT Specifications, BJT Hybrid Model, Determination of h-parameters from Transistor Characteristics, Comparison of CB, CE, and CC Amplifier Configurations, UJT and Characteristics.

UNIT-IV:

Transistor Biasing and Stabilization: Operating Point, The DC and AC Load lines, Need for Biasing, Fixed Bias, Collector Feedback Bias, Emitter Feedback Bias, Collector - Emitter Feedback Bias, Voltage Divider Bias,

Bias Stability, Stabilization Factors, Stabilization against variations in VBE and ß, Bias Compensation using Diodes and Transistors, Thermal Runaway, Thermal Stability, Analysis of a Transistor Amplifier Circuit using h-Parameters.

UNIT-V:

Field Effect Transistor and FET Amplifiers

Field Effect Transistor: The Junction Field Effect Transistor (Construction, principle of operation, symbol) – Pinch-off Voltage - Volt-Ampere characteristics, The JFET Small Signal Model, MOSFET (Construction, principle of operation, symbol), MOSFET Characteristics in Enhancement and Depletion modes.

FET Amplifiers: FET Common Source Amplifier, Common Drain Amplifier, Generalized FET Amplifier, Biasing FET, FET as Voltage Variable Resistor, Comparison of BJT and FET.

TEXT BOOKS:

1. Millman's Electronic Devices and Circuits – J. Millman, C.C.Halkias, and Satyabrata Jit, 2 Ed.,1998, TMH.
2. Electronic Devices and Circuits – Mohammad Rashid, Cengage Learing, 2013
3. Electronic Devices and Circuits – David A. Bell, 5 Ed, Oxford

REFERENCE BOOKS:

1. Integrated Electronics – J. Millman and Christos C. Halkias, 1991 Ed., 2008, TMH.
2. Electronic Devices and Circuits – R.L. Boylestad and Louis Nashelsky, 9 Ed., 2006, PEI/PHI.
3. Electronic Devices and Circuits – B. P. Singh, Rekha Singh, Pearson, 2Ed, 2013.
4. Electronic Devices and Circuits --K. Lal Kishore, 2 Ed., 2005, BSP.
5. Electronic Devices and Circuits – Anil K. Maini, Varsha Agarwal, 1 Ed., 2009, Wiley India Pvt. Ltd.
6. Electronic Devices and Circuits – S.Salivahanan, N.Suresh Kumar, A.Vallavaraj, 2 Ed., 2008, TMH.

Course Outcomes:

At the end of the course, the student will be able to:

- Understand and Analyse the different types of diodes, operation and its characteristics
- Design and analyse the DC bias circuitry of BJT and FET
- Design biasing circuits using diodes and transistors.
- To analyze and design diode application circuits, amplifier circuits and oscillatorsemploying BJT, FET devices.

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- -/3/- 2

(A30482) ELECTRONIC DEVICES AND CIRCUITS LAB

PART A: (Only for Viva-voce Examination)

Electronic Workshop Practice (In 3 Lab Sessions):

1. Identification, Specifications, Testing of R, L, C Components (Color Codes), Potentiometers, Switches (SPDT, DPDT, and DIP), Coils, Gang Condensers, Relays, Bread Boards, PCB's
2. Identification, Specifications and Testing of Active Devices, Diodes, BJT's, Low power JFET's, MOSFET's, Power Transistors, LED's, LCD's, SCR, UJT.
3. Study and operation of

i) Multimeters (Analog and Digital)

ii) Function Generator

iii) Regulated Power Supplies

iv) CRO.

PART B: (For Laboratory Examination – Minimum of 10 experiments)

1. Forward & Reverse Bias Characteristics of PN Junction Diode.
2. Zener diode characteristics and Zener as voltage Regulator.
3. Input & Output Characteristics of Transistor in CB Configuration and h-parameter calculations.
4. Input & Output Characteristics of Transistor in CE Configuration and h-parameter calculations.
5. Half Wave Rectifier with & without filters.
6. Full Wave Rectifier with & without filters.
7. FET characteristics.
8. Design of Self-bias circuit.
9. Frequency Response of CC Amplifier.
10. Frequency Response of CE Amplifier.
11. Frequency Response of Common Source FET amplifier .
12. SCR characteristics.
13. UJT Characteristics

PART C:Equipment required for Laboratories:

1. Regulated Power supplies (RPS) -0-30 V

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2.	CRO's -0-20 MHz.	
3.	Function Generators	-0-1 MHz.
4.	Multimeters	
5.	Decade Resistance Boxes/Rheostats	
6.	Decade Capacitance Boxes	
7.	Ammeters (Analog or Digital)	-0-20 μA, 0-50μA, 0-100μA, 0-200μA, 0-10 mA.
8.	Voltmeters (Analog or Digital)	-0-50V, 0-100V, 0-250V
9.	Electronic Components	-Resistors, Capacitors, BJTs, LCDs, SCRs, UJTs, FETs, LEDs, MOSFETs, Diodes- Ge& Si type, Transistors – NPN, PNP type)

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(A30406) SIGNALS AND SYSTEMS

Objectives:

This is a core subject, basic knowledge of which is required by all the engineers.

This course focuses on:

- To get an in-depth knowledge about signals, systems and analysis of the same using various transforms.

UNIT-I:

Signal Analysis and Fourier Series

Signal Analysis: Analogy between Vectors and Signals, Orthogonal Signal Space, Signal approximation using Orthogonal functions, Mean Square Error, Closed or complete set of Orthogonal functions, Orthogonality in Complex functions, Exponential and Sinusoidal signals, Concepts of Impulse function, Unit Step function, Signum function.

Fourier Series: Representation of Fourier series, Continuous time periodic signals, Properties of Fourier Series, Dirichlet's conditions, Trigonometric Fourier Series and Exponential Fourier Series, Complex Fourier spectrum.

UNIT-II:

Fourier Transforms and Sampling

Fourier Transforms: Deriving Fourier Transform from Fourier Series, Fourier Transform of arbitrary signal, Fourier Transform of standard signals, Fourier Transform of Periodic Signals, Properties of Fourier Transform, Fourier Transforms involving Impulse function and Signum function, Introduction to Hilbert Transform.

Sampling: Sampling theorem – Graphical and analytical proof for Band Limited Signals, Typers of Sampling - Impulse Sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, Effect of under sampling – Aliasing, Introduction to Band Pass sampling.

UNIT-III:

Signal Transmission Through Linear Systems: Linear System, Impulse response, Response of a Linear System, Linear Time Invariant (LTI) System, Linear Time Variant (LTV) System, Transfer function of a LTI system, Filter characteristics of Linear Systems, Distortion less transmission through a system, Signal bandwidth, System bandwidth, Ideal LPF, HPF and BPF characteristics, Causality and Paley-Wiener criterion for physical realization, Relationship between Bandwidth and Rise time.

UNIT-IV:

Convolution and Correlation of Signals: Concept of convolution in Time domain and Frequency domain, Graphical representation of Convolution, Convolution property of Fourier Transforms, Cross Correlation and Auto Correlation of functions, Properties of Correlation function, Energy density spectrum, Parseval's Theorem, Power density spectrum, Relation between Auto Correlation function and Energy/Power spectral density function, Relation between Convolution and Correlation, Detection of periodic signals in the presence of Noise by Correlation, Extraction of signal from noise by filtering.

UNIT-V:

Laplace Transforms and Z-Transforms

Laplace Transforms: Review of Laplace Transforms (L.T), Partial fraction expansion, Inverse Laplace Transform, Concept of Region of Convergence (ROC) for Laplace Transforms, Constraints on ROC for various classes of signals, Properties of L.T, Relation between L.T and F.T of a signal, Laplace Transform of certain signals using waveform synthesis.

Z–Transforms: Fundamental difference between Continuous and Discrete time signals, Discrete time signal representation using Complex exponential and Sinusoidal components, Periodicity of Discrete time signal using complex exponential signal, Concept of Z- Transform of a Discrete Sequence, Distinction between Laplace, Fourier and Z Transforms, Region of Convergence in Z-Transform, Constraints on ROC for various classes of signals, Inverse Z-transform, Properties of Z-transforms.

TEXT BOOKS:

1. Signals, Systems & Communications - B.P. Lathi, 2013, BSP.
2. Signals and Systems - A.V. Oppenheim, A.S. Willsky and S.H. Nawab, 2 Ed., PHI.

REFERENCE BOOKS:

1. Signals & Systems - Simon Haykin and Van Veen,Wiley, 2 Ed.
2. Signals and Signals – Iyer and K. Satya Prasad, Cengage Learning
3. Signals and Systems – A.Rama Krishna Rao – 2008, TMH.
4. Introduction to Signal and System Analysis – K.Gopalan 2009, Cengage Learning.
5. Fundamentals of Signals and Systems - Michel J. Robert, 2008, MGH International Edition.
6. Signals, Systems and Transforms - C. L. Philips, J.M.Parr and Eve A.Riskin, 3 Ed., 2004, PE.

Course Outcomes:

Upon completing this course the student will be able to:

- Represent any arbitrary signals in terms of complete sets of orthogonal functions and understands the principles of impulse functions, step function and signum function.
- Express periodic signals in terms of Fourier series and express the spectrum and express the arbitrary signal (discrete) as Fourier transform to draw the spectrum.
- Understands the principle of linear system, filter characteristics of a system and its bandwidth, the concepts of auto correlation and cross correlation and power Density Spectrum.
- Can design a system for sampling a signal.
- For a given system, response can be obtained using Laplace transform, properties and ROC of L.T.
- Study the continuous and discrete signal relation and relation between F.T., L.T. & Z.T, properties, ROC of Z Transform.

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(A30481) BASIC SIMULATION LAB

Note:

- All the experiments are to be simulated using MATLAB or equivalent software
- Minimum of 15 experiment are to be completed

List of Experiments:

1. Basic Operations on Matrices.
2. Generation of Various Signals and Sequences (Periodic and Aperiodic), such as Unit Impulse, Unit Step, Square, Saw tooth, Triangular, Sinusoidal, Ramp, Sinc.
3. Operations on Signals and Sequences such as Addition, Multiplication, Scaling, Shifting, Folding, Computation of Energy and Average Power.
4. Finding the Even and Odd parts of Signal/Sequence and Real and Imaginary parts of Signal.
5. Convolution between Signals and sequences.
6. Auto Correlation and Cross Correlation between Signals and Sequences.
7. Verification of Linearity and Time Invariance Properties of a given Continuous/Discrete System.
8. Computation of Unit sample, Unit step and Sinusoidal responses of the given LTI system and verifying its physical realiazability and stability properties.
9. Gibbs Phenomenon
10. Finding the Fourier Transform of a given signal and plotting its magnitude and phase spectrum.
11. Waveform Synthesis using Laplace Transform.
12. Locating the Zeros and Poles and plotting the Pole-Zero maps in S-plane and Z-Plane for the given transfer function.
13. Generation of Gaussian noise (Real and Complex), Computation of its mean, M.S. Value and its Skew, Kurtosis, and PSD, Probability Distribution Function.
14. Sampling Theorem Verification.
15. Removal of noise by Autocorrelation / Cross correlation.
16. Extraction of Periodic Signal masked by noise using Correlation.
17. Verification of Weiner-Khinchine Relations.
18. Checking a Random Process for Stationarity in Wide sense.

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II YEAR II SEMESTER

Code	Subject	L	T/P/D	C
A40215	Principles of Electrical Engineering	4	-	4
A40412	Electronic Circuit Analysis	4	-	4
A40415	Pulse and Digital Circuits	4	-	4
A40009	Environmental Studies	4	—	4
A40411	Electromagnetic Theory and Transmission Lines	4	-	4
A40410	Digital Design using Verilog HDL	4	-	4
A40288	Electrical Technology Lab.	-	3	2
A40484	Electronic Circuits and Pulse Circuits Lab.	-	3	2
	Total	**24**	**6**	**28**

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	4	**-/-/-**	**4**

(A40412) ELECTRONIC CIRCUIT ANALYSIS

Course Objective:

- To familiarize the student with the analysis and design of basic transistor amplifier circuits and their frequency response characteristics, feedback amplifiers, oscillators, large signal amplifiers and tuned amplifiers

UNIT -I:

Single Stage and Multi Stage Amplifiers

Single Stage Amplifiers: Classification of Amplifiers – Distortion in Amplifiers, Analysis of CE, CC, and CB Configurations with simplified Hybrid Model, Analysis of CE amplifier with Emitter Resistance and Emitter follower, Miller's Theorem and its dual, Design of Single Stage RC Coupled Amplifier using BJT.

Multi Stage Amplifiers: Analysis of Cascaded RC Coupled BJT amplifiers, Cascode Amplifier, Darlington Pair, Different Coupling Schemes used in Amplifiers - RC Coupled Amplifier, Transformer Coupled Amplifier, Direct Coupled Amplifier.

UNIT –II:

BJT Amplifiers and MOS Amplifiers

BJT Amplifiers - Frequency Response: Logarithms, Decibels, General frequency considerations, Frequency response of BJT Amplifier, Analysis at Low and High frequencies, Effect of coupling and bypass Capacitors, The Hybrid- pi (p) - Common Emitter Transistor Model, CE Short Circuit Current Gain, Current Gain with Resistive Load, Single Stage CE Transistor Amplifier Response, Gain-Bandwidth Product, Emitter follower at higher frequencies.

MOS Amplifiers [3]: Basic concepts, MOS Small signal model, Common source amplifier with Resistive load.

UNIT –III:

Feedback Amplifiers and Oscillators

Feedback Amplifiers: Concepts of Feedback, Classification of Feedback Amplifiers, General characteristics of Negative Feedback Amplifiers, Effect of Feedback on Amplifier Characteristics, Voltage Series, Voltage Shunt, Current Series and Current Shunt Feedback Configurations, Illustrative Problems.

Oscillators: Classification of Oscillators, Conditions for Oscillations, RC Phase Shift Oscillator, Generalized analysis of LC oscillators - Hartley, and

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Colpitts Oscillators, Wien-Bridge & Crystal Oscillators, Stability of Oscillators.

UNIT –IV:

Large Signal Amplifiers : Classification, Class A Large Signal Amplifiers, Transformer Coupled Class A Audio Power Amplifier, Efficiency of Class A Amplifier, Class B Amplifier, Efficiency of Class B Amplifier, Class-B Push-Pull Amplifier, Complementary Symmetry Class B Push-Pull Amplifier, Distortion in Power Amplifiers, Thermal Stability and Heat Sinks.

UNIT –V:

Tuned Amplifiers: Introduction, Q-Factor, Small Signal Tuned Amplifiers, Effect of Cascading Single Tuned Amplifiers on Bandwidth, Effect of Cascading Double Tuned Amplifiers on Bandwidth, Stagger Tuned Amplifiers, Stability of Tuned Amplifiers.

TEXT BOOKS:

1. Integrated Electronics - Jacob Millman and Christos C Halkias, 1991 Ed., 2008, TMH.
2. Electronic Devices and Circuits, B. P. Singh, Rekha Singh, Pearson, 2013.
3. Design of Analog CMOS Integrated Circuits – Behzad Razavi, 2008, TMH.

REFERENCE BOOKS:

1. ElectronicCircuit Analysis – Rashid, Cengage Learning, 2013
2. Electronic Devices and Circuit Theory - Robert L.Boylestad, Louis Nashelsky, 9 Ed., 2008 PE.
3. Microelectric Circuits – Sedra and Smith – 5 Ed., 2009, Oxford University Press.
4. Electronic Circuit Analysis – K. Lal Kishore, 2004, BSP.
5. Electronic Devices and Circuits - S. Salivahanan, N.Suresh Kumar, A Vallavaraj, 2 Ed., 2009, TMH.

Course Outcomes:

Upon completion of the subject, students will be able to:

- Design and analyse the DC bias circuitry of BJT and FET.
- Analyse the different types of amplifiers, operation and its characteristics
- Design circuits like amplifiers, oscillators using the transistors diodes and oscillators.

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(A40415) PULSE AND DIGITAL CIRCUITS

Objectives:

The main objectives are:

- To explain the complete response of R-C and R-L-C transient circuits.
- To explain clippers, clampers, switching characteristics of transistors and sampling gates.
- To construct various multivibrators using transistors, design of sweep circuits and sampling gates.
- To discuss and realize logic gates using diodes and transistors.

UNIT-I:

Linear Wave Shaping: High pass and low pass RC circuits and their response for Sinusoidal, Step, Pulse, Square, & Ramp inputs, High pass RC network as Differentiator, Low pass RC circuit as an Integrator, Attenuators and its application as a CRO Probe, RL and RLC Circuits and their response for Step Input, Ringing Circuit.

UNIT-II:

Non-Linear Wave Shaping: Diode clippers, Transistor clippers, Clipping at two independent levels, Comparators, Applications of Voltage comparators. Clamping Operation, Clamping circuit taking Source and Diode resistances into account, Clamping Circuit Theorem, Practical Clamping Circuits, Effect of Diode Characteristics on Clamping Voltage, Synchronized Clamping.

UNIT-III:

Switching Characteristics of Devices : Diode as a Switch, Piecewise Linear Diode Characteristics, Diode Switching times, Transistor as a Switch, Break down voltages, Transistor in Saturation, Temperature variation of Saturation Parameters, Transistor-switching times, Silicon-controlled-switch circuits, Sampling Gates : Basic operating principles of Sampling Gates, Unidirectional and Bi-directional Sampling Gates, Four Diode Sampling Gate, Reduction of pedestal in Gate Circuits

UNIT–IV:

Multivibrators: Analysis and Design of Bistable, Monostable, Astable Multivibrators and Schmitt trigger using Transistors, Time Base Generators: General features of a Time base Signal, Methods of Generating Time Base Waveform, Miller and Bootstrap

Time Base Generators-Basic Principles, Transistor Miller Time Base generator, Transistor Bootstrap Time Base Generator, Transistor Current Time Base Generators, Methods of Linearity improvement.

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UNIT-V:

Synchronization and Frequency Division: Pulse Synchronization of Relaxation Devices, Frequency division in Sweep Circuit, Stability of Relaxation Devices, Astable Relaxation Circuits, Monostable Relaxation Circuits, Synchronization of a Sweep Circuit with Symmetrical Signals, Sine wave frequency division with a Sweep Circuit, A Sinusoidal Divider using Regeneration and Modulation.

Realization of Logic Gates Using Diodes & Transistors: AND, OR and NOT Gates using Diodes and Transistors, DCTL, RTL, DTL, TTL and CML Logic Families and its Comparison.

TEXT BOOKS:

1. Millman's Pulse, Digital and Switching Waveforms –J. Millman, H. Taub and Mothiki S. Prakash Rao, 2 Ed., 2008, TMH.
2. Solid State Pulse Circuits –David A. Bell, 4 Ed., 2002 PHI.

REFERENCE BOOKS:

1. Pulse and Digital Circuits – A. Anand Kumar, 2005, PHI.
2. Fundamentals of Pulse and Digital Circuits- Ronald J. Tocci, 3 Ed., 2008.
3. Pulse and Digital Circuits – Motheki S. Prakash Rao, 2006, TMH.
4. Wave Generation and Shaping - L. Strauss.

Outcomes:

At the end of the course, the student will be able to:

- Understand the applications of diode as integrator, differentiator, clippers, clampler circuits..
- Learn various switching devices such as diode, transistor, SCR.
- Difference between logic gates and sampling gates
- Design mutivibrators for various applications, synchronization techniques and sweep circuits.
- Realizing logic gates using diodes and transistors.

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(A40484) ELECTRONIC CIRCUITS AND PULSE CIRCUITS LAB

List of Experiments (16 experiments to be done):

PART –I: ELCTRONIC CIRCUITS

Minimum eight experiments to be conducted:

I) Design and Simulation in Simulation Laboratory using any Simulation Software (Minimum 6 Experiments):

1. Common Emitter Amplifier
2. Common Source Amplifier
3. Two Stage RC Coupled Amplifier
4. Current shunt and Voltage Series Feedback Amplifier
5. Cascode Amplifier
6. Wien Bridge Oscillator using Transistors
7. RC Phase Shift Oscillator using Transistors
8. Class A Power Amplifier (Transformer less)
9. Class B Complementary Symmetry Amplifier
10. Common Base (BJT) / Common Gate (JFET) Amplifier.

II) Testing in the Hardware Laboratory (Minimum 2 Experiments)

1. Class A Power Amplifier (with transformer load)
2. Class C Power Amplifier
3. Single Tuned Voltage Amplifier
4. Hartley & Colpitt's Oscillators
5. Darlington Pair
6. MOS Common Source Amplifier

Equipment required for the Laboratory:

1. For software simulation of Electronic circuits
   - i) Computer Systems with latest specifications
   - ii) Connected in LAN (Optional)
   - iii) Operating system (Windows XP)
   - iv) Suitable Simulations software
2. For Hardware simulations of Electronic Circuits
   - i) Regulated Power Supply (0-30V)
   - ii) CRO's

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iii) Functions Generators

iv) Multimeters

v) Components

3. Win XP/ Linux etc.

PART –II: PULSE CIRCUITS

Minimum eight experiments to be conducted:

1. Linear Wave Shaping
   a. RC Low Pass Circuit for different time constants
   b RC High Pass Circuit for different time constants
2. Non-linear wave shaping

a. Transfer characteristics and response of Clippers:
   i) Positive and Negative Clippers
   ii) Clipping at two independent levels

b The steady state output waveform of clampers for a square wave input
   i) Positive and Negative Clampers
   ii) Clamping at reference voltage

3. Comparison Operation of Comparators
4. Switching characteristics of a transistor
5 Design a Bistable Multivibrator and draw its waveforms
6 Design an Astable Multivibrator and draw its waveforms
7. Design a Monostable Multivibrator and draw its waveforms
8. Response of Schmitt Trigger circuit for loop gain less than and greater than one
9. UJT relaxation oscillator
10. The output- voltage waveform of Boot strap sweep circuit
11. The output- voltage waveform of Miller sweep circuit

Equipment required for Laboratories:

Regulated Power Supply	- 0 – 30 V
CRO	- 0 – 20 M Hz.
Function Generators	- 0 – 1 M Hz
Components	
Multi Meters	

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(A40288) ELECTRICAL TECHNOLOGY LAB

PART –A:

1. Verification of KVL and KCL.
2. Serial and Parallel Resonance.
3. Time response of first order RC/RL network for periodic non-sinusoidal inputs – time constant and steady state error determination.
4. Two port network parameters – Z-Y Parameters, chain matrix and analytical verification.
5. Two port network parameters – ABCD and h- Parameters
6. Verification of Superposition and Reciprocity theorems.
7. Verification of maximum power transfer theorem.
8. Verification of Thevenin's and Norton's theorems.

PART –B:

1. Magnetization characteristics of D.C. Shunt generator.
2. Swinburne's Test on DC shunt machine.
3. Brake test on DC shunt motor.
4. OC & SC tests on Single-phase transformer.
5. Load Test on Single Phase Transformer.

Note: Any 12 of the above experiments are to be conducted.

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(A40215) PRINCIPLES OF ELECTRICAL ENGINEERING

Objectives:

This course introduces the basic concepts of transient analysis of the circuits, the basic two-port network parameters and the design analysis of filters and attenuators and their use in circuit theory. The emphasis of this course is laid on the basic operation of the DC machines and transformers which includes DC generators and motors, single-phase transformers.

UNIT –I:

TransientAnalysis (First and Second Order Circuits): Transient Response of RL , RC Series, RLC Circuits for DC excitations, Initial Conditions, Solution using Differential Equations approach and Laplace Transform Method.

UNIT –II:

Two Port Networks: Impedance Parameters, Admittance Parameters, Hybrid Parameters, Transmission (ABCD) Parameters, Conversion of one Parameter to another, Conditions for Reciprocity and Symmetry, Interconnection of Two Port networks in Series, Parallel and Cascaded configurations, Image Parameters, Illustrative problems.

UNIT –III:

Filters and Symmetrical Attenuators: Classification of Filters, Filter Networks, Classification of Pass band and Stop band, Characteristic Impedance in the Pass and Stop Bands, Constant-k Low Pass Filter, High Pass Filter, m-derived T-Section, Band Pass filter and Band Elimination filter, Illustrative Problems. Symmetrical Attenuators – T-Type Attenuator, p–Type Attenuator, Bridged T type Attenuator, Lattice Attenuator.

UNIT –IV:

DC Machines: Principle of Operation of DC Machines, EMF equation, Types of Generators, Magnetization and Load Characteristics of DC Generators. DC Motors, Types of DC Motors, Characteristics of DC Motors, Losses and Efficiency, Swinburne's Test, Speed Control of DC Shunt Motor, Flux and Armature Voltage control methods.

UNIT –V:

Transformers and Their Performance: Principle of Operation of Single Phase transformer, Types, Constructional Features, Phasor Diagram on No Load and Load, Equivalent Circuit, Losses and Efficiency of Transformer and Regulation, OC and SC Tests (Simple Problems). Synchros, Stepper Motors.

TEXT BOOKS:

1. Electric Circuits - A. Chakrabarhty, Dhanipat Rai & Sons.
2. Basic concepts of Electrical Engineering - PS Subramanyam, BS Publications

REFERENCE BOOKS:

1. Engineering circuit analysis - William Hayt and Jack E. Kemmerly, Mc Graw Hill Company, 7th Edition.
2. Basic Electrical Engineering - S.N. Singh, PHI.
3. Electrical Circuits - David A.Bell, Oxford University Press.
4. Electric Circuit Analysis - K.S.Suresh Kumar, Pearson Education.

Outcome:

After going through this course the student gets a thorough knowledge on transient analysis of circuits, filters, attenuators , the operation of DC machines and transformers, with which he/she can able to apply the above conceptual things to real-world problems and applications.

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III YEAR I SEMESTER

Code	Subject	L	T/P/D	C
A50217	Control Systems Engineering	4	-	4
A50516	Computer Organization and Operating Systems	4	-	4
A50418	Antennas and Wave Propagation	4	-	4
A50422	Electronic Measurements and Instrumentation	4	-	4
A50408	Analog Communications	4	-	4
A50425	Linear and Digital IC Applications	4	-	4
A50482	Analog Communications Lab.	-	3	2
A50488	IC Applications and HDL Simulation Lab.	-	3	2
	Total	**24**	**6**	**28**

III YEAR II SEMESTER

Code	Subject	L	T/P/D	C
A60010	Managerial Economics and Financial Analysis	4	-	4
A600 18 A601 17 A600 17	**Open Elective:** Human Values and Professional Ethics Disaster Management Intellectual Property Rights	4	-	4
A60420	Digital Communications	4	-	4
A60432	VLSI Design	4	-	4
A60430	Microprocessors and Microcontrollers	4	-	4
A60421	Digital Signal Processing	4	-	4
A60494	Microprocessors and Microcontrollers Lab.	-	3	2
A60493	Digital Signal Processing Lab.	-	3	2
	Total	**24**	**6**	**28**

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components and its Properties, Modeling of Noise Sources, Average Noise Bandwidth, Effective Noise Temperature, Average Noise Figures, Average Noise Figure of cascaded networks.

Noise in DSB and SSB System Noise in AM System, Noise in Angle Modulation System, Noise Triangle in Angle Modulation System, Pre-emphasis and de-emphasis

UNIT –V:

Receivers: Radio Receiver - Receiver Types - Tuned radio frequency receiver, Superhetrodyne receiver, RF section and Characteristics - Frequency changing and tracking, Intermediate frequency, AGC, FM Receiver, Comparison with AM Receiver, Amplitude limiting.

Pulse Modulation: Types of Pulse modulation, PAM (Single polarity, double polarity) PWM: Generation and demodulation of PWM, PPM, Generation and demodulation of PPM, Time Divison Multiplexing.

TEXTBOOKS:

1. Communication Systems–Simon Haykin, 2 Ed, Wiley Publications.
2. Communication Systems – B.P. Lathi, BS Publication , 2004.

REFERENCE BOOKS:

1. Electronic Communications – Dennis Roddy and John Coolean, 4th Edition,PEA, 2004
2. Electronic Communication Systems – Modulation and Transmission - Robert J. Schoenbeck, 2nd Edition, PHI.
3. Analog and Digital Communication – K. Sam Shanmugam, Willey ,2005
4. Electronics & Communication System – George Kennedy and Bernard Davis , TMH 2004.
5. Principles of Communication Systems – H Taub & D. Schilling, Gautam Sahe, TMH, 2007 , 3rd Edition

Course Outcomes:

Upon completion of the subject, students will be able to:

- Conceptually understand the baseband signal & system.
- Identify various elements, processes, and parameters in telecommunication systems, and describe their functions, effects, and interrelationship.
- Design procedure of AM Transmission & Reception, analyze, measure, and evaluate the performance of a telecommunication system against given criteria.
- Understand basic knowledge of FM Transmission & Reception
- Understand various types of SSB Transmission & Reception.
- Design typical telecommunication systems that consist of basic and essential building blocks.

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III Year B.Tech. ECE-I Sem	**L**	**T/P/D**	**C**
	4	**-/-/-**	**4**

(A50425) LINEAR AND DIGITAL IC APPLICATIONS

Course Objectives:

The main objectives of the course are:

- To introduce the basic building blocks of linear integrated circuits.
- To teach the linear and non - linear applications of operational amplifiers.
- To introduce the theory and applications of analog multipliers and PLL.
- To teach the theory of ADC and DAC.
- To introduce the concepts of waveform generation and introduce some special function ICs.
- To understand and implement the working of basic digital circuits.

UNIT -I:

Operational Amplifier: Ideal and Practical Op-Amp, Op-Amp Characteristics, DC and AC Characteristics, Features of 741 Op-Amp, Modes of Operation - Inverting, Non-Inverting, Differential, Instrumentation Amplifier, AC Amplifier, Differentiators and Integrators, Comparators, Schmitt Trigger, Introduction to Voltage Regulators, Features of 723 Regulator, Three Terminal Voltage Regulators.

UNIT -II:

Op-Amp, IC-555 & IC 565 Applications: Introduction to Active Filters, Characteristics of Band pass, Band reject and All Pass Filters, Analysis of 1st order LPF & HPF Butterworth Filters, Waveform Generators – Triangular, Sawtooth, Square Wave, IC555 Timer - Functional Diagram, Monostable and Astable Operations, Applications, IC565 PLL - Block Schematic, Description of Individual Blocks, Applications.

UNIT -III:

Data Converters : Introduction, Basic DAC techniques, Different types of DACs-Weighted resistor DAC, R-2R ladder DAC, Inverted R-2R DAC, Different Types of ADCs - Parallel Comparator Type ADC, Counter Type ADC, Successive Approximation ADC and Dual Slope ADC, DAC and ADC Specifications.

UNIT -IV:

Digital Integrated Circuits: Classification of Integrated Circuits, Comparison of Various Logic Families, CMOS Transmission Gate, IC interfacing- TTL

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(A50487) ANALOG COMMUNICATIONS LAB

Note:

Minimum 12 experiments should be conducted:

All these experiments are to be simulated first either using MATLAB, Comsim or any other simulation package and then to be realized in hardware

1. Amplitude modulation and demodulation.
2. DSB-SC Modulator & Detector
3. SSB-SC Modulator & Detector (Phase Shift Method)
4. Frequency modulation and demodulation.
5. Study of spectrum analyzer and analysis of AM and FM Signals
6. Pre-emphasis & de-emphasis.
7. Time Division Multiplexing & De multiplexing
8. Frequency Division Multiplexing & De multiplexing
9. Verification of Sampling Theorem
10. Pulse Amplitude Modulation & Demodulation
11. Pulse Width Modulation & Demodulation
12. Pulse Position Modulation & Demodulation
13. Frequency Synthesizer.
14. AGC Characteristics.
15. PLL as FM Demodulator

Equipment required for the Laboratory:

1. RPS - 0 – 30 V
2. CRO - 0 – 20 M Hz.
3. Function Generators - 0 – 1 M Hz
4. RF Generators - 0 – 1000 M Hz./0 – 100 M Hz.
5. Multimeters
6. Lab Experimental kits for Analog Communication
7. Components
8. Radio Receiver/TV Receiver Demo kits or Trainees.
9. Spectrum Analyzer - 60 M Hz.
10. Any one simulation package

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Driving CMOS & CMOS Driving TTL, Combinational Logic ICs – Specifications and Applications of TTL-74XX & CMOS 40XX Series ICs - Code Converters, Decoders, Demultiplexers, LED & LCD Decoders with Drivers , Encoders, Priority Encoders, Multiplexers, Demultiplexers, Priority Generators/Checkers, Parallel Binary Adder/Subtractor, Magnitude Comparators.

UNIT -V:

Sequential Logic IC's and Memories: Familiarity with commonly available 74XX & CMOS 40XX Series ICs – All Types of Flip-flops, Synchronous Counters, Decade Counters, Shift Registers.

Memories - ROM Architecture, Types of ROMS & Applications, RAM Architecture, Static & Dynamic RAMs.

TEXT BOOKS:

1. Op-Amps & Linear ICs – Ramakanth A. Gayakwad, PHI, 2003.
2. Linear Integrated Circuits –D. Roy Chowdhury, New Age International (p) Ltd, 2nd Ed., 2003.
3. Digital Fundamentals – Floyd and Jain, Pearson Education, 8th Edition, 2005.

REFERENCE BOOKS:

1. Op Amps and Linear Integrated Circuits-Concepts and Applications James M. Fiore, Cengage Learning/ Jaico, 2009.
2. Operational Amplifiers with Linear Integrated Circuits by K.Lal Kishore – Pearson, 2009.
3. Linear Integrated Circuits and Applications – Salivahana, TMH.
4. Modern Digital Electronics – RP Jain – 4/e – TMH, 2010.
5. Digital Design Principles and Practices – John. F. Wakerly 3/e, 2005.
6. Operational Amplifiers with Linear Integrated Circuits, 4/e William D.Stanley, Pearson Education India, 2009.

Course Outcomes:

On completion of this course, the students will have:

- A thorough understanding of operational amplifiers with linear integrated circuits.
- Understanding of the different families of digital integrated circuits and their characteristics.
- Also students will be able to design circuits using operational amplifiers for various applications.

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(A50487) ANALOG COMMUNICATIONS LAB

Note:

Minimum 12 experiments should be conducted:

All these experiments are to be simulated first either using MATLAB, Comsim or any other simulation package and then to be realized in hardware

1. Amplitude modulation and demodulation.
2. DSB-SC Modulator & Detector
3. SSB-SC Modulator & Detector (Phase Shift Method)
4. Frequency modulation and demodulation.
5. Study of spectrum analyzer and analysis of AM and FM Signals
6. Pre-emphasis & de-emphasis.
7. Time Division Multiplexing & De multiplexing
8. Frequency Division Multiplexing & De multiplexing
9. Verification of Sampling Theorem
10. Pulse Amplitude Modulation & Demodulation
11. Pulse Width Modulation & Demodulation
12. Pulse Position Modulation & Demodulation
13. Frequency Synthesizer.
14. AGC Characteristics.
15. PLL as FM Demodulator

Equipment required for the Laboratory:

1. RPS - 0 – 30 V
2. CRO - 0 – 20 M Hz.
3. Function Generators - 0 – 1 M Hz
4. RF Generators - 0 – 1000 M Hz./0 – 100 M Hz.
5. Multimeters
6. Lab Experimental kits for Analog Communication
7. Components
8. Radio Receiver/TV Receiver Demo kits or Trainees.
9. Spectrum Analyzer - 60 M Hz.
10. Any one simulation package

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(A50488) IC APPLICATIONS AND HDL SIMULATION LAB

Note: To perform any sixteen experiments (choosing at least seven from each part).

Part-I: Linear IC Experiments

1. OP AMP Applications – Adder, Subtractor, Comparators.
2. Integrator and Differentiator Circuits using IC 741.
3. Active Filter Applications – LPF, HPF (first order)
4. IC 741 Waveform Generators – Sine, Squarewave and Triangular waves.
5. IC 555 Timer – Monostable and Astable Multivibrator Circuits.
6. Schmitt Trigger Circuits – using IC 741
7. IC 565 – PLL Applications.
8. Voltage Regulator using IC 723, Three Terminal Voltage Regulators –7805, 7809, 7912.

EQUIPMENT REQUIRED:

1 20 MHz/ 40 MHz/60 MHz Oscilloscope.
2 1 MHz Function Generator (Sine, Square, Triangular and TTL).
3 Regulated Power Supply.
4 Multimeter / Volt Meter.

Part – II: HDL Simulation programs:

Programming can be done using any complier. Down load the programs on FPGA/CPLD boards and performance testing may be done using pattern generator/logic analyzer apart from verification by simulation using Cadence / Mentor Graphics / Synopsys /Equivalentfront end CAD tools.

1 HDL code to realize all the logic gates
2 Design of 2-to-4 decoder
3 Design of 8-to-3 encoder (without and with Priority)
4 Design of 8-to-1 multiplexer and 1x8 demultiplexer.
5 Design of 4 bit binary to gray code converter
6 Design of 4 bit comparator
7 Design of Full adder using 3 modeling styles
8 Design of flip flops: SR, D, JK, T
9 Design of 4-bit binary, BCD counters (synchronous/ asynchronous reset)
10 Finite State Machine Design

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2. Kenneth. J. Ayala, The 8051 Microcontroller , 3rd Ed., Cengage Learning.

REFERENCE BOOKS:

1. Advanced Microprocessors and Peripherals – A. K. Ray and K.M. Bhurchandani, TMH, 2nd Edition 2006.
2. The 8051Microcontrollers, Architecture and Programming and Applications -K.Uma Rao, Andhe Pallavi, Pearson, 2009.
3. Micro Computer System 8086/8088 FamilyArchitecture, Programming and Design - Liu and GA Gibson, PHI, 2nd Ed.
4. Microcontrollers and Application - Ajay. V. Deshmukh, TMGH, 2005.
5. The 8085 Microprocessor: Architecture, programming and Interfacing – K.Uday Kumar, B.S.Umashankar, 2008, Pearson

Course Outcome:

Upon completion of the course:

- The student will learn the internal organization of popular 8086/8051 microprocessors/microcontrollers.
- The student will learn hardware and software interaction and integration.
- The students will learn the design of microprocessors/ microcontrollers-based systems.

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III Year B.Tech. ECE-II Sem	**L**	**T/P/D**	**C**
	4	**-/-/-**	**4**

(A60421) DIGITAL SIGNAL PROCESSING

Objectives:

This course is an essential course that provides design techniques for processing all type of signals in various fields. The main objectives are:

- To provide background and fundamental material for the analysis and processing of digital signals.
- To familiarize the relationships between continuous-time and discrete-time signals and systems.
- To study fundamentals of time, frequency and Z-plane analysis and to discuss the inter-relationships of these analytic method.
- To study the designs and structures of digital (IIR and FIR) filters from analysis to synthesis for a given specifications.
- The impetus is to introduce a few real-world signal processing applications.
- To acquaint in FFT algorithms, Multi-rate signal processing techniques and finite word length effects.

UNIT -I:

Introduction: Introduction to Digital Signal Processing: Discrete Time Signals & Sequences, Linear Shift Invariant Systems, Stability, and Causality, Linear Constant Coefficient Difference Equations, Frequency Domain Representation of Discrete Time Signals and Systems

Realization of Digital Filters: Applications of Z – Transforms, Solution of Difference Equations of Digital Filters, System Function, Stability Criterion, Frequency Response of Stable Systems, Realization of Digital Filters – Direct, Canonic, Cascade and Parallel Forms.

UNIT -II:

Discrete Fourier series: DFS Representation of Periodic Sequences, Properties of Discrete Fourier Series, Discrete Fourier Transforms: Properties of DFT, Linear Convolution of Sequences using DFT, Computation of DFT: Over-Lap Add Method, Over-Lap Save Method, Relation between DTFT, DFS, DFT and Z-Transform.

Fast Fourier Transforms: Fast Fourier Transforms (FFT) - Radix-2 Decimation-in-Time and Decimation-in-Frequency FFT Algorithms, Inverse FFT, and FFT with General Radix-N.

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III Year B.Tech. ECE-II Sem	L	T/P/D	C
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(A60494) MICROPROCESSORS AND MICROCONTROLLERS LAB

Note: Minimum of 12 experiments are to be conducted.

The Following programs/experiments are to be written for assembler and to be executed the same with 8086 and 8051 kits.

List of Experiments:

1 Programs for 16 bit arithmetic operations for 8086 (using Various Addressing Modes).
2 Program for sorting an array for 8086.
3 Program for searching for a number or character in a string for 8086.
4 Program for string manipulations for 8086.
5 Program for digital clock design using 8086.
6 Interfacing ADC and DAC to 8086.
7 Parallel communication between two microprocessors using 8255.
8 Serial communication between two microprocessor kits using 8251.
9 Interfacing to 8086 and programming to control stepper motor.
10 Programming using arithmetic, logical and bit manipulation instructions of 8051.
11 Program and verify Timer/ Counter in 8051.
12 Program and verify Interrupt handling in 8051
13 UART Operation in 8051.
14 Communication between 8051 kit and PC.
15 Interfacing LCD to 8051.
16 Interfacing Matrix/ Keyboard to 8051.
17 Data Transfer from Peripheral to Memory through DMA controller 8237 / 8257.

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UNIT-III:

IIR Digital Filters: Analog filter approximations – Butterworth and Chebyshev, Design of IIR Digital Filters from Analog Filters, Step and Impulse Invariant Techniques, Bilinear Transformation Method, Spectral Transformations.

UNIT-IV:

FIR Digital Filters: Characteristics of FIR Digital Filters, Frequency Response, Design of FIR Filters: Fourier Method, Digital Filters using Window Techniques, Frequency Sampling Technique, Comparison of IIR & FIR filters.

UNIT-V:

Multirate Digital Signal Processing: Introduction, Down Sampling, Decimation, Upsampling, Interpolation, Sampling Rate Conversion.

Finite Word Length Effects: Limit cycles, Overflow Oscillations, Round-off Noise in IIR Digital Filters, Computational Output Round Off Noise, Methods to Prevent Overflow, Trade Off Between Round Off and Overflow Noise, Dead Band Effects.

TEXT BOOKS:

1. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G. Manolakis, Pearson Education / PHI, 2007.
2. Discrete Time Signal Processing – A. V. Oppenheim and R.W. Schaffer, PHI, 2009
3. Fundamentals of Digital Signal Processing – Loney Ludeman, John Wiley, 2009

REFERENCE BOOKS:

1. Digital Signal Processing – Fundamentals and Applications – Li Tan, Elsevier, 2008
2. Fundamentals of Digital Signal Processing using MATLAB – Robert J. Schilling, Sandra L. Harris, Thomson, 2007
3. Digital Signal Processing – S.Salivahanan, A.Vallavaraj and C.Gnanapriya, TMH, 2009
4. Discrete Systems and Digital Signal Processing with MATLAB – Taan S. ElAli, CRC press, 2009.
5. Digital Signal Processing - A Practical approach, Emmanuel C. Ifeachor and Barrie W. Jervis, 2nd Edition, Pearson Education, 2009.
6. Digital Signal Processing - Nagoor Khani, TMG, 2012

Course Outcomes:

On completion of this subject, the student should be able to:

- Perform time, frequency and Z -transform analysis on signals and systems.

- Understand the inter-relationship between DFT and various transforms.
- Understand the significance of various filter structures and effects of roundoff errors.
- Design a digital filter for a given specification.
- Understand the fast computation of DFT and appreciate the FFT processing.
- Understand the tradeoffs between normal and multi rate DSP techniques and finite length word effects.

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	L	T/P/D	C
III Year B.Tech. ECE-II Sem	**L**	**T/P/D**	**C**
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(A60494) MICROPROCESSORS AND MICROCONTROLLERS LAB

Note: Minimum of 12 experiments are to be conducted.

The Following programs/experiments are to be written for assembler and to be executed the same with 8086 and 8051 kits.

List of Experiments:

1 Programs for 16 bit arithmetic operations for 8086 (using Various Addressing Modes).
2 Program for sorting an array for 8086.
3 Program for searching for a number or character in a string for 8086.
4 Program for string manipulations for 8086.
5 Program for digital clock design using 8086.
6 Interfacing ADC and DAC to 8086.
7 Parallel communication between two microprocessors using 8255.
8 Serial communication between two microprocessor kits using 8251.
9 Interfacing to 8086 and programming to control stepper motor.
10 Programming using arithmetic, logical and bit manipulation instructions of 8051.
11 Program and verify Timer/ Counter in 8051.
12 Program and verify Interrupt handling in 8051
13 UART Operation in 8051.
14 Communication between 8051 kit and PC.
15 Interfacing LCD to 8051.
16 Interfacing Matrix/ Keyboard to 8051.
17 Data Transfer from Peripheral to Memory through DMA controller 8237 / 8257.

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	L	T/P/D	C
III Year B.Tech. ECE-II Sem	-	-/3/-	2

(A60493) DIGITAL SIGNAL PROCESSING LAB

Note:

- Minimum of 12 experiments are to be conducted.
- The programs shall be implemented in software (Using MATLAB / Lab view / C programming/OCTAVE Equivalent) and hardware (Using TI / Analog devices / Motorola / Equivalent DSP processors).

List of Experiments:

1 Generation of Sinusoidal waveform / signal based on recursive difference equations
2 To find DFT / IDFT of given DT signal
3 To find frequency response of a given system given in (Transfer Function/ Differential equation form).
4 Implementation of FFT of given sequence
5 Determination of Power Spectrum of a given signal(s).
6 Implementation of LP FIR filter for a given sequence
7 Implementation of HP FIR filter for a given sequence
8 Implementation of LP IIR filter for a given sequence
9 Implementation of HP IIR filter for a given sequence
10 Generation of Sinusoidal signal through filtering
11 Generation of DTMF signals
12 Implementation of Decimation Process
13 Implementation of Interpolation Process
14 Implementation of I/D sampling rate converters
15 Audio application such as to plot a time and frequency display of microphone plus a cosine using DSP. Read a .wav file and match with their respective spectrograms.
16 Noise removal: Add noise above 3 KHz and then remove, interference suppression using 400 Hz tone.
17 Impulse response of first order and second order systems.

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IVYEAR I SEMESTER

Code	Subject	L	T/P/D	C
A70014	Management Science	4	-	4
A70442	Microwave Engineering	4	-	4
A70515	Computer Networks	4	-	4
A70434	Cellular and Mobile Communications	4	-	4
	Elective -I:	4	-	4
A70436	Digital Image Processing			
A70443	Multimedia and Signal Coding			
A70505	Object Oriented Programming through Java			
	Elective -II:	4	-	4
A70447	Television Engineering			
A70444	Optical Communications			
A70440	Embedded Systems Design			
A70086	Advanced Communication Skills Lab.	-	3	2
A70499	Microwave Engineering and Digital Communications Lab	-	3	2
	Total	**24**	**6**	**28**

IV YEAR II SEMESTER

Code	Subject	L	T/P/D	C
	Elective -III:	4	-	4
A80452	Satellite Communications			
A81102	Biomédical Instrumentation			
A80527	Artificial Neural Networks			
	Elective -IV:	4	-	4
A80431	Telecommunication Switching Systems and Networks			
A80450	Radar Systems			
A80449	Network Security			
	Elective -V:	4	-	4
A80454	Wireless Communications and Networks			
A80437	Digital Signal Processors and Architectures			
A80451	RF Circuit Design			
A80087	Industry Oriented Mini Project	-	-	2
A80089	Seminar	-	6	2
A80088	Major Project Work	-	15	10
A80090	Comprehensive Viva	-	-	2
	Total	**12**	**21**	**28**

Note: All End Examinations (Theory and Practical) are of three hours duration.
T-Tutorial L – Theory P – Practical D-Drawing C – Credits

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	L	T/P/D	C
IV Year B.Tech. ECE-I Sem	**4**	**-/-/-**	**4**

(A70442) MICROWAVE ENGINEERING

Course Objectives:

The objectives of the course are:

- To develop the knowledge on transmission lines for microwaves, cavity resonators and wave guide components and applications.
- To enable the students understand and analyze the operation of Microwave tubes like klystron, magnetron, travelling wave tube, etc.,
- To familiarize with microwave solid state devices.
- To understand the scattering matrix parameters and its use.
- To introduce the student the microwave test bench for measure different parameters like attenuation, VSWR, etc.,

UNIT-I:

Microwave Transmission Lines - I: Introduction, Microwave Spectrum and Bands, Applications of Microwaves. Rectangular Waveguides – Solution of Wave Equations in Rectangular Coordinates, TE/TM mode analysis, Expressions for Fields, Characteristic Equation and Cut-off Frequencies, Filter Characteristics, Dominant and Degenerate Modes, Sketches of TE and TM mode fields in the cross-section, Mode Characteristics – Phase and Group Velocities, Wavelengths and Impedance Relations, Illustrative Problems.

Rectangular Guides: Power Transmission and Power Losses, Impossibility of TEM Mode, Micro strip Lines– Introduction, Zo Relations, Effective Dielectric Constant, Losses, Q factor.

UNIT-II:

Cavity Resonators– Introduction, Rectangular Cavities, Dominant Modes and Resonant Frequencies, Q Factor and Coupling Coefficients, Illustrative Problems

Waveguide Components and Applications: Coupling Mechanisms – Probe, Loop, Aperture types. Waveguide Discontinuities – Waveguide Windows, Tuning Screws and Posts, Matched Loads. Waveguide Attenuators – Different Types, Resistive Card and Rotary Vane Attenuators; Waveguide Phase Shifters – Types, Dielectric and Rotary Vane Phase Shifters, Waveguide Multiport Junctions – E plane and H plane Tees, Magic Tee. Directional Couplers – 2 Hole, Bethe Hole types, Illustrative Problems

Ferrites– Composition and Characteristics, Faraday Rotation, Ferrite

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Components – Gyrator, Isolator, Circulator.

UNIT-III:

Microwave Tubes: Limitations and Losses of conventional Tubes at Microwave Frequencies, Microwave Tubes – O Type and M Type Classifications, O-type Tubes : 2 Cavity Klystrons – Structure, Reentrant Cavities, Velocity Modulation Process and Applegate Diagram, Bunching Process and Small Signal Theory – Expressions for O/P Power and Efficiency. Reflex Klystrons – Structure, Velocity Modulation and Applegate Diagram, Mathematical Theory of Bunching, Power Output, Efficiency, Oscillating Modes and O/P Characteristics, Effect of Repeller Voltage on Power O/P, Illustrative Problems.

Helix TTS: Significance, Types and Characteristics of Slow Wave Structures; Structure of TW T and Amplification Process (qualitative treatment), Suppression of Oscillations, Gain Considerations.

UNIT-IV:

M-Type Tubes:

Introduction, Cross-field Effects, Magnetrons – Different Types, Cylindrical Traveling Wave Magnetron – Hull Cut-off and Hartree Conditions, Modes of Resonance and PI-Mode Operation, Separation of PI-Mode, o/p characteristics, Illustrative Problems

Microwave Solid State Devices: Introduction, Classification, Applications. TEDs – Introduction, Gunn Diodes – Principle, RWH Theory, Characteristics, Basic Modes of Operation - Gunn Oscillation Modes, LSA Mode, Introduction to Avalanche Transit Time Devices.

UNIT-V:

Microwave Measurements: Scattering Matrix– Significance, Formulation and Properties, S Matrix Calculations for – 2 port Junctions, E plane and H plane Tees, Magic Tee, Circulator and Isolator, Illustrative Problems.

Description of Microwave Bench – Different Blocks and their Features, Errors and Precautions, Microwave Power Measurement, Bolometers Measurement of Attenuation, Frequency Standing Wave Measurements – Measurement of Low and High VSWR, Cavity Q, Impedance Measurements.

TEXT BOOKS:

1. Microwave Devices and Circuits – Samuel Y. Liao, Pearson, 3rd Edition, 2003.
2. Microwave Principles – Herbert J. Reich, J.G. Skalnik, P.F. Ordung and H.L. Krauss, CBS Publishers and Distributors, New Delhi, 2004.

REFERENCE BOOKS:

1. Foundations for Microwave Engineering – R.E. Collin, IEEE Press, John Wiley, 2ndEdition, 2002.

2. Microwave Circuits and Passive Devices – M.L. Sisodia and G.S.Raghuvanshi, W iley Eastern Ltd.,New Age International Publishers Ltd., 1995.

3. Microwave Engineering Passive Circuits – Peter A. Rizzi, PHI, 1999.

4. Electronic and Radio Engineering – F.E. Terman, McGraw-Hill, 4th Ed., 1955.

5. Microwave Engineering – A. Das and S.K. Das, TMH, 2nd Ed., 2009.

6. Microwave Engineering - G. S. Raghuvanshi and K. Satya Prasad, Cengage Learning, 2012.

Course Outcomes:

Upon completion of the course, the students will be able to:

- Understand the significance of microwaves and microwave transmission lines.
- Analyze the characteristics of microwave tubes and compare them.
- Be able to list and explain the various microwave solid state devices.
- Can set up a microwave bench for measuring microwave parameters.

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IV Year B.Tech. ECE-I Sem	L	T/P/D	C
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(A70499) MICROWAVE ENGINEERING AND DIGITAL COMMUNICATIONS LAB

Note: Minimum 12 Experiments to be conducted

Part – A: Microwave Engineering Lab (Any 6 Experiments):

1. Reflex Klystron Characteristics
2. Gunn Diode Characteristics
3. Directional Coupler Characteristics
4. VSWR Measurement
5. Measurement of Waveguide Parameters
6. Measurement of Impedance of a given Load
7. Measurement of Scattering parameters of a Magic Tee
8. Measurement of Scattering parameters of a Circulator
9. Attenuation Measurement
10. Microwave Frequency Measurement

Part – B: Digital Communication Lab (Any 6 Experiments):

1. PCM Generation and Detection
2. Differential Pulse Code Modulation
3. Delta Modulation
4. Time Division Multiplexing of 2 Band Limited Signals
5. Frequency shift keying: Generation and Detection
6. Phase Shift Keying: Generation and Detection
7. Amplitude Shift Keying: Generation and Detection
8. Study of the spectral characteristics of PAM, QAM
9. DPSK :Generation and Detection
10. QPSK : Generation and Detection

Equipment required for the Laboratory:

Microwave Engineering Lab:

1. Microwave Bench set up with Klystron Power Supply
2. Microwave Bench set up with Gunn Power Supply
3. Micro Ammeter
4. VSWR meter

5. Microwave Components

Digital Communication Lab:

1. RPS: 0-30V
2. CRO: 0-20MHz
3. Function Generators: 0-1MHz
4. RF Generators: 0-100MHz
5. Experimental Kits /Modules

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Building Vocabulary - Starting a conversation – responding appropriately and relevantly – using the right body language – Role Play in different situations & Discourse Skills- using visuals - Synonyms and antonyms, word roots, one-word substitutes, prefixes and suffixes, study of word origin, business vocabulary, analogy, idioms and phrases, collocations & usage of vocabulary.

2. Activities on Reading Comprehension –General Vs Local comprehension, reading for facts, guessing meanings from context, scanning, skimming, inferring meaning, critical reading & effective googling.
3. Activities on Writing Skills – Structure and presentation of different types of writing – letter writing/Resume writing/ e-correspondence/ Technical report writing/ Portfolio writing – planning for writing – improving one's writing.
4. Activities on Presentation Skills – Oral presentations (individual and group) through JAM sessions/seminars/PPTs and written presentations through posters/projects/reports/ e-mails/assignments etc.
5. Activities on Group Discussion and Interview Skills – Dynamics of group discussion, intervention, summarizing, modulation of voice, body language, relevance, fluency and organization of ideas and rubrics for evaluation- Concept and process, pre-interview planning, opening strategies, answering strategies, interview through tele-conference & video-conference and Mock Interviews.

Minimum Requirement:

The Advanced Communication Skills (ACS) Laboratory shall have the following infra-structural facilities to accommodate at least 35 students in the lab:

- Spacious room with appropriate acoustics.
- Round Tables with movable chairs
- Audio-visual aids
- LCD Projector
- Public Address system
- P – IV Processor, Hard Disk – 80 GB, RAM–512 MB Minimum, Speed – 2.8 GHZ
- T. V, a digital stereo & Camcorder
- Headphones of High quality

Prescribed Lab Manual: A book titled A Course Book of Advanced Communication Skills (ACS) Lab published by Universities Press, Hyderabad.

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ELECTRONICS AND COMMUNICATION ENGINEERING

2018-2019

S. No	Regulations	Number of Courses	Year of Study
1	R18	10	I year I & II Semesters
2	R16	23	II and III year I & II Semesters
3	R15	07	IV year I & II Semesters

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
B.Tech. in ELECTRONICS AND COMMUNICATION ENGINEERING
COURSE STRUCTURE & SYLLABUS (R18)

Applicable From 2018-19 Admitted Batch

I YEAR I SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA101BS	Mathematics - I	3	1	0	4
2	AP102BS	Applied Physics	3	1	0	4
3	CS103ES	Programming for Problem Solving	3	1	0	4
4	ME104ES	Engineering Graphics	1	0	4	3
5	AP105BS	Applied Physics Lab	0	0	3	1.5
6	CS106ES	Programming for Problem Solving Lab	0	0	3	1.5
7	*MC109ES	Environmental Science	3	0	0	0
		Induction Programme				
		Total Credits	**13**	**3**	**10**	**18**

I YEAR II SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA201BS	Mathematics - II	3	1	0	4
2	CH202BS	Chemistry	3	1	0	4
3	EE203ES	Basic Electrical Engineering	3	0	0	3
4	ME205ES	Engineering Workshop	1	0	3	2.5
5	EN205HS	English	2	0	0	2
6	CH206BS	Engineering Chemistry Lab	0	0	3	1.5
7	EN207HS	English Language and Communication Skills Lab	0	0	2	1
8	EE208ES	Basic Electrical Engineering Lab	0	0	2	1
		Total Credits	**12**	**2**	**10**	**19**

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AP102BS/AP202BS: APPLIED PHYSICS

B.Tech. I Year I Sem.

L	T	P	C
3	1	0	4

Course Objectives:

- Students will demonstrate skills in scientific inquiry, problem solving and laboratory techniques.
- Students will be able to demonstrate competency and understanding of the concepts found in Quantum Mechanics, Fiber optics and lasers, Semiconductor physics and Electromagnetic theory and a broad base of knowledge in physics.
- The graduates will be able to solve non-traditional problems that potentially draw on knowledge in multiple areas of physics.
- To study applications in engineering like memory devices, transformer core and electromagnetic machinery.

Course Outcomes: Upon graduation:

- The student would be able to learn the fundamental concepts on Quantum behaviour of matter in its micro state.
- The knowledge of fundamentals of Semiconductor physics, Optoelectronics, Lasers and fibre optics enable the students to apply to various systems like communications, solar cell, photo cells and so on.
- Design, characterization and study of properties of material help the students to prepare new materials for various engineering applications.
- The course also helps the students to be exposed to the phenomena of electromagnetism and also to have exposure on magnetic materials and dielectric materials.

UNIT-I: Quantum Mechanics
Introduction to quantum physics, Black body radiation, Planck's law, Photoelectric effect, Compton effect, de-Broglie's hypothesis, Wave-particle duality, Davisson and Germer experiment, Heisenberg's Uncertainty principle, Born's interpretation of the wave function, Schrodinger's time independent wave equation, Particle in one dimensional box.

UNIT-II: Semiconductor Physics
Intrinsic and Extrinsic semiconductors, Dependence of Fermi level on carrier-concentration and temperature, Carrier generation and recombination, Carrier transport: diffusion and drift, Hall effect, p- n junction diode, Zener diode and their V-I Characteristics, Bipolar Junction Transistor (BJT): Construction, Principle of operation.

UNIT-III: Optoelectronics
Radiative and non-radiative recombination mechanisms in semiconductors, LED and semiconductor lasers: Device structure, Materials, Characteristics and figures of merit, Semiconductor photodetectors: Solar cell, PIN and Avalanche and their structure, Materials, working principle and Characteristics.

UNIT-IV: Lasers and Fibre Optics
Lasers: Introduction to interaction of radiation with matter, Coherence, Principle and working of Laser, Population inversion, Pumping, Types of Lasers: Ruby laser, Carbon dioxide (CO_2) laser, He-Ne laser, Applications of laser. Fibre Optics: Introduction, Optical fibre as a dielectric wave guide, Total internal reflection, Acceptance angle, Acceptance cone and Numerical aperture, Step and Graded index fibres, Losses associated with optical fibres, Applications of optical fibres.

UNIT-V: Electromagnetism and Magnetic Properties of Materials
Laws of electrostatics, Electric current and the continuity equation, Ampere's and Faraday's laws, Maxwell's equations, Polarisation, Permittivity and Dielectric constant, Internal fields in a solid, Clausius-Mossotti equation, Ferroelectrics and Piezoelectrics. Magnetisation, permeability and susceptibility, Classification of magnetic materials, Ferromagnetism and ferromagnetic domains, Hysteresis, Applications of magnetic materials.

TEXT BOOKS:

1. Engineering Physics, B.K. Pandey, S. Chaturvedi - Cengage Learing.
2. Halliday and Resnick, Physics - Wiley.
3. A textbook of Engineering Physics, Dr. M. N. Avadhanulu, Dr. P.G. Kshirsagar - S. Chand

REFERENCE BOOKS:

1. Richard Robinett, Quantum Mechanics
2. J. Singh, Semiconductor Optoelectronics: Physics and Technology, Mc Graw-Hill inc. (1995).
3. Online Course: "Optoelectronic Materials and Devices" by Monica Katiyar and Deepak Guptha on NPTEL

AP105BS/AP205BS: APPLIED PHYSICS LAB

B.Tech. I Year I Sem.

L	T	P	C
0	0	3	1.5

List of Experiments:

1. Energy gap of P-N junction diode:
   To determine the energy gap of a semiconductor diode.
2. Solar Cell:
   To study the V-I Characteristics of solar cell.
3. Light emitting diode:
   Plot V-I and P-I characteristics of light emitting diode.
4. Stewart – Gee's experiment:
   Determination of magnetic field along the axis of a current carrying coil.
5. Hall effect:
   To determine Hall co-efficient of a given semiconductor.
6. Photoelectric effect:
   To determine work function of a given material.
7. LASER:
   To study the characteristics of LASER sources.
8. Optical fibre:
   To determine the bending losses of Optical fibres.
9. LCR Circuit:
   To determine the Quality factor of LCR Circuit.
10. R-C Circuit:
    To determine the time constant of R-C circuit.

Note: Any 8 experiments are to be performed

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CS103ES/CS203ES: PROGRAMMING FOR PROBLEM SOLVING

B.Tech. I Year I Sem.

L	T	P	C
3	1	0	4

Course Objectives:
- To learn the fundamentals of computers.
- To understand the various steps in program development.
- To learn the syntax and semantics of C programming language.
- To learn the usage of structured programming approach in solving problems.

Course Outcomes: The student will learn
- To write algorithms and to draw flowcharts for solving problems.
- To convert the algorithms/flowcharts to C programs.
- To code and test a given logic in C programming language.
- To decompose a problem into functions and to develop modular reusable code.
- To use arrays, pointers, strings and structures to write C programs.
- Searching and sorting problems.

UNIT - I: Introduction to Programming
Introduction to components of a computer system: disks, primary and secondary memory, processor, operating system, compilers, creating, compiling and executing a program etc., Number systems Introduction to Algorithms: steps to solve logical and numerical problems. Representation of Algorithm, Flowchart/Pseudo code with examples, Program design and structured programming Introduction to C Programming Language: variables (with data types and space requirements), Syntax and Logical Errors in compilation, object and executable code , Operators, expressions and precedence, Expression evaluation, Storage classes (auto, extern, static and register), type conversion, The main method and command line arguments
Bitwise operations: Bitwise AND, OR, XOR and NOT operators
Conditional Branching and Loops: Writing and evaluation of conditionals and consequent branching with if, if-else, switch-case, ternary operator, goto, Iteration with for, while, do-while loops
I/O: Simple input and output with scanf and printf, formatted I/O, Introduction to stdin, stdout and stderr. Command line arguments

UNIT - II: Arrays, Strings, Structures and Pointers:
Arrays: one and two dimensional arrays, creating, accessing and manipulating elements of arrays Strings: Introduction to strings, handling strings as array of characters, basic string functions available in C (strlen, strcat, strcpy, strstr etc.), arrays of strings
Structures: Defining structures, initializing structures, unions, Array of structures
Pointers: Idea of pointers, Defining pointers, Pointers to Arrays and Structures, Use of Pointers in self-referential structures, usage of self referential structures in linked list (no implementation) Enumeration data type

UNIT - III: Preprocessor and File handling in C:
Preprocessor: Commonly used Preprocessor commands like include, define, undef, if, ifdef, ifndef Files: Text and Binary files, Creating and Reading and writing text and binary files, Appending data to existing files, Writing and reading structures using binary files, Random access using fseek, ftell and rewind functions.

UNIT - IV: Function and Dynamic Memory Allocation:
Functions: Designing structured programs, Declaring a function, Signature of a function, Parameters and return type of a function, passing parameters to functions, call by value, Passing arrays to functions, passing pointers to functions, idea of call by reference, Some C standard functions and libraries
Recursion: Simple programs, such as Finding Factorial, Fibonacci series etc., Limitations of Recursive functions
Dynamic memory allocation: Allocating and freeing memory, Allocating memory for arrays of different data types.

UNIT - V: Introduction to Algorithms:
Algorithms for finding roots of a quadratic equations, finding minimum and maximum numbers of a given set, finding if a number is prime number, etc.
Basic searching in an array of elements (linear and binary search techniques),
Basic algorithms to sort array of elements (Bubble, Insertion and Selection sort algorithms), Basic concept of order of complexity through the example programs

CS106ES/CS206ES: PROGRAMMING FOR PROBLEM SOLVING LAB

B.Tech. I Year I Sem.

L	T	P	C
0	0	3	1.5

[Note:The programs may be executed using any available Open Source/ Freely available IDE
Some of the Tools available are:
CodeLite: https://codelite.org/
Code::Blocks: http://www.codeblocks.org/
DevCpp : http://www.bloodshed.net/devcpp.html
Eclipse: http://www.eclipse.org
This list is not exhaustive and is NOT in any order of preference]

Course Objectives: The students will learn the following:

- To work with an IDE to create, edit, compile, run and debug programs
- To analyze the various steps in program development.
- To develop programs to solve basic problems by understanding basic concepts in C like operators, control statements etc.
- To develop modular, reusable and readable C Programs using the concepts like functions, arrays etc.
- To Write programs using the Dynamic Memory Allocation concept.
- To create, read from and write to text and binary files

Course Outcomes: The candidate is expected to be able to:

- formulate the algorithms for simple problems
- translate given algorithms to a working and correct program
- correct syntax errors as reported by the compilers
- identify and correct logical errors encountered during execution
- represent and manipulate data with arrays, strings and structures
- use pointers of different types
- create, read and write to and from simple text and binary files
- modularize the code with functions so that they can be reused

Practice sessions:

a. Write a simple program that prints the results of all the operators available in C (including pre/ post increment, bitwise and/or/not, etc.). Read required operand values from standard input.
b. Write a simple program that converts one given data type to another using auto conversion and casting. Take the values form standard input.

Simple numeric problems:

a. Write a program for fiend the max and min from the three numbers.
b. Write the program for the simple, compound interest.
c. Write program that declares Class awarded for a given percentage of marks, where mark <40%= Failed, 40% to <60% = Second class, 60% to <70%=First class, >= 70% = Distinction. Read percentage from standard input.
d. Write a program that prints a multiplication table for a given number and the number of rows in the table. For example, for a number 5 and rows = 3, the output should be:
e. 5 x 1 = 5
f. 5 x 2 = 10
g. 5 x 3 = 15
h. Write a program that shows the binary equivalent of a given positive number between 0 to 255.

Expression Evaluation:

a. A building has 10 floors with a floor height of 3 meters each. A ball is dropped from the top of the building. Find the time taken by the ball to reach each floor. (Use the formula s = ut+(1/2)at^2 where u and a are the initial velocity in m/sec (= 0) and acceleration in m/sec^2 (= 9.8 m/s^2)).
b. Write a C program, which takes two integer operands and one operator from the user, performs the operation and then prints the result. (Consider the operators +,-,*, /, % and use Switch Statement)

c. Write a program that finds if a given number is a prime number
d. Write a C program to find the sum of individual digits of a positive integer and test given number is palindrome.
e. A Fibonacci sequence is defined as follows: the first and second terms in the sequence are 0 and 1. Subsequent terms are found by adding the preceding two terms in the sequence. Write a C program to generate the first n terms of the sequence.
f. Write a C program to generate all the prime numbers between 1 and n, where n is a value supplied by the user.
g. Write a C program to find the roots of a Quadratic equation.
h. Write a C program to calculate the following, where x is a fractional value.
i. 1-x/2 +x^2/4-x^3/6
j. Write a C program to read in two numbers, x and n, and then compute the sum of this geometric progression: 1+x+x^2+x^3+.............+x^n. For example: if n is 3 and x is 5, then the program computes 1+5+25+125.

Arrays and Pointers and Functions:

a. Write a C program to find the minimum, maximum and average in an array of integers.
b. Write a functions to compute mean, variance, Standard Deviation, sorting of n elements in single dimension array.
c. Write a C program that uses functions to perform the following:
d. Addition of Two Matrices
e. ii. Multiplication of Two Matrices
f. iii. Transpose of a matrix with memory dynamically allocated for the new matrix as row and column counts may not be same.
g. Write C programs that use both recursive and non-recursive functions
h. To find the factorial of a given integer.
i. ii. To find the GCD (greatest common divisor) of two given integers.
j. iii. To find x^n
k. Write a program for reading elements using pointer into array and display the values using array.
l. Write a program for display values reverse order from array using pointer.
m. Write a program through pointer variable to sum of n elements from array.

Files:

a. Write a C program to display the contents of a file to standard output device.
b. Write a C program which copies one file to another, replacing all lowercase characters with their uppercase equivalents.
c. Write a C program to count the number of times a character occurs in a text file. The file name and the character are supplied as command line arguments.
d. Write a C program that does the following:
It should first create a binary file and store 10 integers, where the file name and 10 values are given in the command line. (hint: convert the strings using atoi function)
Now the program asks for an index and a value from the user and the value at that index should be changed to the new value in the file. (hint: use fseek function)
The program should then read all 10 values and print them back.
e. Write a C program to merge two files into a third file (i.e., the contents of the firs t file followed by those of the second are put in the third file).

Strings:

a. Write a C program to convert a Roman numeral ranging from I to L to its decimal equivalent.
b. Write a C program that converts a number ranging from 1 to 50 to Roman equivalent
c. Write a C program that uses functions to perform the following operations:
d. To insert a sub-string in to a given main string from a given position.
e. ii. To delete n Characters from a given position in a given string.
f. Write a C program to determine if the given string is a palindrome or not (Spelled same in both directions with or without a meaning like madam, civic, noon, abcba, etc.)
g. Write a C program that displays the position of a character ch in the string S or – 1 if S doesn't contain ch.
h. Write a C program to count the lines, words and characters in a given text.

Miscellaneous:

a. Write a menu driven C program that allows a user to enter n numbers and then choose between finding the smallest, largest, sum, or average. The menu and all the choices are to be functions. Use a switch statement to determine what action to take. Display an error message if an invalid choice is entered.

b. Write a C program to construct a pyramid of numbers as follows:

1	*	1	1	*
1 2	* *	2 3	2 2	* *
1 2 3	* * *	4 5 6	3 3 3	* * *
			4 4 4 4	* *
				*

Sorting and Searching:

a. Write a C program that uses non recursive function to search for a Key value in a given
b. list of integers using linear search method.
c. Write a C program that uses non recursive function to search for a Key value in a given
d. sorted list of integers using binary search method.
e. Write a C program that implements the Bubble sort method to sort a given list of
f. integers in ascending order.
g. Write a C program that sorts the given array of integers using selection sort in descending order
h. Write a C program that sorts the given array of integers using insertion sort in ascending order
i. Write a C program that sorts a given array of names

Suggested Reference Books for solving the problems:

i. Byron Gottfried, Schaum's Outline of Programming with C, McGraw-Hill
ii. B.A. Forouzan and R.F. Gilberg C Programming and Data Structures, Cengage Learning, (3rd Edition)
iii. Brian W. Kernighan and Dennis M. Ritchie, The C Programming Language, Prentice
iv. Hall of India
v. R.G. Dromey, How to solve it by Computer, Pearson (16th Impression)
vi. Programming in C, Stephen G. Kochan, Fourth Edition, Pearson Education.
vii. Herbert Schildt, C: The Complete Reference, Mc Graw Hill, 4th Edition

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CH102BS/CH202BS: CHEMISTRY

B.Tech. I Year II Sem.

L	T	P	C
3	1	0	4

Course Objectives:

- To bring adaptability to the concepts of chemistry and to acquire the required skills to become a perfect engineer.
- To impart the basic knowledge of atomic, molecular and electronic modifications which makes the student to understand the technology based on them.
- To acquire the knowledge of electrochemistry, corrosion and water treatment which are essential for the Engineers and in industry.
- To acquire the skills pertaining to spectroscopy and to apply them for medical and other fields.
- To impart the knowledge of stereochemistry and synthetic aspects useful for understanding reaction pathways

Course Outcomes: The basic concepts included in this course will help the student to gain:

- The knowledge of atomic, molecular and electronic changes, band theory related to conductivity.
- The required principles and concepts of electrochemistry, corrosion and in understanding the problem of water and its treatments.
- The required skills to get clear concepts on basic spectroscopy and application to medical and other fields.
- The knowledge of configurational and conformational analysis of molecules and reaction mechanisms.

UNIT - I:

Molecular structure and Theories of Bonding: Atomic and Molecular orbitals. Linear Combination of Atomic Orbitals (LCAO), molecular orbitals of diatomic molecules, molecular orbital energy level diagrams of N_2, O_2 and F_2 molecules. π molecular orbitals of butadiene and benzene.

Crystal Field Theory (CFT): Salient Features of CFT – Crystal Field Splitting of transition metal ion d-orbitals in Tetrahedral, Octahedral and square planar geometries. Band structure of solids and effect of doping on conductance.

UNIT - II:

Water and its treatment: Introduction – hardness of water – Causes of hardness - Types of hardness: temporary and permanent – expression and units of hardness – Estimation of hardness of water by complexometric method. Potable water and its specifications. Steps involved in treatment of water – Disinfection of water by chlorination and ozonization. Boiler feed water and its treatment – Calgon conditioning, Phosphate conditioning and Colloidal conditioning. External treatment of water – Ion exchange process. Desalination of water – Reverse osmosis. Numerical problems.

UNIT - III:

Electrochemistry and corrosion: Electro chemical cells – electrode potential, standard electrode potential, types of electrodes – calomel, Quinhydrone and glass electrode. Nernst equation Determination of pH of a solution by using quinhydrone and glass electrode. Electrochemical series and its applications. Numerical problems. Potentiometric titrations. Batteries – Primary (Lithium cell) and secondary batteries (Lead – acid storage battery and Lithium ion battery).

Causes and effects of corrosion – theories of chemical and electrochemical corrosion – mechanism of electrochemical corrosion, Types of corrosion: Galvanic, water-line and pitting corrosion. Factors affecting rate of corrosion, Corrosion control methods- Cathodic protection – Sacrificial anode and impressed current cathodic methods. Surface coatings – metallic coatings – methods of application. Electroless plating of Nickel.

UNIT - IV:

Stereochemistry, Reaction Mechanism and synthesis of drug molecules: Introduction to representation of 3-dimensional structures, Structural and stereoisomers, configurations, symmetry and chirality. Enantiomers, diastereomers, optical activity and Absolute configuration. Conformation alanalysis of n- butane.

Substitution reactions: Nucleophilic substitution reactions: Mechanism of S_N1, S_N2 reactions. Electrophilic and nucleophilic addition reactions: Addition of HBr to propene. Markownikoff and anti

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Markownikoff's additions. Grignard additions on carbonyl compounds. Elimination reactions: Dehydro halogenation of alkylhalides. Saytzeff rule. Oxidation reactions: Oxidation of alcohols using $KMnO_4$ and chromic acid.
Reduction reactions: reduction of carbonyl compounds using $LiAlH_4$ & $NaBH_4$. Hydroboration of olefins. Structure, synthesis and pharmaceutical applications of Paracetamol and Aspirin.

UNIT - V:
Spectroscopic techniques and applications: Principles of spectroscopy, selection rules and applications of electronic spectroscopy. vibrational and rotational spectroscopy. Basic concepts of Nuclear magnetic resonance Spectroscopy, chemical shift. Introduction to Magnetic resonance imaging.

TEXT BOOKS:

1. Physical Chemistry, P.W. Atkins, 10th Edn, Oxford University Press.
2. Engineering Chemistry by P.C.Jain & M.Jain; Dhanpat Rai Publishing Company (P) Ltd., New Delhi.
3. Fundamentals of Molecular Spectroscopy, by C.N. Banwell, 4th Edn, McGraw Hill Publishing.
4. Organic Chemistry: Structure and Function by K.P.C. Volhardt and N.E.Schore, 5th Edition, Macmillan International Higher Education.
5. University Chemistry, by B.M. Mahan, Pearson IV Edition.
6. Engineering Chemistry (NPTEL Web-book), by B.L. Tembe, Kamaluddin and M.S. Krishnan

CH106BS/CH206BS: ENGINEERING CHEMISTRY LAB

B.Tech. I Year II Sem.

L	T	P	C
0	0	3	1.5

Course Objectives: The course consists of experiments related to the principles of chemistry required for engineering student. The student will learn:

- Estimation of hardness and chloride content in water to check its suitability for drinking purpose.
- To determine the rate constant of reactions from concentrations as an function of time.
- The measurement of physical properties like adsorption and viscosity.
- To synthesize the drug molecules and check the purity of organic molecules by thin layer chromatographic (TLC) technique.

Course Outcomes: The experiments will make the student gain skills on:

- Determination of parameters like hardness and chloride content in water.
- Estimation of rate constant of a reaction from concentration – time relationships.
- Determination of physical properties like adsorption and viscosity.
- Calculation of R_f values of some organic molecules by TLC technique.

List of Experiments:

1. Determination of total hardness of water by complexometric method using EDTA
2. Determination of chloride content of water by Argentometry
3. Estimation of an HCl by Conductometric titrations
4. Estimation of Acetic acid by Conductometric titrations
5. Estimation of HCl by Potentiometric titrations
6. Estimation of Fe^{2+} by Potentiometry using $KMnO_4$
7. Determination of rate constant of acid catalysed hydrolysis of methyl acetate
8. Synthesis of Aspirin and Paracetamol
9. Thin layer chromatography calculation of R_f values. eg ortho and para nitro phenols
10. Determination of acid value of coconut oil
11. Verification of freundlich adsorption isotherm-adsorption of acetic acid on charcoal
12. Determination of viscosity of castor oil and ground nut oil by using Ostwald's viscometer.
13. Determination of partition coefficient of acetic acid between n-butanol and water.
14. Determination of surface tension of a give liquid using stalagmometer.

References

1. Senior practical physical chemistry, B.D. Khosla, A. Gulati and V. Garg (R. Chand & Co., Delhi)
2. An introduction to practical chemistry, K.K. Sharma and D. S. Sharma (Vikas publishing, N. Delhi)
3. Vogel's text book of practical organic chemistry 5th edition
4. Text book on Experiments and calculations in Engineering chemistry – S.S. Dara

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EE103ES/EE203ES: BASIC ELECTRICAL ENGINEERING

B.Tech. I Year II Sem.

L	T	P	C
3	0	0	3

Course Objectives:
- To introduce the concepts of electrical circuits and its components
- To understand magnetic circuits, DC circuits and AC single phase & three phase circuits
- To study and understand the different types of DC/AC machines and Transformers.
- To import the knowledge of various electrical installations.
- To introduce the concept of power, power factor and its improvement.

Course Outcomes:
- To analyze and solve electrical circuits using network laws and theorems.
- To understand and analyze basic Electric and Magnetic circuits
- To study the working principles of Electrical Machines
- To introduce components of Low Voltage Electrical Installations

UNIT-I: D.C. Circuits
Electrical circuit elements (R, L and C), voltage and current sources, KVL&KCL, analysis of simple circuits with dc excitation. Superposition, Thevenin and Norton Theorems.
Time-domain analysis of first-order RL and RC circuits.

UNIT-II: A.C. Circuits
Representation of sinusoidal waveforms, peak and rms values, phasor representation, real power, reactive power, apparent power, power factor, Analysis of single-phase ac circuits consisting of R, L, C, RL, RC, RLC combinations (series and parallel), resonance in series R-L-C circuit.
Three-phase balanced circuits, voltage and current relations in star and delta connections.

UNIT-III: Transformers
Ideal and practical transformer, equivalent circuit, losses in transformers, regulation and efficiency. Auto-transformer and three-phase transformer connections.

UNIT-IV: Electrical Machines
Generation of rotating magnetic fields, Construction and working of a three-phase induction motor, Significance of torque-slip characteristic. Loss components and efficiency, starting and speed control of induction motor. Single-phase induction motor. Construction, working, torque-speed characteristic and speed control of separately excited dc motor.
Construction and working of synchronous generators.

UNIT-V: Electrical Installations
Components of LT Switchgear: Switch Fuse Unit (SFU), MCB, ELCB, MCCB, Types of Wires and Cables, Earthing. Types of Batteries, Important Characteristics for Batteries. Elementary calculations for energy consumption, power factor improvement and battery backup.

TEXT /REFERENCE BOOKS:

1. Basic Electrical Engineering - D.P. Kothari and I.J. Nagrath, 3rd edition 2010, Tata McGraw Hill.
2. D.C. Kulshreshtha, "Basic Electrical Engineering", McGraw Hill, 2009.
3. L.S. Bobrow, Fundamentals of Electrical Engineering", Oxford University Press, 2011
4. Electrical and Electronics Technology, E. Hughes, 10th Edition, Pearson, 2010
5. Electrical Engineering Fundamentals, Vincent Deltoro, Second Edition, Prentice Hall India, 1989.

EE108ES/EE208ES: BASIC ELECTRICAL ENGINEERING LAB

B.Tech. I Year II Sem.

L	T	P	C
0	0	2	1

Course Objectives:

- To analyze a given network by applying various electrical laws and network theorems
- To know the response of electrical circuits for different excitations
- To calculate, measure and know the relation between basic electrical parameters.
- To analyze the performance characteristics of DC and AC electrical machines

Course Outcomes:

- Get an exposure to basic electrical laws.
- Understand the response of different types of electrical circuits to different excitations.
- Understand the measurement, calculation and relation between the basic electrical parameters
- Understand the basic characteristics of transformers and electrical machines.

List of experiments/demonstrations:

1. Verification of Ohms Law
2. Verification of KVL and KCL
3. Transient Response of Series RL and RC circuits using DC excitation
4. Transient Response of RLC Series circuit using DC excitation
5. Resonance in series RLC circuit
6. Calculations and Verification of Impedance and Current of RL, RC and RLC series circuits
7. Measurement of Voltage, Current and Real Power in primary and Secondary Circuits of a Single-Phase Transformer
8. Load Test on Single Phase Transformer (Calculate Efficiency and Regulation)
9. Three Phase Transformer: Verification of Relationship between Voltages and Currents (Star-Delta, Delta-Delta, Delta-star, Star-Star)
10. Measurement of Active and Reactive Power in a balanced Three-phase circuit
11. Performance Characteristics of a Separately/Self Excited DC Shunt/Compound Motor
12. Torque-Speed Characteristics of a Separately/Self Excited DC Shunt/Compound Motor
13. Performance Characteristics of a Three-phase Induction Motor
14. Torque-Speed Characteristics of a Three-phase Induction Motor
15. No-Load Characteristics of a Three-phase Alternator

EN105HS/EN205HS: ENGLISH

B.Tech. I Year II Sem.

L	T	P	C
2	0	0	2

INTRODUCTION

In view of the growing importance of English as a tool for global communication and the consequent emphasis on training students to acquire language skills, the syllabus of English has been designed to develop linguistic, communicative and critical thinking competencies of Engineering students.

In English classes, the focus should be on the skills development in the areas of vocabulary, grammar, reading and writing. For this, the teachers should use the prescribed text for detailed study. The students should be encouraged to read the texts leading to reading comprehension and different passages may be given for practice in the class. The time should be utilized for working out the exercises given after each excerpt, and also for supplementing the exercises with authentic materials of a similar kind, for example, newspaper articles, advertisements, promotional material etc. *The focus in this syllabus is on skill development, fostering ideas and practice of language skills in various contexts and cultures.*

Learning Objectives: The course will help to

a. Improve the language proficiency of students in English with an emphasis on Vocabulary, Grammar, Reading and Writing skills.
b. Equip students to study academic subjects more effectively and critically using the theoretical and practical components of English syllabus.
c. Develop study skills and communication skills in formal and informal situations.

Course Outcomes: Students should be able to

- Use English Language effectively in spoken and written forms.
- Comprehend the given texts and respond appropriately.
- Communicate confidently in various contexts and different cultures.
- Acquire basic proficiency in English including reading and listening comprehension, writing and speaking skills.

SYLLABUS

UNIT –I

'The Raman Effect' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.

Vocabulary Building: The Concept of Word Formation --The Use of Prefixes and Suffixes.

Grammar: Identifying Common Errors in Writing with Reference to Articles and Prepositions.

Reading: Reading and Its Importance- Techniques for Effective Reading.

Basic Writing Skills: Sentence Structures -Use of Phrases and Clauses in Sentences- Importance of Proper Punctuation- Techniques for writing precisely – **Paragraph writing** – Types, Structures and Features of a Paragraph - Creating Coherence-Organizing Principles of Paragraphs in Documents.

UNIT –II

'Ancient Architecture in India' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.

Vocabulary: Synonyms and Antonyms.

Grammar: Identifying Common Errors in Writing with Reference to Noun-pronoun Agreement and Subject-verb Agreement.

Reading: Improving Comprehension Skills – Techniques for Good Comprehension

Writing: Format of a Formal Letter-**Writing Formal Letters** E.g.., Letter of Complaint, Letter of Requisition, Job Application with Resume.

UNIT –III

'Blue Jeans' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.

Vocabulary: Acquaintance with Prefixes and Suffixes from Foreign Languages in English to form Derivatives-Words from Foreign Languages and their Use in English.

Grammar: Identifying Common Errors in Writing with Reference to Misplaced Modifiers and Tenses.
Reading: Sub-skills of Reading- Skimming and Scanning
Writing: Nature and Style of Sensible Writing- **Defining- Describing** Objects, Places and Events – **Classifying**- Providing Examples or Evidence

UNIT –IV
'What Should You Be Eating' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.
Vocabulary: Standard Abbreviations in English
Grammar: Redundancies and Clichés in Oral and Written Communication.
Reading: Comprehension- Intensive Reading and Extensive Reading
Writing: Writing Practices--Writing Introduction and Conclusion - Essay Writing-Précis Writing.

UNIT –V
'How a Chinese Billionaire Built Her Fortune' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.
Vocabulary: Technical Vocabulary and their usage
Grammar: Common Errors in English
Reading: Reading Comprehension-Exercises for Practice
Writing: **Technical Reports**- Introduction – Characteristics of a Report – Categories of Reports
Formats- Structure of Reports (Manuscript Format) -Types of Reports - Writing aReport.

TEXT BOOK:

1. Sudarshana, N.P. and Savitha, C. (2018). English for Engineers. Cambridge University Press.

REFERENCE BOOKS:

1. Swan, M. (2016). Practical English Usage. Oxford University Press.
2. Kumar, S and Lata, P. (2018). Communication Skills. Oxford University Press.
3. Wood, F.T. (2007). Remedial English Grammar. Macmillan.
4. Zinsser, William. (2001). On Writing Well. Harper Resource Book.
5. Hamp-Lyons, L. (2006). Study Writing. Cambridge University Press.
6. Exercises in Spoken English. Parts I –III. CIEFL, Hyderabad. Oxford University Press.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

B.TECH. ELECTRONICS AND COMMUNICATION ENGINEERING

COURSE STRUCTURE & SYLLABUS (2016-17)

II YEAR I SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA301BS	Mathematics – IV	4	1	0	4
2	EC302ES	Analog Electronics	4	1	0	4
3	EC303ES	Electrical Technology	4	1	0	4
4	EC304ES	Signals and Stochastic Process	3	1	0	3
5	EC305ES	Network Analysis	3	1	0	3
6	EC306ES	Electronic Devices and Circuits Lab	0	0	3	2
7	EC307ES	Basic Simulation Lab	0	0	3	2
8	EC308ES	Basic Electrical Engineering Lab	0	0	3	2
9	*MC300ES	Environmental Science and Technology	3	0	0	0
		Total Credits	**21**	**5**	**9**	**24**

II YEAR II SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	EC401ES	Switching Theory and Logic Design	3	1	0	3
2	EC402ES	Pulse and Digital Circuits	4	0	0	4
3	EE404ES	Control Systems	4	1	0	4
4	EC405ES	Analog Communications	4	0	0	4
5	SM405MS	Business Economics and Financial Analysis	3	0	0	3
6	EC406ES	Analog Communications Lab	0	0	3	2
7	EC407ES	Pulse and Digital Circuits Lab	0	0	3	2
8	EC408ES	Analog Electronics Lab	0	0	3	2
9	*MC400HS	Gender Sensitization Lab	0	0	3	0
		Total Credits	**18**	**2**	**12**	**24**

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EC302ES: ANALOG ELECTRONICS

B.Tech. II Year I Sem.

L	T	P	C
4	1	0	4

Course Objectives:

- To introduce circuit realizations with components such as diodes, BJTs and transistors studied earlier.
- To give understanding of various types of amplifier circuits such as small signal, cascaded, large signal and tuned amplifiers.
- To familiarize the Concept of feedback in amplifiers so as to differentiate between negative and positive feedback.

Course Outcomes: Upon completion of the Course, the students will be able to:

- Design and analyze small signal amplifier circuits applying the biasing techniques learnt earlier.
- Cascade different amplifier configurations to obtain the required overall specifications like Gain, Bandwidth, Input and Output interfacing Impedances.
- Design and realize different classes of Power Amplifiers and tuned amplifiers useable for audio and Radio applications.
- Utilize the Concepts of negative feedback to improve the stability of amplifiers and positive feedback to generate sustained oscillations.

UNIT – I

Analysis And Design of Small Signal Low Frequency BJT Amplifiers: Review of transistor biasing, Classification of Amplifiers – Distortion in amplifiers, Analysis of CE, CC, and CB Amplifiers and CE Amplifier with emitter resistance, low frequency response of BJT Amplifiers, effect of coupling and bypass capacitors, Design of single stage RC coupled amplifier Different coupling schemes used in amplifiers, Analysis of Cascaded RC Coupled amplifiers, Cascode amplifier, Darlington pair,

UNIT – II

Transistor At High Frequency: The Hybrid- pi (□) – Common Emitter transistor model, CE short circuit current gain, current gain with resistive load, single stage CE transistor amplifier response, Gain-bandwidth product.

UNIT – III

FET Amplifiers: Analysis of JFET Amplifiers, Analysis of CS, CD, CG JFET Amplifiers, comparison of performance with BJT Amplifiers, Basic Concepts of MOS Amplifiers, – MOSFET – MOSFET Characteristics in Enhancement and Depletion mode – MOS Small signal model, Common source amplifier with resistive, Diode connected and Current source loads, Source follower, Common Gate Stage, Cascode and Folded Cascode Amplifier – frequency response.

UNIT –III

Positive & Negative Feedback In Amplifiers: Classification of amplifiers, Concepts of feedback – Classification of feedback amplifiers – General characteristics of negative feedback amplifiers – Effect of Feedback on Amplifier characteristics – Voltage series, Voltage shunt, Current series and Current shunt Feedback configurations – Simple problems. Condition for oscillations. RC and LC type Oscillators – Frequency and amplitude stability of oscillators – Generalized analysis of LC oscillators, Quartz, Hartley, and Colpitts Oscillators – RC-phase shift and Wien-bridge oscillators.

UNIT – IV

Large Signal Amplifiers: Class A Power Amplifier, Maximum Value of Efficiency of Class – A Amplifier, Transformer Coupled Amplifier, Push Pull and Complimentary Symmetry Class B and Class AB Power Amplifiers – Principle of operation of class –C Amplifier, Transistor Power Dissipation, Heat Sinks.

Tuned Amplifiers: Introduction, Q-Factor, Small Signal Tuned Amplifiers, frequency response of tuned amplifiers

TEXT BOOKS:

1. Electronic Devices and Circuits, David A. Bell – 5th Edition, Oxford.
2. Electronic Devices and Circuits, S. Salivahanan, N. Suresh Kumar, A Vallvaraj, 5th Edition, MC GRAW HILL EDUCATION.
3. Electronics circuits and applications , Md H Rashid, Cengage 2014

REFERENCES:

1. Integrated Electronics, Jacob Millman, Christos C Halkias, McGraw Hill Education
2. Electronic Devices and Circuits theory– Robert L. Boylestead, Louis Nashelsky, 11th Edition, 2009, Pearson.
3. Electronic Devices Conventional and current version -Thomas L. Floyd 2015, person

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EC306ES: ELECTRONIC DEVICES AND CIRCUITS LAB

B.Tech. II Year I Sem.

L	T	P	C
0	0	3	2

Course Objectives

- To identify various components and testing of active devices.
- To study and operation of millimeters, function generators ,regulated power supplies and CRO To know the characteristics of various active devices.
- To study frequency response amplifier.

Course Outcomes:

- After Completion of the course the student is able to Apply various devices to real time problems.
- Compute frequency response of various amplifiers.

Part A: (Only for viva-voce Examination)

ELECTRONIC WORKSHOP PRACTICE (in 3 lab sessions):

1. Identification, Specification, testing of R,L,C components (color codes), Potentiometers (SPDT, DPDT, and DIP), Coils, Gang Condensers, Relays, Bread Board, PCB's
2. Identification, Specification, testing of Active devices: Diodes, BJT, Low power JFET's, MOSFET's, Power Transistors, LED's, LCD's, SCR, UJT.
3. Study and operation of:
   i. Multimeters (Analog and Digital)
   ii. Function Generator
   iii. Regulated Power Supplies
   iv. CRO

Part B: (For Laboratory Examination – Minimum of 12 experiments)

1. Forward and Reverse Bias V-I characteristics of PN junction Diode.
2. Zener diode V-I characteristics and Zener diode as voltage regulator.
3. Half Wave rectifier, with and without filters
4. Full wave rectifier with and without filters.
5. Input and output Characteristics of a BJT in CE configuration and calculation of h-parameters.
6. Input and output Characteristics of a BJT in CB configuration and calculation of h-parameters.
7. FET characteristics in CS configuration.
8. Design of self bias circuit
9. Frequency response of CE Amplifier.
10. Frequency response of CC Amplifier.
11. Frequency response of CS FET Amplifier.
12. SCR characteristics.
13. UJT characteristics.

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PART C: Equipment required for Laboratory:

1. Regulated Power supplies (RPS) : 0-30 V
2. CRO's : 0-20 MHz.
3. Function Generators : 0-1 MHz.
4. Multimeters
5. Decade Resistance Boxes/Rheostats
6. Decade Capacitance Boxes
7. Ammeters (Analog or Digital) : 0-20 μA, 0-50μA, 0-100μA, 0-200μA,10 mA.
8. Voltmeters (Analog or Digital) : 0-50V, 0-100V, 0-250V
9. Electronic Components: Resistors, Capacitors, BJTs, LCDs, SCRs, UJTs, FETs, LEDs, MOSFETs, Diodes-Ge & Si type, Transistors – NPN, PNP type.

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EC303ES: ELECTRICAL TECHNOLOGY

B.Tech. II Year I Sem.

L	T	P	C
4	1	0	4

Course Objectives:

- To know the basic principle of DC generators and motors.
- To know the basic principle of single phase transformers.
- To understand the basic principle of three-phase induction motor and alternators.
- To understand the basic principle of special motors and electrical instruments.

Course Outcome:

- To analyze the performance of dc generators and motors.
- To analyze the performance of transformers.
- To learn the in-depth knowledge on three phase induction motors.
- To analyze the performance of special motors and electrical instruments in real time applications.

UNIT - I

D.C Generators and DC Motors: Principle of operation of DC Machines- EMF equation – Types of generators – Magnetization and load characteristics of DC generators, DC Motors – Types of DC Motors – Characteristics of DC motors – 3-point starters for DC shunt motor – Losses and efficiency – Swinburne's test – Speed control of DC shunt motor – Flux and Armature voltage control methods.

UNIT - II

Transformers & Performance: Principle of operation of single phase transformer – types – Constructional features – Phasor diagram on No Load and Load – Equivalent circuit, Losses and Efficiency of transformer and Regulation – OC and SC tests – Predetermination of efficiency and regulation (Simple Problems).

UNIT - III

Three Phase Induction Motor: Principle of operation of three-phase induction motors –Slip ring and Squirrel cage motors – Slip-Torque characteristics – Efficiency calculation – Starting methods.

UNIT - IV

Alternators: Alternators – Constructional features – Principle of operation – Types - EMF Equation – Distribution and Coil span factors – Predetermination of regulation by Synchronous Impedance Method – OC and SC tests.

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UNIT - V

Special Motors & Electrical Instruments : Principle of operation - Shaded pole motors – Capacitor motors, AC servomotor, AC tachometers, Synchros, Stepper Motors – Characteristics, Basic Principles of indicating instruments – Moving Coil and Moving iron Instruments (Ammeters and Voltmeters).

TEXT BOOKS:

1. Introduction to Electrical Engineering – M.S Naidu and S. Kamakshaiah, TMH Publ.
2. Basic Electrical Engineering - T.K. Nagasarkar and M. S. Sukhija, Oxford University Press, 2005

REFERENCES:

1. Principles of Electrical Engineering - V.K Mehta, S. Chand Publications.
2. Theory and Problems of basic electrical engineering - I.J. Nagarath and D.P Kothari, PHI Publications
3. Essentials of Electrical and Computer Engineering - David V. Kerns, JR. J. David Irwin

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EC308ES: BASIC ELECTRICAL ENGINEERING LAB

B.Tech. II Year I Sem.

L	T	P	C
0	0	3	2

Note: Minimum 6 experiments from each part are to be conducted

PART – A

1. Verification of KVL and KCL.
2. Serial and Parallel Resonance – Timing, Resonant frequency, Bandwidth and Q-factor determination for RLC network.
3. Time response of first order RC/RL network for periodic non-sinusoidal inputs – time constant and steady state error determination.
4. Two port network parameters – Z-Y Parameters, chain matrix and analytical verification.
5. Two post network parameters -ABCD and h parameters
6. Verification of Superposition and Reciprocity theorems.
7. Verification of maximum power transfer theorem. Verification on DC, verification on AC with Resistive and Reactive loads.
8. Experimental determination of Thevenin's and Norton's equivalent circuits and verification by direct test.

PART – B

1. Magnetization characteristics of D.C. Shunt generator. Determination of critical field resistance.
2. Swinburne's Test on DC shunt machine (Predetermination of efficiency of a given DC Shunt machine working as motor and generator).
3. Brake test on DC shunt motor. Determination of performance characteristics.
4. OC & SC tests on Single-phase transformer (Predetermination of efficiency and regulation at given power factors and determination of equivalent circuit).
5. Brake test on 3-phase Induction motor (performance characteristics).
6. Regulation of alternator by synchronous impedance method.
7. Load test on single phase transform

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Sampling: Sampling theorem – Graphical and analytical proof for Band Limited Signals, Reconstruction of signal from its samples, Effect of under sampling – Aliasing.

UNIT – III

Laplace Transforms and Z–Transforms: Laplace Transforms: Review of Laplace Transforms (L.T), Partial fraction expansion, Inverse Laplace Transform, Concept of Region of Convergence (ROC) for Laplace Transforms, Constraints on ROC for various classes of signals, Properties of L.T, Relation between L.T and F.T of a signal, Laplace Transform of certain signals using waveform synthesis.

Z–Transforms: Fundamental difference between Continuous and Discrete time signals, Discrete time signal representation using Complex exponential and Sinusoidal components, Periodicity of Discrete time signal using complex exponential signal, Concept of Z-Transform of a Discrete Sequence, Distinction between Laplace, Fourier and Z Transforms, Region of Convergence in Z-Transform, Constraints on ROC for various classes of signals, Inverse Z-transform, Properties of Z-transforms.

UNIT – IV

Random Processes – Temporal Characteristics: The Random Process Concept, Classification of Processes, Deterministic and Nondeterministic Processes, Distribution and Density Functions, concept of Stationarity and Statistical Independence. First-Order Stationary Processes, Second- Order and Wide-Sense Stationarity, (N-Order) and Strict-Sense Stationarity, Time Averages and Ergodicity, Autocorrelation Function and Its Properties, Cross-Correlation Function and Its Properties, Covariance Functions, Gaussian Random Processes, Poisson Random Process. Random Signal, Mean and Mean-squared Value of System Response, autocorrelation Function of Response, Cross-Correlation Functions of Input and Output.

UNIT- V:

Random Processes – Spectral Characteristics: The Power Spectrum: Properties, Relationship between Power Spectrum and Autocorrelation Function, The Cross-Power Density Spectrum, Properties, Relationship between Cross-Power Spectrum and Cross-Correlation Function. Spectral Characteristics of System Response: Power Density Spectrum of Response, Cross-Power Density Spectrums of Input and Output.

TEXT BOOKS:

1. Signals, Systems & Communications - B.P. Lathi , 2013, BSP.
2. Signal and systems principles and applications, shaila dinakar Apten, Cambridez university press, 2016.
3. Probability, Random Variables & Random Signal Principles - Peyton Z. Peebles, MC GRAW HILL EDUCATION, 4th Edition, 2001

REFERENCE BOOKS:

1. Signals and Systems - A.V. Oppenheim, A.S. Willsky and S.H. Nawab, 2 Ed.,
2. Signals and Signals – Iyer and K. Satya Prasad, Cengage Learning

EC305ES: NETWORK ANALYSIS

B.Tech. II Year I Sem.

L	T	P	C
3	1	0	3

Pre-requisite: Basic Electrical & Electronics Engineering

Course Objectives: Objectives of this course are

- To understand the basic concepts on RLC circuits.
- To know the behavior of the steady states and transients states in RLC circuits.
- To know the basic Laplace transforms techniques in periods waveforms.
- To understand the two port network parameters.
- To understand the properties of LC networks and filters.

Course Outcomes: After completion of this course student:

- Gains the knowledge on Basic network elements.
- Learns and analyze the RLC circuits' behavior in detail.
- Analyze the performance of periodic waveforms.
- Learns and gain the knowledge in characteristics of two port network parameters (Z, Y, ABCD, h & g).
- To analyze the filter design concepts in real world applications.

UNIT - I

Review of R, L,C, RC, RL, RLC circuits, Network Topology, Terminology, Basic cutest and tie set matrices for planar networks, Illustrative Problems, Magnetic Circuits, Self and Mutual inductances, dot convention, impedance, reactance concept, Impedance transformation and coupled circuits, co-efficient of coupling, equivalent T for Magnetically coupled circuits, Ideal Transformer.

UNIT - II

Steady state and transient analysis of RC, RL and RLC Circuits, Circuits with switches, step response, 2nd order series and parallel RLC Circuits, Root locus, damping factor, over damped, under damped, critically damped cases, quality factor and bandwidth for series and parallel resonance, resonance curves

UNIT - III

Network Analysis using Laplace transform techniques, step, impulse and exponential excitation, response due to periodic excitation, RMS and average value of periodic waveforms.

UNIT - IV

Two port network parameters, Z, Y, ABCD, h and g parameters, Characteristic impedance, Image transfer constant, image and iterative impedance, network function, driving point and transfer functions – using transformed (S) variables, Poles and Zeros.

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EC307ES: BASIC SIMULATION LAB

B.Tech. II Year I Sem.

L	T	P	C
0	0	3	2

Note:

- All the experiments are to be simulated using MATLAB or equivalent software
- Minimum of 15 experiments are to be completed

List of Experiments:

1. Basic Operations on Matrices.
2. Generation of Various Signals and Sequences (Periodic and Aperiodic), such as Unit Impulse, Unit Step, Square, Saw tooth, Triangular, Sinusoidal, Ramp, Sinc.
3. Operations on Signals and Sequences such as Addition, Multiplication, Scaling, Shifting, Folding, Computation of Energy and Average Power.
4. Finding the Even and Odd parts of Signal/Sequence and Real and Imaginary parts of Signal.
5. Convolution for Signals and sequences.
6. Auto Correlation and Cross Correlation for Signals and Sequences.
7. Verification of Linearity and Time Invariance Properties of a given Continuous/Discrete System.
8. Computation of Unit sample, Unit step and Sinusoidal responses of the given LTI system and verifying its physical realiazability and stability properties.
9. Gibbs Phenomenon Simulation.
10. Finding the Fourier Transform of a given signal and plotting its magnitude and phase spectrum.
11. Waveform Synthesis using Laplace Transform.
12. Locating the Zeros and Poles and plotting the Pole-Zero maps in S-plane and Z-Plane for the given transfer function.
13. Generation of Gaussian noise (Real and Complex), Computation of its mean, M.S. Value and its Skew, Kurtosis, and PSD, Probability Distribution Function.
14. Sampling Theorem Verification.
15. Removal of noise by Autocorrelation / Cross correlation.
16. Extraction of Periodic Signal masked by noise using Correlation.
17. Verification of Weiner-Khinchine Relations.
18. Checking a Random Process for Stationarity in Wide sense.

EC402ES: PULSE AND DIGITAL CIRCUITS

B.Tech. II Year II Sem.

L	T	P	C
4	0	0	4

Course Objectives:

- To explain the complete response of R-C and R-L-C transient circuits.
- To explain clippers, clampers, switching characteristics of transistors and sampling gates.
- To construct various multivibrators using transistors, design of sweep circuits and sampling gates.
- To discuss and realize logic gates using diodes and transistors.

Course Outcomes: At the end of the course, the student will be able to:

- Understand the applications of diode as integrator, differentiator, clippers, clampler circuits.
- Learn various switching devices such as diode, transistor, SCR. Difference between logic gates and sampling gates
- Design multivibrators for various applications, synchronization techniques and sweep circuits.
- Realizing logic gates using diodes and transistors.
- Understanding of time and frequency domain aspects.
- Importance of clock pulse and its generating techniques.

UNIT - I

Linear Wave Shaping: High pass and low pass RC circuits and their response for Sinusoidal, Step, Pulse, Square, & Ramp inputs, High pass RC network as Differentiator, Low pass RC circuit as an Integrator, Attenuators and its application as a CRO Probe, RL and RLC Circuits and their response for Step Input, Ringing Circuit.

UNIT - II

Non-Linear Wave Shaping: Diode clippers, Transistor clippers, Clipping at two independent levels, Comparators, Applications of Voltage comparators. Clamping Operation, Clamping circuit taking Source and Diode resistances into account, Clamping Circuit Theorem, Practical Clamping Circuits, Effect of Diode Characteristics on Clamping Voltage, Synchronized Clamping.

UNIT - III

Switching Characteristics of Devices: Diode as a Switch, Piecewise Linear Diode Characteristics, Diode Switching times, Transistor as a Switch, Break down voltages, Transistor in Saturation, Temperature variation of Saturation Parameters, Transistor-switching times, Silicon-controlled-switch circuits.

UNIT – IV

Multivibrators: Analysis and Design of Bistable, Monostable, Astable Multivibrators and Schmitt trigger using Transistors.

Time Base Generators: General features of a Time base Signal, Methods of Generating Time Base Waveform, Transistor Miller Time Base generator, Transistor Bootstrap Time Base Generator, Transistor Current Time Base Generators, Methods of Linearity improvement.

UNIT - V

Sampling Gates: Basic operating principles of Sampling Gates, Unidirectional and Bi-directional Sampling Gates, Four Diode Sampling Gate, Reduction of pedestal in Gate Circuits

Realization of Logic Gates Using Diodes & Transistors: AND, OR and NOT Gates using Diodes and Transistors, DCTL, RTL, DTL, TTL and CML Logic Families and its Comparison.

TEXT BOOKS:

1. Millman's Pulse, Digital and Switching Waveforms –J. Millman, H. Taub and Mothiki S. Prakash Rao, 2 Ed., 2008, McGraw Hill.
2. Pulse, Switching and Digital Circuits - David A. Bell, 5th edition 2015, OXFORD University Press

REFERENCE BOOKS:

1. Pulse and Digital Circuits -Venkata Rao K, Rama Sudha K, Manmadha rao G, Pearson, 2010
2. Pulse and Digital Circuits – A. Anand Kumar, 2005, PHI.

EC405ES: ANALOG COMMUNICATIONS

B.Tech. II Year II Sem.

L	T	P	C
4	0	0	4

Course Objectives:

- To develop ability to analyze system requirements of analog communication systems.
- To understand the need for modulation
- To understand the generation, detection of various analog modulation techniques and also perform the mathematical analysis associated with these techniques.
- To acquire knowledge to analyze the noise performance of analog modulation techniques.
- To acquire theoretical knowledge of each block in AM and FM receivers.
- To understand the pulse modulation techniques.

Course Outcomes:

- Able to analyze and design various modulation and demodulation analog systems.
- Understand the characteristics of noise present in analog systems.
- Study of signal to Noise Ration (SNR) performance, of various Analog Communication systems.
- Analyze and design the various Pulse Modulation Systems.
- Understand the concepts of Multiplexing: Time Division Multiplexing (TDM) and Frequency Division Multiplexing (FDM).

UNIT - I

Amplitude Modulation: Introduction to communication system, Need for modulation, Frequency Division Multiplexing , Amplitude Modulation, Definition, Time domain and frequency domain description, single tone modulation, power relations in AM waves, Generation of AM waves, square law Modulator, Switching modulator, Detection of AM Waves; Square law detector, Envelope detector, Double side band suppressed carrier modulators, time domain and frequency domain description, Generation of DSBSC Waves, Balanced Modulators, Ring Modulator, Coherent detection of DSB-SC Modulated waves, COSTAS Loop.

UNIT - II

SSB Modulation: Introduction to Hilbert Transform, Frequency domain description, Frequency discrimination method for generation of AM SSB Modulated Wave, Time domain description, Phase discrimination method for generating AM SSB Modulated waves. Demodulation of SSB Waves, Vestigial side band modulation: Frequency description, Generation of VSB Modulated wave, Time domain description, Envelope detection of a VSB Wave pulse Carrier, Comparison of AM Techniques, Applications of different AM Systems.

UNIT - III

Angle Modulation: Basic concepts, Frequency Modulation: Single tone frequency modulation, Spectrum Analysis of Sinusoidal FM Wave, Narrow band FM, Wide band FM, Constant Average Power, Transmission bandwidth of FM Wave - Generation of FM Waves, Direct FM, Detection of FM Waves: Balanced Frequency discriminator, Zero crossing detector, Phase locked loop, Comparison of FM and AM.

UNIT - IV

Noise: Resistive Noise Source (Thermal), Arbitrary Noise Sources, Effective Noise Temperature, Average Noise Figures, Average Noise Figure of cascaded networks, Narrow Band noise, Quadrature representation of narrow band noise, & its properties

Noise in Analog communication System, Noise in DSB and SSB System Noise in AM System, Noise in Angle Modulation System, Threshold effect in Angle Modulation System, Pre-emphasis and de-emphasis.

UNIT - V

Receivers: Radio Receiver - Receiver Types - Tuned radio frequency receiver, Super heterodyne receiver, RF section and Characteristics - Frequency changing and tracking, Intermediate frequency, AGC, FM Receiver, Comparison with AM Receiver, Amplitude limiting.

PULSE MODULATION: Types of Pulse modulation, PAM (Single polarity, double polarity) PWM: Generation and demodulation of PWM, PPM, Generation and demodulation of PPM, Time Division Multiplexing.

TEXTBOOKS:

1. Communication Systems by Simon Haykins John Wiley & Sons, 4th Edition.
2. Electronics & Communication System – George Kennedy and Bernard Davis, McGraw Hill Education 2004.

REFERENCES:

1. Communication theory, thomas,2 edition, McGraw-Hill Education
2. Communication Systems, 2E, R. P. Singh, S. D. Sapre, McGraw-Hill Education, 2008.
3. Analog and Digital Communication – K. Sam Shanmugam, Willey, 2005
4. Electronics Communication Systems- Wayne Tomasi, 6th Edition, Person 2009

EC408ES: ANALOG ELECTRONICS LAB

B.Tech. II Year II Sem.

L	T	P	C
0	0	3	2

Note:

- Minimum 12 experiments should be conducted:
- Experiments are to be simulated using Multisim or P-spice or Equivalent Simulation and then testing to be done in hardware.

LIST OF EXPERIMENTS:

1. Common Emitter Amplifier
2. Common Base Amplifier
3. Common Source amplifier
4. Two Stage RC Coupled Amplifier
5. Current Shunt Feedback Amplifier
6. Voltage Series Feedback Amplifier
7. Cascode Amplifier
8. Wien Bridge Oscillator using Transistors
9. RC Phase Shift Oscillator using Transistors
10. Class A Power Amplifier (Transformer less)
11. Class B Complementary Symmetry Amplifier
12. Hartley Oscillator
13. Colpitt's Oscillator
14. Single Tuned Voltage Amplifier

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EC402ES: PULSE AND DIGITAL CIRCUITS

B.Tech. II Year II Sem.

L	T	P	C
4	0	0	4

Course Objectives:

- To explain the complete response of R-C and R-L-C transient circuits.
- To explain clippers, clampers, switching characteristics of transistors and sampling gates.
- To construct various multivibrators using transistors, design of sweep circuits and sampling gates.
- To discuss and realize logic gates using diodes and transistors.

Course Outcomes: At the end of the course, the student will be able to:

- Understand the applications of diode as integrator, differentiator, clippers, clampler circuits.
- Learn various switching devices such as diode, transistor, SCR. Difference between logic gates and sampling gates
- Design multivibrators for various applications, synchronization techniques and sweep circuits.
- Realizing logic gates using diodes and transistors.
- Understanding of time and frequency domain aspects.
- Importance of clock pulse and its generating techniques.

UNIT - I

Linear Wave Shaping: High pass and low pass RC circuits and their response for Sinusoidal, Step, Pulse, Square, & Ramp inputs, High pass RC network as Differentiator, Low pass RC circuit as an Integrator, Attenuators and its application as a CRO Probe, RL and RLC Circuits and their response for Step Input, Ringing Circuit.

UNIT - II

Non-Linear Wave Shaping: Diode clippers, Transistor clippers, Clipping at two independent levels, Comparators, Applications of Voltage comparators. Clamping Operation, Clamping circuit taking Source and Diode resistances into account, Clamping Circuit Theorem, Practical Clamping Circuits, Effect of Diode Characteristics on Clamping Voltage, Synchronized Clamping.

UNIT - III

Switching Characteristics of Devices: Diode as a Switch, Piecewise Linear Diode Characteristics, Diode Switching times, Transistor as a Switch, Break down voltages, Transistor in Saturation, Temperature variation of Saturation Parameters, Transistor-switching times, Silicon-controlled-switch circuits.

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UNIT – IV

Multivibrators: Analysis and Design of Bistable, Monostable, Astable Multivibrators and Schmitt trigger using Transistors.

Time Base Generators: General features of a Time base Signal, Methods of Generating Time Base Waveform, Transistor Miller Time Base generator, Transistor Bootstrap Time Base Generator, Transistor Current Time Base Generators, Methods of Linearity improvement.

UNIT - V

Sampling Gates: Basic operating principles of Sampling Gates, Unidirectional and Bi-directional Sampling Gates, Four Diode Sampling Gate, Reduction of pedestal in Gate Circuits

Realization of Logic Gates Using Diodes & Transistors: AND, OR and NOT Gates using Diodes and Transistors, DCTL, RTL, DTL, TTL and CML Logic Families and its Comparison.

TEXT BOOKS:

1. Millman's Pulse, Digital and Switching Waveforms –J. Millman, H. Taub and Mothiki S. Prakash Rao, 2 Ed., 2008, McGraw Hill.
2. Pulse, Switching and Digital Circuits - David A. Bell, 5th edition 2015, OXFORD University Press

REFERENCE BOOKS:

1. Pulse and Digital Circuits -Venkata Rao K, Rama Sudha K, Manmadha rao G, Pearson, 2010
2. Pulse and Digital Circuits – A. Anand Kumar, 2005, PHI.

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EC407ES: PULSE AND DIGITAL CIRCUITS LAB

B.Tech. II Year II Sem.

L	T	P	C
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Note:

Minimum Twelve experiments to be conducted:

1. Linear wave Shaping
   a. RC Low Pass Circuit for different time constants
   b. RC High Pass Circuit for different time constants

2. Non-linear wave shaping
   a. Transfer characteristics and response of Clippers:
      i) Positive and Negative Clippers
      ii) Clipping at two independent levels
   b. The steady state output waveform of clampers for a square wave input
      i) Positive and Negative Clampers
      ii) Clamping at different reference voltage

3. Comparison Operation of different types of Comparators
4. Switching characteristics of a transistor
5. Design a Bistable Multivibrator and draw its waveforms
6. Design an Astable Multivibrator and draw its waveforms
7. Design a Monostable Multivibrator and draw its waveforms
8. Response of Schmitt Trigger circuit for loop gain less than and greater than one
9. UJT relaxation oscillator
10. The output- voltage waveform of Boot strap sweep circuit
11. The output- voltage waveform of Miller sweep circuit
12. Pulse Synchronization of An Astable circuit
13. Response of a transistor Current sweep circuit
14. Sampling gates
    a. Response of Unidirectional gate
    b. Response of Bidirectional gate using transistors
15. Study of logic gates

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EC408ES: ANALOG ELECTRONICS LAB

B.Tech. II Year II Sem.

L	T	P	C
0	0	3	2

Note:

- Minimum 12 experiments should be conducted:
- Experiments are to be simulated using Multisim or P-spice or Equivalent Simulation and then testing to be done in hardware.

LIST OF EXPERIMENTS:

1. Common Emitter Amplifier
2. Common Base Amplifier
3. Common Source amplifier
4. Two Stage RC Coupled Amplifier
5. Current Shunt Feedback Amplifier
6. Voltage Series Feedback Amplifier
7. Cascode Amplifier
8. Wien Bridge Oscillator using Transistors
9. RC Phase Shift Oscillator using Transistors
10. Class A Power Amplifier (Transformer less)
11. Class B Complementary Symmetry Amplifier
12. Hartley Oscillator
13. Colpitt's Oscillator
14. Single Tuned Voltage Amplifier

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MC400HS: GENDER SENSITIZATION LAB

B.Tech. II Year II Sem.

L	T	P	C
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Course Objectives:

- To develop students' sensibility with regard to issues of gender in contemporary India.
- To provide a critical perspective on the socialization of men and women.
- To introduce students to information about some key biological aspects of genders.
- To expose the students to debates on the politics and economics of work.
- To help students reflect critically on gender violence.
- To expose students to more egalitarian interactions between men and women.

Course Outcomes:

- Students will have developed a better understanding of important issues related to gender in contemporary India.
- Students will be sensitized to basic dimensions of the biological, sociological, psychological and legal aspects of gender. This will be achieved through discussion of materials derived from research, facts, everyday life, literature, and film.
- Students will attain a finer grasp of how gender discrimination works in our society and how to counter it.
- Students will acquire insight into the gendered division of labour and its relation to politics and economics.
- Men and women students and professionals will be better equipped to work and live together as equals.
- Students will develop a sense of appreciation of women in all walks of life.
- Through providing accounts of studies and movements as well as the new laws that provide protection and relief to women, the textbook will empower students to understand and respond to gender violence.

UNIT - I

UNDERSTANDING GENDER

Gender: Why Should We Study It? (*Towards a World of Equals*: Unit -1)

Socialization: Making Women, Making Men (*Towards a World of Equals*: Unit -2)

Introduction. Preparing for Womanhood. Growing up Male. First lessons in Caste. Different Masculinities.

UNIT - II

GENDER AND BIOLOGY:

Missing Women: Sex Selection and Its Consequences (*Towards a World of Equals*: Unit -4)

Declining Sex Ratio. Demographic Consequences.

Gender Spectrum: Beyond the Binary (*Towards a World of Equals*: Unit -10)

Two or Many? Struggles with Discrimination.

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UNIT - III

GENDER AND LABOUR

Housework: the Invisible Labour (*Towards a World of Equals*: Unit -3)

"My Mother doesn't Work." "Share the Load."

Women's Work: Its Politics and Economics (*Towards a World of Equals*: Unit -7)

Fact and Fiction. Unrecognized and Unaccounted work. Additional Reading: Wages and Conditions of Work.

UNIT-IV

ISSUES OF VIOLENCE

Sexual Harassment: Say No! (*Towards a World of Equals*: Unit -6)

Sexual Harassment, not Eve-teasing- Coping with Everyday Harassment- Further Reading: "*Chupulu*".

Domestic Violence: Speaking Out (*Towards a World of Equals*: Unit -8)

Is Home a Safe Place? -When Women Unite [Film]. Rebuilding Lives. Additional Reading: New Forums for Justice.

Thinking about Sexual Violence (*Towards a World of Equals*: Unit -11)

Blaming the Victim-"I Fought for my Life…." - Additional Reading: The Caste Face of Violence.

UNIT - V

GENDER: CO - EXISTENCE

Just Relationships: Being Together as Equals (*Towards a World of Equals*: Unit -12)

Mary Kom and Onler. Love and Acid just do not Mix. Love Letters. Mothers and Fathers. Additional Reading: Rosa Parks-The Brave Heart.

TEXTBOOK

All the five Units in the Textbook, ***"Towards a World of Equals: A Bilingual Textbook on Gender"*** written by A. Suneetha, Uma Bhrugubanda, Duggirala Vasanta, Rama Melkote, Vasudha Nagaraj, Asma Rasheed, Gogu Shyamala, Deepa Sreenivas and Susie Tharu and published by **Telugu Akademi, Hyderabad**,Telangana State in the year **2015**.

Note: Since it is an Interdisciplinary Course, Resource Persons can be drawn from the fields of English Literature or Sociology or Political Science or any other qualified faculty who has expertise in this field from engineering departments.

REFERENCE BOOKS:

1. Menon, Nivedita. Seeing like a Feminist. New Delhi: Zubaan-Penguin Books, 2012
2. Abdulali Sohaila. "*I Fought For My Life…and Won*."Available online at: http://www.thealternative.in/lifestyle/i-fought-for-my-lifeand-won-sohaila-abdulal/

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

B.TECH. ELECTRONICS AND COMMUNICATION ENGINEERING
III YEAR COURSE STRUCTURE & SYLLABUS (R16)

Applicable From 2016-17 Admitted Batch

III YEAR I SEMESTER

S. No	Course Code	Course Title	L	T	P	Credits
1	EC501PC	Electromagnetic Theory and Transmission Lines	4	1	0	4
2	EC502PC	[redacted]	4	0	0	4
3	EC503PC	[redacted]	4	1	0	4
4	SM504MS	Fundamentals of Management	3	0	0	3
5		Open Elective – I	3	0	0	3
6	EC505PC	[redacted]	0	0	3	2
7	EC506PC	[redacted]	0	0	3	2
8	EC507PC	[redacted]	0	0	3	2
9	[redacted]	[redacted]	3	0	0	0
		Total Credits	**21**	**2**	**9**	**24**

III YEAR II SEMESTER

S. No	Course Code	Course Title	L	T	P	Credits
1		Open Elective-II	3	0	0	3
2		Professional Elective-I	3	0	0	3
3	EC601PC	Antennas and Wave Propagation	4	0	0	4
4	EC602PC	[redacted]	4	0	0	4
5	EC603PC	[redacted]	4	0	0	4
6	EC604PC	[redacted]	0	0	3	2
7	EC605PC	[redacted]	0	0	3	2
8	EN606HS	[redacted]	0	0	3	2
		Total Credits	**18**	**0**	**9**	**24**

During Summer Vacation between III and IV Years: Industry Oriented Mini Project

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LINEAR AND DIGITAL IC APPLICATIONS

B.Tech. III Year I Sem. | | L T P C
Course Code: EC502PC | | 4 0 0 4

Course Objectives:

1. The main objectives of the course are:
2. To introduce the basic building blocks of linear integrated circuits.
3. To teach the linear and non - linear applications of operational amplifiers.
4. To introduce the theory and applications of analog multipliers and PLL.
5. To teach the theory of ADC and DAC.
6. To introduce the concepts of waveform generation and introduce some special function ICs.
7. To understand and implement the working of basic digital circuits

Course Outcomes: On completion of this course, the students will have:

1. A thorough understanding of operational amplifiers with linear integrated circuits.
2. Understanding of the different families of digital integrated circuits and their characteristics.
3. Also students will be able to design circuits using operational amplifiers for various applications.

UNIT - I

Operational Amplifier: Ideal and Practical Op-Amp, Op-Amp Characteristics, DC and AC Characteristics, Features of 741 Op-Amp, Modes of Operation - Inverting, Non-Inverting, Differential, Instrumentation Amplifier, AC Amplifier, Differentiators and Integrators, Comparators, Schmitt Trigger, Introduction to Voltage Regulators, Features of 723 Regulator, Three Terminal Voltage Regulators.

UNIT - II

Op-Amp, IC-555 & IC 565 Applications: Introduction to Active Filters, Characteristics of Band pass, Band reject and All Pass Filters, Analysis of 1st order LPF & HPF Butterworth Filters, Waveform Generators – Triangular, Saw tooth, Square Wave, IC555 Timer - Functional Diagram, Monostable, and Astable Operations, Applications, IC565 PLL - Block Schematic, Description of Individual Blocks, Applications.

UNIT - III

Data Converters: Introduction, Basic DAC techniques, Different types of DACs-Weighted resistor DAC, R-2R ladder DAC, Inverted R-2R DAC, Different Types of ADCs - Parallel Comparator Type ADC, Counter Type ADC, Successive Approximation ADC and Dual Slope ADC, DAC and ADC Specifications.

UNIT - IV

Digital Integrated Circuits: Classification of Integrated Circuits, Comparison of Various Logic Families Combinational Logic ICs – Specifications and Applications of TTL-74XX & Code Converters, Decoders, Demultiplexers, LED & LCD Decoders with Drivers, Encoders, Priority Encoders, Multiplexers, Demultiplexers, Priority Generators/Checkers, Parallel Binary Adder/Subtractor, Magnitude Comparators.

UNIT - V

Sequential Logic IC's and Memories: Familiarity with commonly available 74XX & CMOS 40XX Series ICs – All Types of Flip-flops, Synchronous Counters, Decade Counters, Shift Registers.

Memories - ROM Architecture, Types of ROMS & Applications, RAM Architecture, Static & Dynamic RAMs.

TEXT BOOKS:

1. Op-Amps & Linear ICs – Ramakanth A. Gayakwad, PHI, 2003.
2. Digital Fundamentals – Floyd and Jain, Pearson Education, 8th Edition, 2005.

REFERENCE BOOKS:

1. Linear Integrated Circuits –D. Roy Chowdhury, New Age International (p) Ltd, 2nd Ed., 2003.
2. Op Amps and Linear Integrated Circuits-Concepts and Applications James M. Fiore, Cengage Learning/ Jaico, 2009.
3. Operational Amplifiers with Linear Integrated Circuits by K. Lal Kishore – Pearson, 2009.
4. Linear Integrated Circuits and Applications – Salivahanan, MC GRAW HILL EDUCATION.
5. Modern Digital Electronics – RP Jain – 4/e – MC GRAW HILL EDUCATION, 2010.

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LINEAR IC APPLICATIONS LAB

B.Tech. III Year I Sem.
Course Code: EC505PC

L	T	P	C
0	0	3	2

Note:

- To perform any twelve experiments
- Verify the functionality of the IC in the given application.

Design and Implementation of:

1. Inverting and Non-inverting Amplifiers using Op Amps.
2. Adder and Subtractor using Op Amp.
3. Comparators using Op Amp.
4. Integrator Circuit using IC 741.
5. Differentiator circuit using Op Amp.
6. Active Filter Applications – LPF, HPF (first order)
7. IC 741 Waveform Generators – Sine, Square wave and Triangular waves.
8. Mono-stable Multivibrator using IC 555.
9. Astable Multivibrator using IC 555.
10. Schmitt Trigger Circuits – using IC 741.
11. IC 565 – PLL Applications.
12. Voltage Regulator using IC 723.
13. Three Terminal Voltage Regulators –7805, 7809, 7912.

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DIGITAL IC APPLICATIONS LAB

B.Tech. III Year I Sem. **L T P C**
Course Code: EC506PC **0 0 3 2**

Note:

- To perform any twelve experiments
- Verify the functionality of the IC in the given application.

Design and Implementation of:

1. Design a 16 x 4 priority encoder using two 8 x 3 priority encoder.
2. Design a 16 bit comparator using 4 bit Comparators.
3. Design a model to 53 counter using two decade counters.
4. Design a 450 KHz clock using NAND / NOR gates.
5. Design a 4 bit pseudo random sequence generator using 4 – bit ring counter.
6. Design a 16 x 1 multiplexer using 8 x 1 multiplexer.
7. Design a 16 bit Adder / Subtractor using 4 – bit Adder / Subtractor IC's
8. Plot the transform Characteristics of 74H, LS, HS series IC's.
9. Design a 4 – bit Gray to Binary and Binary to Gray Converter.
10. Design a two Digit 7 segment display unit using this display the Mod counter output of experiment 3.
11. Design an 8 bit parallel load and serial out shift register using two 4 bit shift register.
12. Design an 8 bit Serial in and serial out shift register using two 4 bit shift register.
13. Design a Ring counter and Twisted ring counter using a 4-bit shift register
14. Design a 4 digit hex counter using synchronous one digit hex counters.
15. Design a 4 digit hex counter using Asynchronous one digit hex counters.

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DIGITAL COMMUNICATIONS

B.Tech. III Year I Sem. **L T P C**
Course Code: EC503PC **4 1 0 4**

Course Objectives:

- To understand the functional block diagram of Digital communication system.
- To understand the need for source and channel coding.
- To study various source and channel coding techniques.
- To understand a mathematical model of digital communication system for bit error rate analysis of different digital communication systems.

Course Outcomes: At the end of the course, the student will be able to:

- Understand basic components of Digital Communication Systems.
- Design optimum receiver for Digital Modulation techniques.
- Analyze the error performance of Digital Modulation Techniques.
- Understand the redundancy present in Digital Communication by using various source coding techniques.
- Know about different error detecting and error correction codes like block codes, cyclic codes and convolution codes.

UNIT - I

Elements of Digital Communication Systems: Model of Digital Communication Systems, Digital Representation of Analog Signal, Certain Issues in Digital Transmission, Advantages of Digital Communication Systems, Sampling Theorem, Types of Sampling – Impulse Sampling , Natural Sampling , Flat – Top Sampling. Introduction to Baseband Sampling.

Waveform Coding Techniques: PCM Generation and Reconstruction, Quantization Noise, Non Uniform Quantization and Companding, DPCM, Adaptive DPCM, DM and Adaptive DM, Noise in PCM and DM.

UNIT - II

Information theory: Information and Entropy, Conditional Entropy and Redundancy, Shannon-Fano Coding Mutual information, Information Loss due to Noise, Source coding- Huffman Code, Variable Length Coding, Lempel-ziv coding, Source coding to increase average information per bit, Lossy Source coding, Bandwidth-S/N Trade off, Hartley Shannon Law.

Error Control Codes

Linear Block Codes: Matrix Description of Linear Block Codes, Error Detection and Error Correction Capabilities of Linear Block Codes. Cyclic Codes: Algebraic Structure, Encoding, Syndrome Calculation, Decoding. **Convolution Codes:** Encoding, Decoding,

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UNIT - III

Baseband Pulse Transmission: Introduction, Matched Filter, Error Rate Due to Noise, intersymbol interference Nyquist's criterion for Distortionless Baseband Binary Transmission, Correlative -Level Coding Baseband M-Array PAM Transmission PAM Transmission, Digital subscriber Lines, Optimal Liner Receiver, Adaptive Equalization, Eye patterns.

Digital pass band transmission: pass band transmission model, Gram-Schmidt orthogonalization procedure, Geometric interpretation of signals Coherent detection of signals in noise, probability of error, Correlation receiver.

UNIT - IV

Digital Modulation Techniques: Introduction, ASK, ASK Modulator, Coherent ASK Detector, Non-Coherent ASK Detector, FSK, Bandwidth and Frequency Spectrum of FSK, Non Coherent FSK Detector, Coherent FSK Detector, FSK Detection using PLL, BPSK, Coherent PSK Detection, QPSK, 8-PSK, 16-PSK Differential PSK, QAM .

UNIT - V

Spread Spectrum Modulation: Use of Spread Spectrum, Direct Sequence Spread (DSSS), and Code Division Multiple Access, Ranging using DSSS, Frequency Hopping Spread Spectrum, PN - Sequence: Generation and characteristics, Synchronization in Spread Spectrum Systems.

TEXT BOOKS:

1. Communications system, S. Haykin, Wiley, 4 edition 2009.
2. Digital and Analog Communication Systems – Sam Shanmugam, John Wiley, 2005.

REFERENCES:

1. Principles of Communication Systems - Herbert Taub, Donald L Schiling, Goutam Saha, 3rd Edition, McGraw-Hill, 2008
2. Electronic communication systems, Wayne Tomasi, 5 edition, Pearson
3. Communication Systems: Analog and Digital, R. P. Singh , S. Sapre, McGraw-Hill Education, 2012
4. Digital Communications – John G. Proakis , Masoud Salehi – 5th Edition, McGraw-Hill, 2008.

DIGITAL COMMUNICATIONS LAB

B.Tech. III Year I Sem.
Course Code: EC507PC

L	T	P	C
0	0	3	2

Note:

- Perform any twelve experiments.
- Hardware Testing to be done

List of Experiments:

1. PCM Generation and Detection
2. Differential Pulse Code Modulation
3. Delta Modulation
4. Adaptive Delta modulation
5. Time Division Multiplexing of 2 Band Limited Signals
6. Frequency Shift Keying: Generation and Detection
7. Phase Shift Keying: Generation and Detection
8. Amplitude Shift Keying: Generation and Detection
9. Study of the spectral characteristics of PAM
10. Study of the spectral characteristics of PWM
11. Study of the spectral characteristics of QAM.
12. DPSK :Generation and Detection
13. QPSK : Generation and Detection
14. OFDM: Generation and Detection

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MICROPROCESSORS AND MICROCONTROLLERS

B.Tech. III Year II Sem. **L T P C**
Course Code: EC602PC **4 0 0 4**

Course Objectives:

- To develop an understanding of the operations of microprocessors and micro controllers; machine language programming and interfacing techniques.

Course Outcomes:

- Understands the internal architecture and organization of 8086, 8051 and ARM processors/controllers.
- Understands the interfacing techniques to 8086 and 8051 and can develop assembly language programming to design microprocessor/ micro controller based systems.

UNIT - I

8086 Architecture: 8086 Architecture-Functional diagram, Register Organization, Memory Segmentation, Programming Model, Memory addresses, Physical Memory Organization, Architecture of 8086, Signal descriptions of 8086, interrupts of 8086.

Instruction Set and Assembly Language Programming of 8086: Instruction formats, Addressing modes, Instruction Set, Assembler Directives, Macros, and Simple Programs involving Logical, Branch and Call Instructions, Sorting, String Manipulations.

UNIT - II

Introduction to Microcontrollers: Overview of 8051 Microcontroller, Architecture, I/O Ports, Memory Organization, Addressing Modes and Instruction set of 8051.

8051 Real Time Control: Programming Timer Interrupts, Programming External Hardware Interrupts, Programming the Serial Communication Interrupts, Programming 8051 Timers and Counters

UNIT – III

I/O And Memory Interface: LCD, Keyboard, External Memory RAM, ROM Interface, ADC, DAC Interface to 8051.

Serial Communication and Bus Interface: Serial Communication Standards, Serial Data Transfer Scheme, On board Communication Interfaces-I2C Bus, SPI Bus, UART; External Communication Interfaces-RS232,USB.

UNIT – IV

ARM Architecture: ARM Processor fundamentals, ARM Architecture – Register, CPSR, Pipeline, exceptions and interrupts interrupt vector table, ARM instruction set – Data processing, Branch instructions, load store instructions, Software interrupt instructions, Program status register instructions, loading constants, Conditional execution, Introduction to Thumb instructions.

UNIT – V

Advanced ARM Processors: Introduction to CORTEX Processor and its architecture, OMAP Processor and its Architecture.

TEXT BOOKS:

1. Advanced Microprocessors and Peripherals – A. K. Ray and K.M. Bhurchandani, MHE, 2nd Edition 2006.
2. The 8051 Microcontroller, Kenneth. J. Ayala, Cengage Learning, 3rd Ed.
3. ARM System Developers guide, Andrew N SLOSS, Dominic SYMES, Chris WRIGHT, Elsevier, 2012

REFERENCE BOOKS:

1. Microprocessors and Interfacing, D. V. Hall, MGH, 2nd Edition 2006.
2. Introduction to Embedded Systems, Shibu K.V, MHE, 2009
3. The 8051Microcontrollers, Architecture and Programming and Applications -K.Uma Rao, Andhe Pallavi, Pearson, 2009.

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MICROPROCESSORS AND MICROCONTROLLERS LAB

B.Tech. III Year II Sem.	**L T P C**
Course Code: EC605PC	**0 0 3 2**

Note: - Minimum of 12 experiments to be conducted.
The following programs/experiments are to be written for assembler and to be executed the same with 8086 and 8051 kits.

List of Experiments:

1. Programs for 16 bit arithmetic operations 8086(using various addressing modes)
2. Programs for sorting an array for 8086.
3. Programs for searching for a number of characters in a string for 8086.
4. Programs for string manipulation for 8086.
5. Programs for digital clock design using 8086.
6. Interfacing ADC and DAC to 8086.
7. Parallel communication between two microprocessor kits using 8255.
8. Serial communication between two microprocessor kits using 8251.
9. Interfacing to 8086 and programming to control stepper motor.
10. Programming using arithmetic, logical and bit manipulation instructions of 8051.
11. Program and verify Timer/Counter in 8051.
12. Program and verify interrupt handling in 8051.
13. UART operation in 8051.
14. Communication between 8051 kit and PC
15. Interfacing LCD to 8051
16. Interfacing Matrix/Keyboard to 8051
17. Data transfer from peripheral to memory through DMA controller 8237/8257

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DIGITAL SIGNAL PROCESSING

B.Tech. III Year II Sem. | **L T P C**
Course Code: EC603PC | **4 0 0 4**

Course Objectives: This course is an essential course that provides design techniques for processing all type of signals in various fields. The main objectives are:

- To provide background and fundamental material for the analysis and processing of digital signals.
- To familiarize the relationships between continuous-time and discrete time signals and systems.
- To study fundamentals of time, frequency and Z-plane analysis and to discuss the inter-relationships of these analytic method.
- To study the designs and structures of digital (IIR and FIR) filters from analysis to synthesis for a given specifications.
- The impetus is to introduce a few real-world signal processing applications.
- To acquaint in FFT algorithms, Multi-rate signal processing techniques and finite word length effects.

Course Outcomes: On completion of this subject, the student should be able to:

- Perform time, frequency, and Z -transform analysis on signals and systems.
- Understand the inter-relationship between DFT and various transforms.
- Understand the significance of various filter structures and effects of round off errors.
- Design a digital filter for a given specification.
- Understand the fast computation of DFT and appreciate the FFT processing.
- Understand the tradeoffs between normal and multi rate DSP techniques and finite length word effects.

UNIT - I

Introduction: Introduction to Digital Signal Processing: Discrete Time Signals & Sequences, conversion of continuous to discrete signal, Normalized Frequency, Linear Shift Invariant Systems, Stability, and Causality, linear differential equation to difference equation, Linear Constant Coefficient Difference Equations, Frequency Domain Representation of Discrete Time Signals and Systems

Realization of Digital Filters: Applications of Z – Transforms, Solution of Difference Equations of Digital Filters, System Function, Stability Criterion, Frequency Response of Stable Systems, Realization of Digital Filters – Direct, Canonic, Cascade and Parallel Forms.

UNIT - II

Discrete Fourier Transforms: Properties of DFT, Linear Convolution of Sequences using DFT, Computation of DFT: Over-Lap Add Method, Over-Lap Save Method, Relation between DTFT, DFS, DFT and Z-Transform.

Fast Fourier Transforms: Fast Fourier Transforms (FFT) - Radix-2 Decimation-in-Time and Decimation-in-Frequency FFT Algorithms, Inverse FFT, and FFT with General Radix-N.

UNIT - III

IIR Digital Filters: Analog filter approximations – Butterworth and Chebyshev, Design of IIR Digital Filters from Analog Filters, Step and Impulse Invariant Techniques, Bilinear Transformation Method, Spectral Transformations.

UNIT - IV

FIR Digital Filters: Characteristics of FIR Digital Filters, Frequency Response, Design of FIR Filters: Fourier Method, Digital Filters using Window Techniques, Frequency Sampling Technique, Comparison of IIR & FIR filters.

UNIT - V

Multirate Digital Signal Processing: Introduction, Down Sampling, Decimation, Upsampling, Interpolation, Sampling Rate Conversion, Conversion of Band Pass Signals, Concept of Resampling, Applications of Multi Rate Signal Processing.

Finite Word Length Effects: Limit cycles, Overflow Oscillations, Round-off Noise in IIR Digital Filters, Computational Output Round off Noise, Methods to Prevent Overflow, Trade off between Round Off and Overflow Noise, Measurement of Coefficient Quantization Effects through Pole-Zero Movement, Dead Band Effects.

TEXT BOOKS:

1. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G. Manolakis, Pearson Education / PHI, 2007.
2. Discrete Time Signal Processing – A. V. Oppenheim and R.W. Schaffer, PHI, 2009
3. Fundamentals of Digital Signal Processing – Loney Ludeman, John Wiley, 2009

REFERENCES:

1. Digital Signal Processing – Fundamentals and Applications – Li Tan, Elsevier, 2008
2. Fundamentals of Digital Signal Processing using MATLAB – Robert J. Schilling, Sandra L. Harris, Thomson, 2007
3. Digital Signal Processing - A Practical approach, Emmanuel C. Ifeachor and Barrie W. Jervis, 2nd Edition, Pearson Education, 2009

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DIGITAL SIGNAL PROCESSING LAB

B.Tech. III Year II Sem. **L T P C**
Course Code: EC604PC **0 0 3 2**

Note:

1. The Programs shall be implemented in Software (Using MATLAB / Lab View / C Programming/ Equivalent) and Hardware (Using TI / Analog Devices / Motorola / Equivalent DSP processors).
2. Minimum of 12 experiments to be conducted.

List of Experiments

1. Generation of Sinusoidal Waveform / Signal based on Recursive Difference Equations
2. Histogram of White Gaussian Noise and Uniformly Distributed Noise.
3. To find DFT / IDFT of given DT Signal
4. To find Frequency Response of a given System given in Transfer Function/ Differential equation form.
5. Obtain Fourier series coefficients by formula and using FET and compare for half sine wave.
6. Implementation of FFT of given Sequence
7. Determination of Power Spectrum of a given Signal(s).
8. Implementation of LP FIR Filter for a given Sequence/Signal.
9. Implementation of HP IIR Filter for a given Sequence/Signal
10. Generation of Narrow Band Signal through Filtering
11. Generation of DTMF Signals
12. Implementation of Decimation Process
13. Implementation of Interpolation Process
14. Implementation of I/D Sampling Rate Converters
15. Impulse Response of First order and Second Order Systems.

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ADVANCED ENGLISH COMMUNICATION SKILLS (AECS) LAB

B.Tech. III Year II Sem. **L T P C**
Course Code: EN606HS **0 0 3 2**

Introduction
A course on *Advanced English Communication Skills (AECS) Lab* is considered essential at the third year level of B.Tech and B.Pharmacy courses. At this stage, the students need to prepare themselves for their career which requires them to listen to, read, speak and write in English both for their professional and interpersonal communication. The main purpose of this course is to prepare the students of Engineering for their placements.

Course Objectives: This Lab focuses on using multi-media instruction for language development to meet the following targets:

- To improve students' fluency in spoken English
- To enable them to listen to English spoken at normal conversational speed
- To help students develop their vocabulary
- To read and comprehend texts in different contexts
- To communicate their ideas relevantly and coherently in writing
- To make students industry-ready
- To help students acquire behavioural skills for their personal and professional life
- To respond appropriately in different socio-cultural and professional contexts

Course Outcomes: Students will be able to:

- Acquire vocabulary and use it contextually
- Listen and speak effectively
- Develop proficiency in academic reading and writing
- Increase possibilities of job prospects
- Communicate confidently in formal and informal contexts

Syllabus
The following course activities will be conducted as part of the Advanced English Communication Skills (AECS) Lab:

1. **Inter-personal Communication and Building Vocabulary** - Starting a Conversation – Responding Appropriately and Relevantly – Using Appropriate Body Language – Role Play in Different Situations - Synonyms and Antonyms, One-word Substitutes, Prefixes and Suffixes, Idioms and Phrases and Collocations.
2. **Reading Comprehension** –General Vs Local Comprehension, Reading for Facts, Guessing Meanings from Context, , Skimming, Scanning, Inferring Meaning.
3. **Writing Skills** – Structure and Presentation of Different Types of Writing – Letter Writing/Resume Writing/ e-correspondence/ Technical Report Writing.
4. **Presentation Skills** – Oral Presentations (individual or group) through JAM Sessions/Seminars/PPTs and Written Presentations through Posters/Projects/Reports/ e-mails/Assignments… etc.,
5. **Group Discussion and Interview Skills** – Dynamics of Group Discussion, Intervention, Summarizing, Modulation of Voice, Body Language, Relevance, Fluency and Organization of Ideas and Rubrics of Evaluation- Concept and Process,

Pre-interview Planning, Opening Strategies, Answering Strategies, Interview through Tele-conference & Video-conference and Mock Interviews.

Minimum Hardware Requirement:
Advanced English Communication Skills (AECS) Laboratory shall have the following infrastructural facilities to accommodate at least 35 students in the lab:

- **Spacious room with appropriate acoustics**
- **Eight round tables with five movable chairs for each table.**
- **Audio-visual aids**
- **LCD Projector**
- **Public Address system**
- **Computer with suitable configuration**

Suggested Software: The software consisting of the prescribed topics elaborated above should be procured and used.

- **Oxford Advanced Learner's Compass, 8th Edition**
- **DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice.**

REFERENCES:

1. Kumar, Sanjay and Pushp Lata. ***English for Effective Communication***, Oxford University Press, 2015.
2. Konar, Nira. ***English Language Laboratories – A Comprehensive Manual***, PHI Learning Pvt. Ltd., 2011.

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IVYEAR I SEMESTER

Code	Subject	L	T/P/D	C
A70014	Management Science	4	-	4
A70442	Microwave Engineering	4	-	4
A70515	Computer Networks	4	-	4
A70434	Cellular and Mobile Communications	4	-	4
	Elective -I:	4	-	4
A70436	Digital Image Processing			
A70443	Multimedia and Signal Coding			
A70505	Object Oriented Programming through Java			
	Elective -II:	4	-	4
A70447	Television Engineering			
A70444	Optical Communications			
A70440	Embedded Systems Design			
A70086	Advanced Communication Skills Lab.	-	3	2
A70499	Microwave Engineering and Digital Communications Lab	-	3	2
	Total	**24**	**6**	**28**

IV YEAR II SEMESTER

Code	Subject	L	T/P/D	C
	Elective -III:	4	-	4
A80452	Satellite Communications			
A81102	Biomédical Instrumentation			
A80527	Artificial Neural Networks			
	Elective -IV:	4	-	4
A80431	Telecommunication Switching Systems and Networks			
A80450	Radar Systems			
A80449	Network Security			
	Elective -V:	4	-	4
A80454	Wireless Communications and Networks			
A80437	Digital Signal Processors and Architectures			
A80451	RF Circuit Design			
A80087	Industry Oriented Mini Project	-	-	2
A80089	Seminar	-	6	2
A80088	Major Project Work	-	15	10
A80090	Comprehensive Viva	-	-	2
	Total	**12**	**21**	**28**

Note: All End Examinations (Theory and Practical) are of three hours duration.
T-Tutorial L – Theory P – Practical D-Drawing C – Credits

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

IV Year B.Tech. ECE-I Sem	L	T/P/D	C
	4	-/-/-	4

(A70442) MICROWAVE ENGINEERING

Course Objectives:

The objectives of the course are:

- To develop the knowledge on transmission lines for microwaves, cavity resonators and wave guide components and applications.
- To enable the students understand and analyze the operation of Microwave tubes like klystron, magnetron, travelling wave tube, etc.,
- To familiarize with microwave solid state devices.
- To understand the scattering matrix parameters and its use.
- To introduce the student the microwave test bench for measure different parameters like attenuation, VSWR, etc.,

UNIT-I:

Microwave Transmission Lines - I: Introduction, Microwave Spectrum and Bands, Applications of Microwaves. Rectangular Waveguides – Solution of Wave Equations in Rectangular Coordinates, TE/TM mode analysis, Expressions for Fields, Characteristic Equation and Cut-off Frequencies, Filter Characteristics, Dominant and Degenerate Modes, Sketches of TE and TM mode fields in the cross-section, Mode Characteristics – Phase and Group Velocities, Wavelengths and Impedance Relations, Illustrative Problems.

Rectangular Guides: Power Transmission and Power Losses, Impossibility of TEM Mode, Micro strip Lines– Introduction, Zo Relations, Effective Dielectric Constant, Losses, Q factor.

UNIT-II:

Cavity Resonators– Introduction, Rectangular Cavities, Dominant Modes and Resonant Frequencies, Q Factor and Coupling Coefficients, Illustrative Problems

Waveguide Components and Applications: Coupling Mechanisms – Probe, Loop, Aperture types. Waveguide Discontinuities – Waveguide Windows, Tuning Screws and Posts, Matched Loads. Waveguide Attenuators – Different Types, Resistive Card and Rotary Vane Attenuators; Waveguide Phase Shifters – Types, Dielectric and Rotary Vane Phase Shifters, Waveguide Multiport Junctions – E plane and H plane Tees, Magic Tee. Directional Couplers – 2 Hole, Bethe Hole types, Illustrative Problems

Ferrites– Composition and Characteristics, Faraday Rotation, Ferrite

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Components – Gyrator, Isolator, Circulator.

UNIT-III:

Microwave Tubes: Limitations and Losses of conventional Tubes at Microwave Frequencies, Microwave Tubes – O Type and M Type Classifications, O-type Tubes : 2 Cavity Klystrons – Structure, Reentrant Cavities, Velocity Modulation Process and Applegate Diagram, Bunching Process and Small Signal Theory – Expressions for O/P Power and Efficiency. Reflex Klystrons – Structure, Velocity Modulation and Applegate Diagram, Mathematical Theory of Bunching, Power Output, Efficiency, Oscillating Modes and O/P Characteristics, Effect of Repeller Voltage on Power O/P, Illustrative Problems.

Helix TTS: Significance, Types and Characteristics of Slow Wave Structures; Structure of TW T and Amplification Process (qualitative treatment), Suppression of Oscillations, Gain Considerations.

UNIT-IV:

M-Type Tubes:

Introduction, Cross-field Effects, Magnetrons – Different Types, Cylindrical Traveling Wave Magnetron – Hull Cut-off and Hartree Conditions, Modes of Resonance and PI-Mode Operation, Separation of PI-Mode, o/p characteristics, Illustrative Problems

Microwave Solid State Devices: Introduction, Classification, Applications. TEDs – Introduction, Gunn Diodes – Principle, RWH Theory, Characteristics, Basic Modes of Operation - Gunn Oscillation Modes, LSA Mode, Introduction to Avalanche Transit Time Devices.

UNIT-V:

Microwave Measurements: Scattering Matrix– Significance, Formulation and Properties, S Matrix Calculations for – 2 port Junctions, E plane and H plane Tees, Magic Tee, Circulator and Isolator, Illustrative Problems.

Description of Microwave Bench – Different Blocks and their Features, Errors and Precautions, Microwave Power Measurement, Bolometers Measurement of Attenuation, Frequency Standing Wave Measurements – Measurement of Low and High VSWR, Cavity Q, Impedance Measurements.

TEXT BOOKS:

1. Microwave Devices and Circuits – Samuel Y. Liao, Pearson, 3rd Edition, 2003.
2. Microwave Principles – Herbert J. Reich, J.G. Skalnik, P.F. Ordung and H.L. Krauss, CBS Publishers and Distributors, New Delhi, 2004.

REFERENCE BOOKS:

1. Foundations for Microwave Engineering – R.E. Collin, IEEE Press, John Wiley, 2ndEdition, 2002.

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2. Microwave Circuits and Passive Devices – M.L. Sisodia and G.S.Raghuvanshi, W iley Eastern Ltd.,New Age International Publishers Ltd., 1995.
3. Microwave Engineering Passive Circuits – Peter A. Rizzi, PHI, 1999.
4. Electronic and Radio Engineering – F.E. Terman, McGraw-Hill, 4th Ed., 1955.
5. Microwave Engineering – A. Das and S.K. Das, TMH, 2nd Ed., 2009.
6. Microwave Engineering - G. S. Raghuvanshi and K. Satya Prasad, Cengage Learning, 2012.

Course Outcomes:

Upon completion of the course, the students will be able to:

- Understand the significance of microwaves and microwave transmission lines.
- Analyze the characteristics of microwave tubes and compare them.
- Be able to list and explain the various microwave solid state devices.
- Can set up a microwave bench for measuring microwave parameters.

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IV Year B.Tech. ECE-I Sem	L	T/P/D	C
	-	-/3/-	2

(A70499) MICROWAVE ENGINEERING AND DIGITAL COMMUNICATIONS LAB

Note: Minimum 12 Experiments to be conducted

Part – A: Microwave Engineering Lab (Any 6 Experiments):

1. Reflex Klystron Characteristics
2. Gunn Diode Characteristics
3. Directional Coupler Characteristics
4. VSWR Measurement
5. Measurement of Waveguide Parameters
6. Measurement of Impedance of a given Load
7. Measurement of Scattering parameters of a Magic Tee
8. Measurement of Scattering parameters of a Circulator
9. Attenuation Measurement
10. Microwave Frequency Measurement

Part – B: Digital Communication Lab (Any 6 Experiments):

1. PCM Generation and Detection
2. Differential Pulse Code Modulation
3. Delta Modulation
4. Time Division Multiplexing of 2 Band Limited Signals
5. Frequency shift keying: Generation and Detection
6. Phase Shift Keying: Generation and Detection
7. Amplitude Shift Keying: Generation and Detection
8. Study of the spectral characteristics of PAM, QAM
9. DPSK :Generation and Detection
10. QPSK : Generation and Detection

Equipment required for the Laboratory:

Microwave Engineering Lab:

1. Microwave Bench set up with Klystron Power Supply
2. Microwave Bench set up with Gunn Power Supply
3. Micro Ammeter
4. VSWR meter

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5. Microwave Components

Digital Communication Lab:

1. RPS: 0-30V
2. CRO: 0-20MHz
3. Function Generators: 0-1MHz
4. RF Generators: 0-100MHz
5. Experimental Kits /Modules

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Building Vocabulary - Starting a conversation – responding appropriately and relevantly – using the right body language – Role Play in different situations & Discourse Skills- using visuals - Synonyms and antonyms, word roots, one-word substitutes, prefixes and suffixes, study of word origin, business vocabulary, analogy, idioms and phrases, collocations & usage of vocabulary.

2. Activities on Reading Comprehension –General Vs Local comprehension, reading for facts, guessing meanings from context, scanning, skimming, inferring meaning, critical reading & effective googling.
3. Activities on Writing Skills – Structure and presentation of different types of writing – letter writing/Resume writing/ e-correspondence/ Technical report writing/ Portfolio writing – planning for writing – improving one's writing.
4. Activities on Presentation Skills – Oral presentations (individual and group) through JAM sessions/seminars/PPTs and written presentations through posters/projects/reports/ e-mails/assignments etc.
5. Activities on Group Discussion and Interview Skills – Dynamics of group discussion, intervention, summarizing, modulation of voice, body language, relevance, fluency and organization of ideas and rubrics for evaluation- Concept and process, pre-interview planning, opening strategies, answering strategies, interview through tele-conference & video-conference and Mock Interviews.

Minimum Requirement:

The Advanced Communication Skills (ACS) Laboratory shall have the following infra-structural facilities to accommodate at least 35 students in the lab:

- Spacious room with appropriate acoustics.
- Round Tables with movable chairs
- Audio-visual aids
- LCD Projector
- Public Address system
- P – IV Processor, Hard Disk – 80 GB, RAM–512 MB Minimum, Speed – 2.8 GHZ
- T. V, a digital stereo & Camcorder
- Headphones of High quality

Prescribed Lab Manual: A book titled A Course Book of Advanced Communication Skills (ACS) Lab published by Universities Press, Hyderabad.

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Suggested Software:

The software consisting of the prescribed topics elaborated above should be procured and used.

- Oxford Advanced Learner's Compass, 7th Edition
- DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice.
- Lingua TOEFL CBT Insider, by Dreamtech
- TOEFL & GRE(KAPLAN, AARCO & BARRONS, USA, Cracking GRE by CLIFFS)
- The following software from 'train2success.com'
    - Ø Preparing for being Interviewed
    - Ø Positive Thinking
    - Ø Interviewing Skills
    - Ø Telephone Skills
    - Ø Time Management

Books Recommended:

1. Technical Communication by Meenakshi Raman & Sangeeta Sharma, Oxford University Press 2009.
2. Advanced Communication Skills Laboratory Manual by Sudha Rani, D, Pearson Education 2011.
3. Technical Communication by Paul V. Anderson. 2007. Cengage Learning pvt. Ltd. New Delhi.
4. Business and Professional Communication: Keys for Workplace Excellence. Kelly M. Quintanilla & Shawn T. Wahl. Sage South Asia Edition. Sage Publications. 2011.
5. The Basics of Communication: A Relational Perspective. Steve Duck & David T. McMahan. Sage South Asia Edition. Sage Publications. 2012.
6. English Vocabulary in Use series, Cambridge University Press 2008.
7. Management Shapers Series by Universities Press(India)Pvt Ltd., Himayatnagar, Hyderabad 2008.
8. Handbook for Technical Communication by David A. McMurrey & Joanne Buckley. 2012. CengageLearning.
9. Communication Skills by Leena Sen, PHI Learning Pvt Ltd., New Delhi, 2009.
10. Handbook for Technical Writing by David A McMurrey & Joanne Buckely CENGAGE Learning 2008.

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College Code - TP

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ELECTRONICS AND COMMUNICATION ENGINEERING

2019-2020

S. No	Regulations	Number of Courses	Year of Study
1	R18	23	I and II years I&II Semesters
2	R16	17	III and IV year I & II Semesters

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
B.Tech. in ELECTRONICS AND COMMUNICATION ENGINEERING
COURSE STRUCTURE & SYLLABUS (R18)

Applicable From 2018-19 Admitted Batch

I YEAR I SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA101BS	Mathematics - I	3	1	0	4
2	AP102BS	Applied Physics	3	1	0	4
3	CS103ES	Programming for Problem Solving	3	1	0	4
4	ME104ES	Engineering Graphics	1	0	4	3
5	AP105BS	Applied Physics Lab	0	0	3	1.5
6	CS106ES	Programming for Problem Solving Lab	0	0	3	1.5
7	*MC109ES	Environmental Science	3	0	0	0
		Induction Programme				
		Total Credits	**13**	**3**	**10**	**18**

I YEAR II SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA201BS	Mathematics - II	3	1	0	4
2	CH202BS	Chemistry	3	1	0	4
3	EE203ES	Basic Electrical Engineering	3	0	0	3
4	ME205ES	Engineering Workshop	1	0	3	2.5
5	EN205HS	English	2	0	0	2
6	CH206BS	Engineering Chemistry Lab	0	0	3	1.5
7	EN207HS	English Language and Communication Skills Lab	0	0	2	1
8	EE208ES	Basic Electrical Engineering Lab	0	0	2	1
		Total Credits	**12**	**2**	**10**	**19**

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AP102BS/AP202BS: APPLIED PHYSICS

B.Tech. I Year I Sem.

L	T	P	C
3	1	0	4

Course Objectives:

- Students will demonstrate skills in scientific inquiry, problem solving and laboratory techniques.
- Students will be able to demonstrate competency and understanding of the concepts found in Quantum Mechanics, Fiber optics and lasers, Semiconductor physics and Electromagnetic theory and a broad base of knowledge in physics.
- The graduates will be able to solve non-traditional problems that potentially draw on knowledge in multiple areas of physics.
- To study applications in engineering like memory devices, transformer core and electromagnetic machinery.

Course Outcomes: Upon graduation:

- The student would be able to learn the fundamental concepts on Quantum behaviour of matter in its micro state.
- The knowledge of fundamentals of Semiconductor physics, Optoelectronics, Lasers and fibre optics enable the students to apply to various systems like communications, solar cell, photo cells and so on.
- Design, characterization and study of properties of material help the students to prepare new materials for various engineering applications.
- The course also helps the students to be exposed to the phenomena of electromagnetism and also to have exposure on magnetic materials and dielectric materials.

UNIT-I: Quantum Mechanics

Introduction to quantum physics, Black body radiation, Planck's law, Photoelectric effect, Compton effect, de-Broglie's hypothesis, Wave-particle duality, Davisson and Germer experiment, Heisenberg's Uncertainty principle, Born's interpretation of the wave function, Schrodinger's time independent wave equation, Particle in one dimensional box.

UNIT-II: Semiconductor Physics

Intrinsic and Extrinsic semiconductors, Dependence of Fermi level on carrier-concentration and temperature, Carrier generation and recombination, Carrier transport: diffusion and drift, Hall effect, p- n junction diode, Zener diode and their V-I Characteristics, Bipolar Junction Transistor (BJT): Construction, Principle of operation.

UNIT-III: Optoelectronics

Radiative and non-radiative recombination mechanisms in semiconductors, LED and semiconductor lasers: Device structure, Materials, Characteristics and figures of merit, Semiconductor photodetectors: Solar cell, PIN and Avalanche and their structure, Materials, working principle and Characteristics.

UNIT-IV: Lasers and Fibre Optics

Lasers: Introduction to interaction of radiation with matter, Coherence, Principle and working of Laser, Population inversion, Pumping, Types of Lasers: Ruby laser, Carbon dioxide (CO_2) laser, He-Ne laser, Applications of laser. Fibre Optics: Introduction, Optical fibre as a dielectric wave guide, Total internal reflection, Acceptance angle, Acceptance cone and Numerical aperture, Step and Graded index fibres, Losses associated with optical fibres, Applications of optical fibres.

UNIT-V: Electromagnetism and Magnetic Properties of Materials

Laws of electrostatics, Electric current and the continuity equation, Ampere's and Faraday's laws, Maxwell's equations, Polarisation, Permittivity and Dielectric constant, Internal fields in a solid, Clausius-Mossotti equation, Ferroelectrics and Piezoelectrics. Magnetisation, permeability and susceptibility, Classification of magnetic materials, Ferromagnetism and ferromagnetic domains, Hysteresis, Applications of magnetic materials.

TEXT BOOKS:

1. Engineering Physics, B.K. Pandey, S. Chaturvedi - Cengage Learing.
2. Halliday and Resnick, Physics - Wiley.
3. A textbook of Engineering Physics, Dr. M. N. Avadhanulu, Dr. P.G. Kshirsagar - S. Chand

REFERENCE BOOKS:

1. Richard Robinett, Quantum Mechanics
2. J. Singh, Semiconductor Optoelectronics: Physics and Technology, Mc Graw-Hill inc. (1995).
3. Online Course: "Optoelectronic Materials and Devices" by Monica Katiyar and Deepak Guptha on NPTEL

AP105BS/AP205BS: APPLIED PHYSICS LAB

B.Tech. I Year I Sem.

L	T	P	C
0	0	3	1.5

List of Experiments:

1. Energy gap of P-N junction diode:
   To determine the energy gap of a semiconductor diode.

2. Solar Cell:
   To study the V-I Characteristics of solar cell.

3. Light emitting diode:
   Plot V-I and P-I characteristics of light emitting diode.

4. Stewart – Gee's experiment:
   Determination of magnetic field along the axis of a current carrying coil.

5. Hall effect:
   To determine Hall co-efficient of a given semiconductor.

6. Photoelectric effect:
   To determine work function of a given material.

7. LASER:
   To study the characteristics of LASER sources.

8. Optical fibre:
   To determine the bending losses of Optical fibres.

9. LCR Circuit:
   To determine the Quality factor of LCR Circuit.

10. R-C Circuit:
    To determine the time constant of R-C circuit.

Note: Any 8 experiments are to be performed

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CS103ES/CS203ES: PROGRAMMING FOR PROBLEM SOLVING

B.Tech. I Year I Sem.

L	T	P	C
3	1	0	4

Course Objectives:

- To learn the fundamentals of computers.
- To understand the various steps in program development.
- To learn the syntax and semantics of C programming language.
- To learn the usage of structured programming approach in solving problems.

Course Outcomes: The student will learn

- To write algorithms and to draw flowcharts for solving problems.
- To convert the algorithms/flowcharts to C programs.
- To code and test a given logic in C programming language.
- To decompose a problem into functions and to develop modular reusable code.
- To use arrays, pointers, strings and structures to write C programs.
- Searching and sorting problems.

UNIT - I: Introduction to Programming

Introduction to components of a computer system: disks, primary and secondary memory, processor, operating system, compilers, creating, compiling and executing a program etc., Number systems Introduction to Algorithms: steps to solve logical and numerical problems. Representation of Algorithm, Flowchart/Pseudo code with examples, Program design and structured programming Introduction to C Programming Language: variables (with data types and space requirements), Syntax and Logical Errors in compilation, object and executable code , Operators, expressions and precedence, Expression evaluation, Storage classes (auto, extern, static and register), type conversion, The main method and command line arguments

Bitwise operations: Bitwise AND, OR, XOR and NOT operators

Conditional Branching and Loops: Writing and evaluation of conditionals and consequent branching with if, if-else, switch-case, ternary operator, goto, Iteration with for, while, do-while loops

I/O: Simple input and output with scanf and printf, formatted I/O, Introduction to stdin, stdout and stderr. Command line arguments

UNIT - II: Arrays, Strings, Structures and Pointers:

Arrays: one and two dimensional arrays, creating, accessing and manipulating elements of arrays Strings: Introduction to strings, handling strings as array of characters, basic string functions available in C (strlen, strcat, strcpy, strstr etc.), arrays of strings

Structures: Defining structures, initializing structures, unions, Array of structures

Pointers: Idea of pointers, Defining pointers, Pointers to Arrays and Structures, Use of Pointers in self-referential structures, usage of self referential structures in linked list (no implementation) Enumeration data type

UNIT - III: Preprocessor and File handling in C:

Preprocessor: Commonly used Preprocessor commands like include, define, undef, if, ifdef, ifndef Files: Text and Binary files, Creating and Reading and writing text and binary files, Appending data to existing files, Writing and reading structures using binary files, Random access using fseek, ftell and rewind functions.

UNIT - IV: Function and Dynamic Memory Allocation:

Functions: Designing structured programs, Declaring a function, Signature of a function, Parameters and return type of a function, passing parameters to functions, call by value, Passing arrays to functions, passing pointers to functions, idea of call by reference, Some C standard functions and libraries

Recursion: Simple programs, such as Finding Factorial, Fibonacci series etc., Limitations of Recursive functions Dynamic memory allocation: Allocating and freeing memory, Allocating memory for arrays of different data types.

UNIT - V: Introduction to Algorithms:

Algorithms for finding roots of a quadratic equations, finding minimum and maximum numbers of a given set, finding if a number is prime number, etc.

Basic searching in an array of elements (linear and binary search techniques),

Basic algorithms to sort array of elements (Bubble, Insertion and Selection sort algorithms), Basic concept of order of complexity through the example programs

CS106ES/CS206ES: PROGRAMMING FOR PROBLEM SOLVING LAB

B.Tech. I Year I Sem.

L	T	P	C
0	0	3	1.5

[Note: The programs may be executed using any available Open Source/ Freely available IDE
Some of the Tools available are:
CodeLite: https://codelite.org/
Code::Blocks: http://www.codeblocks.org/
DevCpp : http://www.bloodshed.net/devcpp.html
Eclipse: http://www.eclipse.org
This list is not exhaustive and is NOT in any order of preference]

Course Objectives: The students will learn the following:

- To work with an IDE to create, edit, compile, run and debug programs
- To analyze the various steps in program development.
- To develop programs to solve basic problems by understanding basic concepts in C like operators, control statements etc.
- To develop modular, reusable and readable C Programs using the concepts like functions, arrays etc.
- To Write programs using the Dynamic Memory Allocation concept.
- To create, read from and write to text and binary files

Course Outcomes: The candidate is expected to be able to:

- formulate the algorithms for simple problems
- translate given algorithms to a working and correct program
- correct syntax errors as reported by the compilers
- identify and correct logical errors encountered during execution
- represent and manipulate data with arrays, strings and structures
- use pointers of different types
- create, read and write to and from simple text and binary files
- modularize the code with functions so that they can be reused

Practice sessions:

a. Write a simple program that prints the results of all the operators available in C (including pre/ post increment, bitwise and/or/not, etc.). Read required operand values from standard input.
b. Write a simple program that converts one given data type to another using auto conversion and casting. Take the values form standard input.

Simple numeric problems:

a. Write a program for fiend the max and min from the three numbers.
b. Write the program for the simple, compound interest.
c. Write program that declares Class awarded for a given percentage of marks, where mark <40%= Failed, 40% to <60% = Second class, 60% to <70%=First class, >= 70% = Distinction. Read percentage from standard input.
d. Write a program that prints a multiplication table for a given number and the number of rows in the table. For example, for a number 5 and rows = 3, the output should be:
e. 5 x 1 = 5
f. 5 x 2 = 10
g. 5 x 3 = 15
h. Write a program that shows the binary equivalent of a given positive number between 0 to 255.

Expression Evaluation:

a. A building has 10 floors with a floor height of 3 meters each. A ball is dropped from the top of the building. Find the time taken by the ball to reach each floor. (Use the formula s = ut+(1/2)at^2 where u and a are the initial velocity in m/sec (= 0) and acceleration in m/sec^2 (= 9.8 m/s^2)).
b. Write a C program, which takes two integer operands and one operator from the user, performs the operation and then prints the result. (Consider the operators +,-,*, /, % and use Switch Statement)

c. Write a program that finds if a given number is a prime number
d. Write a C program to find the sum of individual digits of a positive integer and test given number is palindrome.
e. A Fibonacci sequence is defined as follows: the first and second terms in the sequence are 0 and 1. Subsequent terms are found by adding the preceding two terms in the sequence. Write a C program to generate the first n terms of the sequence.
f. Write a C program to generate all the prime numbers between 1 and n, where n is a value supplied by the user.
g. Write a C program to find the roots of a Quadratic equation.
h. Write a C program to calculate the following, where x is a fractional value.
i. 1-x/2 +x^2/4-x^3/6
j. Write a C program to read in two numbers, x and n, and then compute the sum of this geometric progression: 1+x+x^2+x^3+.............+x^n. For example: if n is 3 and x is 5, then the program computes 1+5+25+125.

Arrays and Pointers and Functions:

a. Write a C program to find the minimum, maximum and average in an array of integers.
b. Write a functions to compute mean, variance, Standard Deviation, sorting of n elements in single dimension array.
c. Write a C program that uses functions to perform the following:
d. Addition of Two Matrices
e. ii. Multiplication of Two Matrices
f. iii. Transpose of a matrix with memory dynamically allocated for the new matrix as row and column counts may not be same.
g. Write C programs that use both recursive and non-recursive functions
h. To find the factorial of a given integer.
i. ii. To find the GCD (greatest common divisor) of two given integers.
j. iii. To find x^n
k. Write a program for reading elements using pointer into array and display the values using array.
l. Write a program for display values reverse order from array using pointer.
m. Write a program through pointer variable to sum of n elements from array.

Files:

a. Write a C program to display the contents of a file to standard output device.
b. Write a C program which copies one file to another, replacing all lowercase characters with their uppercase equivalents.
c. Write a C program to count the number of times a character occurs in a text file. The file name and the character are supplied as command line arguments.
d. Write a C program that does the following:
It should first create a binary file and store 10 integers, where the file name and 10 values are given in the command line. (hint: convert the strings using atoi function)
Now the program asks for an index and a value from the user and the value at that index should be changed to the new value in the file. (hint: use fseek function)
The program should then read all 10 values and print them back.
e. Write a C program to merge two files into a third file (i.e., the contents of the firs t file followed by those of the second are put in the third file).

Strings:

a. Write a C program to convert a Roman numeral ranging from I to L to its decimal equivalent.
b. Write a C program that converts a number ranging from 1 to 50 to Roman equivalent
c. Write a C program that uses functions to perform the following operations:
d. To insert a sub-string in to a given main string from a given position.
e. ii. To delete n Characters from a given position in a given string.
f. Write a C program to determine if the given string is a palindrome or not (Spelled same in both directions with or without a meaning like madam, civic, noon, abcba, etc.)
g. Write a C program that displays the position of a character ch in the string S or – 1 if S doesn't contain ch.
h. Write a C program to count the lines, words and characters in a given text.

Miscellaneous:

a. Write a menu driven C program that allows a user to enter n numbers and then choose between finding the smallest, largest, sum, or average. The menu and all the choices are to be functions. Use a switch statement to determine what action to take. Display an error message if an invalid choice is entered.

b. Write a C program to construct a pyramid of numbers as follows:

```
1        *        1        1        *
1 2      * *      2 3      2 2      * *
1 2 3    * * *    4 5 6    3 3 3    * * *
                           4 4 4 4  * *
                                    *
```

Sorting and Searching:

a. Write a C program that uses non recursive function to search for a Key value in a given
b. list of integers using linear search method.
c. Write a C program that uses non recursive function to search for a Key value in a given
d. sorted list of integers using binary search method.
e. Write a C program that implements the Bubble sort method to sort a given list of
f. integers in ascending order.
g. Write a C program that sorts the given array of integers using selection sort in descending order
h. Write a C program that sorts the given array of integers using insertion sort in ascending order
i. Write a C program that sorts a given array of names

Suggested Reference Books for solving the problems:

i. Byron Gottfried, Schaum's Outline of Programming with C, McGraw-Hill
ii. B.A. Forouzan and R.F. Gilberg C Programming and Data Structures, Cengage Learning, (3rd Edition)
iii. Brian W. Kernighan and Dennis M. Ritchie, The C Programming Language, Prentice
iv. Hall of India
v. R.G. Dromey, How to solve it by Computer, Pearson (16th Impression)
vi. Programming in C, Stephen G. Kochan, Fourth Edition, Pearson Education.
vii. Herbert Schildt, C: The Complete Reference, Mc Graw Hill, 4th Edition

CH102BS/CH202BS: CHEMISTRY

B.Tech. I Year II Sem.

L	T	P	C
3	1	0	4

Course Objectives:

- To bring adaptability to the concepts of chemistry and to acquire the required skills to become a perfect engineer.
- To impart the basic knowledge of atomic, molecular and electronic modifications which makes the student to understand the technology based on them.
- To acquire the knowledge of electrochemistry, corrosion and water treatment which are essential for the Engineers and in industry.
- To acquire the skills pertaining to spectroscopy and to apply them for medical and other fields.
- To impart the knowledge of stereochemistry and synthetic aspects useful for understanding reaction pathways

Course Outcomes: The basic concepts included in this course will help the student to gain:

- The knowledge of atomic, molecular and electronic changes, band theory related to conductivity.
- The required principles and concepts of electrochemistry, corrosion and in understanding the problem of water and its treatments.
- The required skills to get clear concepts on basic spectroscopy and application to medical and other fields.
- The knowledge of configurational and conformational analysis of molecules and reaction mechanisms.

UNIT - I:

Molecular structure and Theories of Bonding: Atomic and Molecular orbitals. Linear Combination of Atomic Orbitals (LCAO), molecular orbitals of diatomic molecules, molecular orbital energy level diagrams of N_2, O_2 and F_2 molecules. π molecular orbitals of butadiene and benzene.

Crystal Field Theory (CFT): Salient Features of CFT – Crystal Field Splitting of transition metal ion d-orbitals in Tetrahedral, Octahedral and square planar geometries. Band structure of solids and effect of doping on conductance.

UNIT - II:

Water and its treatment: Introduction – hardness of water – Causes of hardness - Types of hardness: temporary and permanent – expression and units of hardness – Estimation of hardness of water by complexometric method. Potable water and its specifications. Steps involved in treatment of water – Disinfection of water by chlorination and ozonization. Boiler feed water and its treatment – Calgon conditioning, Phosphate conditioning and Colloidal conditioning. External treatment of water – Ion exchange process. Desalination of water – Reverse osmosis. Numerical problems.

UNIT - III:

Electrochemistry and corrosion: Electro chemical cells – electrode potential, standard electrode potential, types of electrodes – calomel, Quinhydrone and glass electrode. Nernst equation Determination of pH of a solution by using quinhydrone and glass electrode. Electrochemical series and its applications. Numerical problems. Potentiometric titrations. Batteries – Primary (Lithium cell) and secondary batteries (Lead – acid storage battery and Lithium ion battery).

Causes and effects of corrosion – theories of chemical and electrochemical corrosion – mechanism of electrochemical corrosion, Types of corrosion: Galvanic, water-line and pitting corrosion. Factors affecting rate of corrosion, Corrosion control methods- Cathodic protection – Sacrificial anode and impressed current cathodic methods. Surface coatings – metallic coatings – methods of application. Electroless plating of Nickel.

UNIT - IV:

Stereochemistry, Reaction Mechanism and synthesis of drug molecules: Introduction to representation of 3-dimensional structures, Structural and stereoisomers, configurations, symmetry and chirality. Enantiomers, diastereomers, optical activity and Absolute configuration. Conformation alanalysis of n- butane.

Substitution reactions: Nucleophilic substitution reactions: Mechanism of S_N1, S_N2 reactions. Electrophilic and nucleophilic addition reactions: Addition of HBr to propene. Markownikoff and anti

Markownikoff's additions. Grignard additions on carbonyl compounds. Elimination reactions: Dehydro halogenation of alkylhalides. Saytzeff rule. Oxidation reactions: Oxidation of alcohols using $KMnO_4$ and chromic acid.
Reduction reactions: reduction of carbonyl compounds using $LiAlH_4$ & $NaBH_4$. Hydroboration of olefins. Structure, synthesis and pharmaceutical applications of Paracetamol and Aspirin.

UNIT - V:
Spectroscopic techniques and applications: Principles of spectroscopy, selection rules and applications of electronic spectroscopy. vibrational and rotational spectroscopy. Basic concepts of Nuclear magnetic resonance Spectroscopy, chemical shift. Introduction to Magnetic resonance imaging.

TEXT BOOKS:

1. Physical Chemistry, P.W. Atkins, 10th Edn, Oxford University Press.
2. Engineering Chemistry by P.C.Jain & M.Jain; Dhanpat Rai Publishing Company (P) Ltd., New Delhi.
3. Fundamentals of Molecular Spectroscopy, by C.N. Banwell, 4th Edn, McGraw Hill Publishing.
4. Organic Chemistry: Structure and Function by K.P.C. Volhardt and N.E.Schore, 5th Edition, Macmillan International Higher Education.
5. University Chemistry, by B.M. Mahan, Pearson IV Edition.
6. Engineering Chemistry (NPTEL Web-book), by B.L. Tembe, Kamaluddin and M.S. Krishnan

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CH106BS/CH206BS: ENGINEERING CHEMISTRY LAB

B.Tech. I Year II Sem.

L	T	P	C
0	0	3	1.5

Course Objectives: The course consists of experiments related to the principles of chemistry required for engineering student. The student will learn:

- Estimation of hardness and chloride content in water to check its suitability for drinking purpose.
- To determine the rate constant of reactions from concentrations as an function of time.
- The measurement of physical properties like adsorption and viscosity.
- To synthesize the drug molecules and check the purity of organic molecules by thin layer chromatographic (TLC) technique.

Course Outcomes: The experiments will make the student gain skills on:

- Determination of parameters like hardness and chloride content in water.
- Estimation of rate constant of a reaction from concentration – time relationships.
- Determination of physical properties like adsorption and viscosity.
- Calculation of R_f values of some organic molecules by TLC technique.

List of Experiments:

1. Determination of total hardness of water by complexometric method using EDTA
2. Determination of chloride content of water by Argentometry
3. Estimation of an HCl by Conductometric titrations
4. Estimation of Acetic acid by Conductometric titrations
5. Estimation of HCl by Potentiometric titrations
6. Estimation of Fe^{2+} by Potentiometry using $KMnO_4$
7. Determination of rate constant of acid catalysed hydrolysis of methyl acetate
8. Synthesis of Aspirin and Paracetamol
9. Thin layer chromatography calculation of R_f values. eg ortho and para nitro phenols
10. Determination of acid value of coconut oil
11. Verification of freundlich adsorption isotherm-adsorption of acetic acid on charcoal
12. Determination of viscosity of castor oil and ground nut oil by using Ostwald's viscometer.
13. Determination of partition coefficient of acetic acid between n-butanol and water.
14. Determination of surface tension of a give liquid using stalagmometer.

References

1. Senior practical physical chemistry, B.D. Khosla, A. Gulati and V. Garg (R. Chand & Co., Delhi)
2. An introduction to practical chemistry, K.K. Sharma and D. S. Sharma (Vikas publishing, N. Delhi)
3. Vogel's text book of practical organic chemistry 5th edition
4. Text book on Experiments and calculations in Engineering chemistry – S.S. Dara

EE103ES/EE203ES: BASIC ELECTRICAL ENGINEERING

B.Tech. I Year II Sem.

L	T	P	C
3	0	0	3

Course Objectives:

- To introduce the concepts of electrical circuits and its components
- To understand magnetic circuits, DC circuits and AC single phase & three phase circuits
- To study and understand the different types of DC/AC machines and Transformers.
- To import the knowledge of various electrical installations.
- To introduce the concept of power, power factor and its improvement.

Course Outcomes:

- To analyze and solve electrical circuits using network laws and theorems.
- To understand and analyze basic Electric and Magnetic circuits
- To study the working principles of Electrical Machines
- To introduce components of Low Voltage Electrical Installations

UNIT-I: D.C. Circuits

Electrical circuit elements (R, L and C), voltage and current sources, KVL&KCL, analysis of simple circuits with dc excitation. Superposition, Thevenin and Norton Theorems.
Time-domain analysis of first-order RL and RC circuits.

UNIT-II: A.C. Circuits

Representation of sinusoidal waveforms, peak and rms values, phasor representation, real power, reactive power, apparent power, power factor, Analysis of single-phase ac circuits consisting of R, L, C, RL, RC, RLC combinations (series and parallel), resonance in series R-L-C circuit.
Three-phase balanced circuits, voltage and current relations in star and delta connections.

UNIT-III: Transformers

Ideal and practical transformer, equivalent circuit, losses in transformers, regulation and efficiency. Auto-transformer and three-phase transformer connections.

UNIT-IV: Electrical Machines

Generation of rotating magnetic fields, Construction and working of a three-phase induction motor, Significance of torque-slip characteristic. Loss components and efficiency, starting and speed control of induction motor. Single-phase induction motor. Construction, working, torque-speed characteristic and speed control of separately excited dc motor.
Construction and working of synchronous generators.

UNIT-V: Electrical Installations

Components of LT Switchgear: Switch Fuse Unit (SFU), MCB, ELCB, MCCB, Types of Wires and Cables, Earthing. Types of Batteries, Important Characteristics for Batteries. Elementary calculations for energy consumption, power factor improvement and battery backup.

TEXT /REFERENCE BOOKS:

1. Basic Electrical Engineering - D.P. Kothari and I.J. Nagrath, 3rd edition 2010, Tata McGraw Hill.
2. D.C. Kulshreshtha, "Basic Electrical Engineering", McGraw Hill, 2009.
3. L.S. Bobrow, Fundamentals of Electrical Engineering", Oxford University Press, 2011
4. Electrical and Electronics Technology, E. Hughes, 10th Edition, Pearson, 2010
5. Electrical Engineering Fundamentals, Vincent Deltoro, Second Edition, Prentice Hall India, 1989.

EE108ES/EE208ES: BASIC ELECTRICAL ENGINEERING LAB

B.Tech. I Year II Sem.

L	T	P	C
0	0	2	1

Course Objectives:

- To analyze a given network by applying various electrical laws and network theorems
- To know the response of electrical circuits for different excitations
- To calculate, measure and know the relation between basic electrical parameters.
- To analyze the performance characteristics of DC and AC electrical machines

Course Outcomes:

- Get an exposure to basic electrical laws.
- Understand the response of different types of electrical circuits to different excitations.
- Understand the measurement, calculation and relation between the basic electrical parameters
- Understand the basic characteristics of transformers and electrical machines.

List of experiments/demonstrations:

1. Verification of Ohms Law
2. Verification of KVL and KCL
3. Transient Response of Series RL and RC circuits using DC excitation
4. Transient Response of RLC Series circuit using DC excitation
5. Resonance in series RLC circuit
6. Calculations and Verification of Impedance and Current of RL, RC and RLC series circuits
7. Measurement of Voltage, Current and Real Power in primary and Secondary Circuits of a Single-Phase Transformer
8. Load Test on Single Phase Transformer (Calculate Efficiency and Regulation)
9. Three Phase Transformer: Verification of Relationship between Voltages and Currents (Star-Delta, Delta-Delta, Delta-star, Star-Star)
10. Measurement of Active and Reactive Power in a balanced Three-phase circuit
11. Performance Characteristics of a Separately/Self Excited DC Shunt/Compound Motor
12. Torque-Speed Characteristics of a Separately/Self Excited DC Shunt/Compound Motor
13. Performance Characteristics of a Three-phase Induction Motor
14. Torque-Speed Characteristics of a Three-phase Induction Motor
15. No-Load Characteristics of a Three-phase Alternator

EN105HS/EN205HS: ENGLISH

B.Tech. I Year II Sem.

L	T	P	C
2	0	0	2

INTRODUCTION

In view of the growing importance of English as a tool for global communication and the consequent emphasis on training students to acquire language skills, the syllabus of English has been designed to develop linguistic, communicative and critical thinking competencies of Engineering students.

In English classes, the focus should be on the skills development in the areas of vocabulary, grammar, reading and writing. For this, the teachers should use the prescribed text for detailed study. The students should be encouraged to read the texts leading to reading comprehension and different passages may be given for practice in the class. The time should be utilized for working out the exercises given after each excerpt, and also for supplementing the exercises with authentic materials of a similar kind, for example, newspaper articles, advertisements, promotional material etc. *The focus in this syllabus is on skill development, fostering ideas and practice of language skills in various contexts and cultures.*

Learning Objectives: The course will help to

a. Improve the language proficiency of students in English with an emphasis on Vocabulary, Grammar, Reading and Writing skills.
b. Equip students to study academic subjects more effectively and critically using the theoretical and practical components of English syllabus.
c. Develop study skills and communication skills in formal and informal situations.

Course Outcomes: Students should be able to

- Use English Language effectively in spoken and written forms.
- Comprehend the given texts and respond appropriately.
- Communicate confidently in various contexts and different cultures.
- Acquire basic proficiency in English including reading and listening comprehension, writing and speaking skills.

SYLLABUS

UNIT –I

'The Raman Effect' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.

Vocabulary Building: The Concept of Word Formation --The Use of Prefixes and Suffixes.

Grammar: Identifying Common Errors in Writing with Reference to Articles and Prepositions.

Reading: Reading and Its Importance- Techniques for Effective Reading.

Basic Writing Skills: Sentence Structures -Use of Phrases and Clauses in Sentences- Importance of Proper Punctuation- Techniques for writing precisely – **Paragraph writing** – Types, Structures and Features of a Paragraph - Creating Coherence-Organizing Principles of Paragraphs in Documents.

UNIT –II

'Ancient Architecture in India' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.

Vocabulary: Synonyms and Antonyms.

Grammar: Identifying Common Errors in Writing with Reference to Noun-pronoun Agreement and Subject-verb Agreement.

Reading: Improving Comprehension Skills – Techniques for Good Comprehension

Writing: Format of a Formal Letter-**Writing Formal Letters** E.g.., Letter of Complaint, Letter of Requisition, Job Application with Resume.

UNIT –III

'Blue Jeans' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.

Vocabulary: Acquaintance with Prefixes and Suffixes from Foreign Languages in English to form Derivatives-Words from Foreign Languages and their Use in English.

Grammar: Identifying Common Errors in Writing with Reference to Misplaced Modifiers and Tenses.
Reading: Sub-skills of Reading- Skimming and Scanning
Writing: Nature and Style of Sensible Writing- **Defining- Describing** Objects, Places and Events – **Classifying**- Providing Examples or Evidence

UNIT –IV

'What Should You Be Eating' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.
Vocabulary: Standard Abbreviations in English
Grammar: Redundancies and Clichés in Oral and Written Communication.
Reading: Comprehension- Intensive Reading and Extensive Reading
Writing: Writing Practices–Writing Introduction and Conclusion - Essay Writing-Précis Writing.

UNIT –V

'How a Chinese Billionaire Built Her Fortune' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.
Vocabulary: Technical Vocabulary and their usage
Grammar: Common Errors in English
Reading: Reading Comprehension-Exercises for Practice
Writing: **Technical Reports**- Introduction – Characteristics of a Report – Categories of Reports
Formats- Structure of Reports (Manuscript Format) -Types of Reports - Writing aReport.

TEXT BOOK:

1. Sudarshana, N.P. and Savitha, C. (2018). English for Engineers. Cambridge University Press.

REFERENCE BOOKS:

1. Swan, M. (2016). Practical English Usage. Oxford University Press.
2. Kumar, S and Lata, P. (2018). Communication Skills. Oxford University Press.
3. Wood, F.T. (2007). Remedial English Grammar. Macmillan.
4. Zinsser, William. (2001). On Writing Well. Harper Resource Book.
5. Hamp-Lyons, L. (2006). Study Writing. Cambridge University Press.
6. Exercises in Spoken English. Parts I –III. CIEFL, Hyderabad. Oxford University Press.

II YEAR I SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	EC301PC	Electronic Devices and Circuits	3	1	0	4
2	EC302PC	Network Analysis and Transmission Lines	3	0	0	3
3	EC303PC	Digital System Design	3	1	0	4
4	EC304PC	Signals and Systems	3	1	0	4
5	EC305ES	Probability Theory and Stochastic Processes	3	0	0	3
6	EC306PC	Electronic Devices and Circuits Lab	0	0	2	1
7	EC307PC	Digital System Design Lab	0	0	2	1
8	EC308ES	Basic Simulation Lab	0	0	2	1
9	*MC309	Constitution of India	3	0	0	0
		Total Credits	**18**	**3**	**6**	**21**

II YEAR II SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA401BS	Laplace Transforms, Numerical Methods & Complex Variables	3	1	0	4
2	EC402PC	Electromagnetic Fields and Waves	3	0	0	3
3	EC403PC	Analog and Digital Communications	3	1	0	4
4	EC404PC	Linear IC Applications	3	0	0	3
5	EC405PC	Electronic Circuit Analysis	3	0	0	3
6	EC406PC	Analog and Digital Communications Lab	0	0	3	1.5
7	EC407PC	IC Applications Lab	0	0	3	1.5
8	EC408PC	Electronic Circuit Analysis Lab	0	0	2	1
9	*MC409	Gender Sensitization Lab	0	0	2	0
		Total Credits	**15**	**2**	**10**	**21**

PRINCIPAL
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EC301PC: ELECTRONIC DEVICES AND CIRCUITS

B.Tech. II Year I Sem.

L	T	P	C
3	1	0	4

Course Objectives:
- To introduce components such as diodes, BJTs and FETs.
- To know the applications of components.
- To know the switching characteristics of components
- To give understanding of various types of amplifier circuits

Course Outcomes: Upon completion of the Course, the students will be able to:
- Know the characteristics of various components.
- Understand the utilization of components.
- Understand the biasing techniques
- Design and analyze small signal amplifier circuits.

UNIT - I

Diode and Applications: Diode - Static and Dynamic resistances, Equivalent circuit, Load line analysis, Diffusion and Transition Capacitances, Diode Applications: Switch-Switching times.

Rectifier - Half Wave Rectifier, Full Wave Rectifier, Bridge Rectifier, Rectifiers with Capacitive and Inductive Filters, Clippers-Clipping at two independent levels, Clamper-Clamping Circuit Theorem, Clamping Operation, Types of Clampers.

UNIT - II

Bipolar Junction Transistor (BJT): Principle of Operation, Common Emitter, Common Base and Common Collector Configurations, Transistor as a switch, switching times, Transistor Biasing and Stabilization - Operating point, DC & AC load lines, Biasing - Fixed Bias, Self Bias, Bias Stability, Bias Compensation using Diodes.

UNIT - III

Junction Field Effect Transistor (FET): Construction, Principle of Operation, Pinch-Off Voltage, Volt-Ampere Characteristic, Comparison of BJT and FET, Biasing of FET, FET as Voltage Variable Resistor.

Special Purpose Devices: Zener Diode - Characteristics, Voltage Regulator. Principle of Operation - SCR, Tunnel diode, UJT, Varactor Diode.

UNIT – IV

Analysis and Design of Small Signal Low Frequency BJT Amplifiers: Transistor Hybrid model, Determination of h-parameters from transistor characteristics, Typical values of h- parameters in CE, CB and CC configurations, Transistor amplifying action, Analysis of CE, CC, CB Amplifiers and CE Amplifier with emitter resistance, low frequency response of BJT Amplifiers, effect of coupling and bypass capacitors on CE Amplifier.

UNIT – V

FET Amplifiers: Small Signal Model, Analysis of JFET Amplifiers, Analysis of CS, CD, CG JFET Amplifiers. MOSFET Characteristics in Enhancement and Depletion mode, Basic Concepts of MOS Amplifiers.

TEXT BOOKS:

1. Electronic Devices and Circuits- Jacob Millman, McGraw Hill Education
2. Electronic Devices and Circuits theory– Robert L. Boylestead, Louis Nashelsky, 11th Edition, 2009, Pearson.

REFERENCE BOOKS:

1. The Art of Electronics, Horowitz, 3rd Edition Cambridge University Press
2. Electronic Devices and Circuits, David A. Bell – 5th Edition, Oxford.
3. Pulse, Digital and Switching Waveforms –J. Millman, H. Taub and Mothiki S. Prakash Rao, 2Ed., 2008, Mc Graw Hill.

EC306PC: ELECTRONIC DEVICES AND CIRCUITS LAB

B.Tech. II Year I Sem.

L	T	P	C
0	0	2	1

List of Experiments (Twelve experiments to be done):

Verify any twelve experiments in H/W Laboratory

1. PN Junction diode characteristics A) Forward bias B) Reverse bias.
2. Zener diode characteristics and Zener as voltage Regulator
3. Full Wave Rectifier with & without filters
4. Input and output characteristics of BJT in CE Configuration
5. Input and output characteristics of FE in CS Configuration
6. Common Emitter Amplifier Characteristics
7. Common Base Amplifier Characteristics
8. Common Source amplifier Characteristics
9. Measurement of h-parameters of transistor in CB, CE, CC configurations
10. Switching characteristics of a transistor
11. SCR Characteristics.
12. Types of Clippers at different reference voltages
13. Types of Clampers at different reference voltages
14. The steady state output waveform of clampers for a square wave input

Major Equipment required for Laboratories:

1. Regulated Power Suppliers, 0-30V
2. 20 MHz, Dual Channel Cathode Ray Oscilloscopes.
3. Functions Generators-Sine and Square wave signals
4. Multimeters
5. Electronic Components

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EC303PC: DIGITAL SYSTEM DESIGN

B.Tech. II Year I Sem.

L	T	P	C
3	1	0	4

Pre-Requisites: Nil

Course Objectives:
- To understand common forms of number representation in logic circuits
- To learn basic techniques for the design of digital circuits and fundamental concepts used in the design of digital systems.
- To understand the concepts of combinational logic circuits and sequential circuits.
- To understand the Realization of Logic Gates Using Diodes & Transistors.

Course Outcomes: Upon completing this course, the student will be able to
- Understand the numerical information in different forms and Boolean Algebra theorems
- Postulates of Boolean algebra and to minimize combinational functions
- Design and analyze combinational and sequential circuits
- Known about the logic families and realization of logic gates.

UNIT - I:
Number Systems: Number systems, Complements of Numbers, Codes- Weighted and Non-weighted codes and its Properties, Parity check code and Hamming code.
Boolean Algebra: Basic Theorems and Properties, Switching Functions- Canonical and Standard Form, Algebraic Simplification, Digital Logic Gates, EX-OR gates, Universal Gates, Multilevel NAND/NOR realizations.

UNIT - II:
Minimization of Boolean functions: Karnaugh Map Method - Up to five Variables, Don't Care Map Entries, Tabular Method,
Combinational Logic Circuits: Adders, Subtractors, Comparators, Multiplexers, Demultiplexers, Encoders, Decoders and Code converters, Hazards and Hazard Free Relations.

UNIT - III
Sequential Circuits Fundamentals: Basic Architectural Distinctions between Combinational and Sequential circuits, SR Latch, Flip Flops: SR, JK, JK Master Slave, D and T Type Flip Flops, Excitation Table of all Flip Flops, Timing and Triggering Consideration, Conversion from one type of Flip-Flop to another.
Registers and Counters: Shift Registers – Left, Right and Bidirectional Shift Registers, Applications of Shift Registers - Design and Operation of Ring and Twisted Ring Counter, Operation of Asynchronous and Synchronous Counters.

UNIT - IV
Sequential Machines: Finite State Machines, Synthesis of Synchronous Sequential Circuits- Serial Binary Adder, Sequence Detector, Parity-bit Generator, Synchronous Modulo N –Counters. Finite state machine-capabilities and limitations, Mealy and Moore models.

UNIT - V
Realization of Logic Gates Using Diodes & Transistors: AND, OR and NOT Gates using Diodes and Transistors, DCTL, RTL, DTL, TTL, CML and CMOS Logic Families and its Comparison, Classification of Integrated circuits, comparison of various logic families, standard TTL NAND Gate-Analysis & characteristics, TTL open collector O/Ps, Tristate TTL, MOS & CMOS open drain and tri-state outputs, CMOS transmission gate, IC interfacing- TTL driving CMOS & CMOS driving TTL.

TEXT BOOKS:
1. Switching and Finite Automata Theory - Zvi Kohavi & Niraj K. Jha, 3rd Edition, Cambridge, 2010.
2. Modern Digital Electronics – R. P. Jain, 3rd Edition, 2007- Tata McGraw-Hill

REFERENCE BOOKS:
1. Digital Design- Morris Mano, PHI, 4th Edition,2006

2. Introduction to Switching Theory and Logic Design – Fredriac J. Hill, Gerald R. Peterson, 3rd Ed, John Wiley & Sons Inc.
3. Fundamentals of Logic Design- Charles H. Roth, Cengage Learning, 5th, Edition, 2004.
4. Switching Theory and Logic Design – A Anand Kumar, PHI, 2013

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EC304PC: SIGNALS AND SYSTEMS

B.Tech. II Year I Sem.

L	T	P	C
3	1	0	4

Pre-requisite: Nil

Course Objectives:
- This gives the basics of Signals and Systems required for all Electrical Engineering related courses.
- To understand the behavior of signal in time and frequency domain
- To understand the characteristics of LTI systems
- This gives concepts of Signals and Systems and its analysis using different transform techniques.

Course Outcomes: Upon completing this course, the student will be able to
- Differentiate various signal functions.
- Represent any arbitrary signal in time and frequency domain.
- Understand the characteristics of linear time invariant systems.
- Analyze the signals with different transform technique

UNIT - I
Signal Analysis: Analogy between Vectors and Signals, Orthogonal Signal Space, Signal approximation using Orthogonal functions, Mean Square Error, Closed or complete set of Orthogonal functions, Orthogonality in Complex functions, Classification of Signals and systems, Exponential and Sinusoidal signals, Concepts of Impulse function, Unit Step function, Signum function.

UNIT – II
Fourier series: Representation of Fourier series, Continuous time periodic signals, Properties of Fourier Series, Dirichlet's conditions, Trigonometric Fourier Series and Exponential Fourier Series, Complex Fourier spectrum.
Fourier Transforms: Deriving Fourier Transform from Fourier series, Fourier Transform of arbitrary signal, Fourier Transform of standard signals, Fourier Transform of Periodic Signals, Properties of Fourier Transform, Fourier Transforms involving Impulse function and Signum function, Introduction to Hilbert Transform.

UNIT - III
Signal Transmission through Linear Systems: Linear System, Impulse response, Response of a Linear System, Linear Time Invariant(LTI) System, Linear Time Variant (LTV) System, Transfer function of a LTI System, Filter characteristic of Linear System, Distortion less transmission through a system, Signal bandwidth, System Bandwidth, Ideal LPF, HPF, and BPF characteristics, Causality and Paley-Wiener criterion for physical realization, Relationship between Bandwidth and rise time, Convolution and Correlation of Signals, Concept of convolution in Time domain and Frequency domain, Graphical representation of Convolution.

UNIT – IV
Laplace Transforms: Laplace Transforms (L.T), Inverse Laplace Transform, Concept of Region of Convergence (ROC) for Laplace Transforms, Properties of L.T, Relation between L.T and F.T of a signal, Laplace Transform of certain signals using waveform synthesis.
Z–Transforms: Concept of Z- Transform of a Discrete Sequence, Distinction between Laplace, Fourier and Z Transforms, Region of Convergence in Z-Transform, Constraints on ROC for various classes of signals, Inverse Z-transform, Properties of Z-transforms.

UNIT - V
Sampling theorem: Graphical and analytical proof for Band Limited Signals, Impulse Sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, Effect of under sampling – Aliasing, Introduction to Band Pass Sampling.
Correlation: Cross Correlation and Auto Correlation of Functions, Properties of Correlation Functions, Energy Density Spectrum, Parsevals Theorem, Power Density Spectrum, Relation between Autocorrelation Function and Energy/Power Spectral Density Function, Relation between Convolution

and Correlation, Detection of Periodic Signals in the presence of Noise by Correlation, Extraction of Signal from Noise by Filtering.

TEXT BOOKS:

1. Signals, Systems & Communications - B.P. Lathi, 2013, BSP.
2. Signals and Systems - A.V. Oppenheim, A.S. Willsky and S.H. Nawabi, 2 Ed.

REFERENCE BOOKS:

1. Signals and Systems – Simon Haykin and Van Veen, Wiley 2 Ed.,
2. Signals and Systems – A. Rama Krishna Rao, 2008, TMH
3. Fundamentals of Signals and Systems - Michel J. Robert, 2008, MGH International Edition.
4. Signals, Systems and Transforms - C. L. Philips, J.M.Parr and Eve A.Riskin, 3 Ed., 2004, PE.
5. Signals and Systems – K. Deergha Rao, Birkhauser, 2018.

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EC308ES: BASIC SIMULATION LAB

B.Tech. II Year I Sem.

L	T	P	C
0	0	2	1

Note:

- All the experiments are to be simulated using MATLAB or equivalent software
- Minimum of 15 experiment are to be completed

List of Experiments:

1. Basic Operations on Matrices.
2. Generation of Various Signals and Sequences (Periodic and Aperiodic), such as Unit Impulse, Unit Step, Square, Saw tooth, Triangular, Sinusoidal, Ramp, Sinc.
3. Operations on Signals and Sequences such as Addition, Multiplication, Scaling, Shifting, Folding, Computation of Energy and Average Power.
4. Finding the Even and Odd parts of Signal/Sequence and Real and Imaginary parts of Signal.
5. Convolution for Signals and sequences.
6. Auto Correlation and Cross Correlation for Signals and Sequences.
7. Verification of Linearity and Time Invariance Properties of a given Continuous/Discrete System.
8. Computation of Unit sample, Unit step and Sinusoidal responses of the given LTI system and verifying its physical realiazability and stability properties.
9. Gibbs Phenomenon Simulation.
10. Finding the Fourier Transform of a given signal and plotting its magnitude and phase spectrum.
11. Waveform Synthesis using Laplace Transform.
12. Locating the Zeros and Poles and plotting the Pole-Zero maps in S-plane and Z-Plane for the given transfer function.
13. Generation of Gaussian noise (Real and Complex), Computation of its mean, M.S. Value and its Skew, Kurtosis, and PSD, Probability Distribution Function.
14. Verification of Sampling Theorem.
15. Removal of noise by Autocorrelation / Cross correlation.
16. Extraction of Periodic Signal masked by noise using Correlation.
17. Verification of Weiner-Khinchine Relations.
18. Checking a Random Process for Stationarity in Wide sense.

Major Equipments required for Laboratories:

1. Computer System with latest specifications connected
2. Window Xp or equivalent
3. Simulation software-MAT Lab or any equivalent simulation software

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EC403PC: ANALOG AND DIGITAL COMMUNICATIONS

B.Tech. II Year II Semester

L	T	P	C
3	1	0	4

Prerequisite: Probability theory and Stochastic Processes

Course Objectives:
- To develop ability to analyze system requirements of analog and digital communication systems.
- To understand the generation, detection of various analog and digital modulation techniques.
- To acquire theoretical knowledge of each block in AM, FM transmitters and receivers.
- To understand the concepts of baseband transmissions.

Course Outcomes: Upon completing this course, the student will be able to
- Analyze and design of various continuous wave and angle modulation and demodulation techniques
- Understand the effect of noise present in continuous wave and angle modulation techniques.
- Attain the knowledge about AM , FM Transmitters and Receivers
- Analyze and design the various Pulse Modulation Techniques.
- Understand the concepts of Digital Modulation Techniques and Baseband transmission.

UNIT - I

Amplitude Modulation: Need for modulation, Amplitude Modulation - Time and frequency domain description, single tone modulation, power relations in AM waves, Generation of AM waves - Switching modulator, Detection of AM Waves - Envelope detector, DSBSC modulation - time and frequency domain description, Generation of DSBSC Waves - Balanced Modulators, Coherent detection of DSB-SC Modulated waves, COSTAS Loop, SSB modulation - time and frequency domain description, frequency discrimination and Phase discrimination methods for generating SSB, Demodulation of SSB Waves, principle of Vestigial side band modulation.

UNIT - II

Angle Modulation: Basic concepts of Phase Modulation, Frequency Modulation: Single tone frequency modulation, Spectrum Analysis of Sinusoidal FM Wave using Bessel functions, Narrow band FM, Wide band FM, Constant Average Power, Transmission bandwidth of FM Wave - Generation of FM Signal-Armstrong Method, Detection of FM Signal: Balanced slope detector, Phase locked loop, Comparison of FM and AM., Concept of Pre-emphasis and de-emphasis.

UNIT - III

Transmitters: Classification of Transmitters, AM Transmitters, FM Transmitters
Receivers: Radio Receiver - Receiver Types - Tuned radio frequency receiver, Superhetrodyne receiver, RF section and Characteristics - Frequency changing and tracking, Intermediate frequency, Image frequency, AGC, Amplitude limiting, FM Receiver, Comparison of AM and FM Receivers.

UNIT - IV

Pulse Modulation: Types of Pulse modulation- PAM, PWM and PPM. Comparison of FDM and TDM.
Pulse Code Modulation: PCM Generation and Reconstruction, Quantization Noise, Non-Uniform Quantization and Companding, DPCM, Adaptive DPCM, DM and Adaptive DM, Noise in PCM and DM.

UNIT - V

Digital Modulation Techniques: ASK- Modulator, Coherent ASK Detector, FSK- Modulator, Non-Coherent FSK Detector, BPSK- Modulator, Coherent BPSK Detection. Principles of QPSK, Differential PSK and QAM.
Baseband Transmission and Optimal Reception of Digital Signal: A Baseband Signal Receiver, Probability of Error, Optimum Receiver, Coherent Reception, ISI, Eye Diagrams.

TEXT BOOKS:
1. Analog and Digital Communications – Simon Haykin, John Wiley, 2005.
2. Electronics Communication Systems-Fundamentals through Advanced-Wayne Tomasi, 5th Edition, 2009, PHI.

REFERENCE BOOKS:

1. Principles of Communication Systems - Herbert Taub, Donald L Schilling, Goutam Saha, 3rd Edition, McGraw-Hill, 2008.
2. Electronic Communications – Dennis Roddy and John Coolean , 4th Edition , PEA, 2004
3. Electronics & Communication System – George Kennedy and Bernard Davis, TMH 2004
4. Analog and Digital Communication – K. Sam Shanmugam, Willey ,2005

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EC406PC: ANALOG AND DIGITAL COMMUNICATIONS LAB

B.Tech. II Year II Sem.

L	T	P	C
0	0	3	1.5

Note:

- Minimum 12 experiments should be conducted:
- All these experiments are to be simulated first either using MATLAB, COMSIM or any other simulation package and then to be realized in hardware

List of Experiments:

1. (i) Amplitude modulation and demodulation (ii) Spectrum analysis of AM
2. (i) Frequency modulation and demodulation (ii) Spectrum analysis of FM
3. DSB-SC Modulator & Detector
4. SSB-SC Modulator & Detector (Phase Shift Method)
5. Frequency Division Multiplexing & De multiplexing
6. Pulse Amplitude Modulation & Demodulation
7. Pulse Width Modulation & Demodulation
8. Pulse Position Modulation & Demodulation
9. PCM Generation and Detection
10. Delta Modulation
11. Frequency Shift Keying: Generation and Detection
12. Binary Phase Shift Keying: Generation and Detection
13. Generation and Detection (i) DPSK (ii) QPSK

Major Equipments required for Laboratories:

1. CROs: 20MHz
2. Function Generators: 2MHz
3. Spectrum Analyzer
4. Regulated Power Supplies: 0-30V
5. MAT Lab/Equivalent Simulation Package with Communication tool box
6. Analog and Digital Modulation and Demodulation Trainer Kits.

EC404PC: LINEAR IC APPLICATIONS

B.Tech. II Year II Sem.

L	T	P	C
3	0	0	3

Pre-requisite: Electronic Devices & Circuits

Course Objectives: The main objectives of the course are:
- To introduce the basic building blocks of linear integrated circuits.
- To introduce the theory and applications of analog multipliers and PLL.
- To introduce the concepts of waveform generation and introduce some special function ICs.

Course Outcomes: Upon completing this course, the student will be able to
- A thorough understanding of operational amplifiers with linear integrated circuits.
- Attain the knowledge of functional diagrams and applications of IC 555 and IC 565
- Acquire the knowledge about the Data converters.

UNIT - I
Integrated Circuits: Classification, chip size and circuit complexity, basic information of Op-amp, ideal and practical Op-amp, internal circuits, Op-amp characteristics, DC and AC Characteristics, 741 op-amp and its features, modes of operation-inverting, non-inverting, differential.

UNIT - II
Op-amp and Applications: Basic information of Op-amp, instrumentation amplifier, ac amplifier, V to I and I to V converters, Sample & hold circuits, multipliers and dividers, differentiators and integrators, comparators, Schmitt trigger, Multivibrators, introduction to voltage regulators, features of 723

UNIT - III
Active Filters & Oscillators: Introduction, 1st order LPF, HPF filters, Band pass, Band reject and all pass filters. Oscillator types and principle of operation - RC, Wien and quadrature type, waveform generators - triangular, sawtooth, square wave and VCO.

UNIT - IV
Timers & Phase Locked Loops: Introduction to 555 timer, functional diagram, monostable and astable operations and applications, Schmitt Trigger. PLL - introduction, block schematic, principles and description of individual blocks of 565.

UNIT - V
D-A and A-D Converters: Introduction, basic DAC techniques, weighted resistor DAC, R-2R ladder DAC, inverted R-2R DAC, and IC 1408 DAC, Different types of ADCs - parallel comparator type ADC, counter type ADC, successive approximation ADC dual slope integration type ADC, DAC and ADC specifications.

TEXT BOOKS:

1. Linear Integrated Circuits, D. Roy Chowdhury, New Age International(p) Ltd.
2. Op-Amps & Linear ICs, Ramakanth A. Gayakwad, PHI

REFERENCES BOOKS:

1. Operational Amplifiers & Linear Integrated Circuits, R.F. Coughlin & Fredrick F. Driscoll, PHI.
2. Operational Amplifiers & Linear Integrated Circuits: Theory & Applications, Denton J. Daibey, TMH.
3. Design with Operational Amplifiers & Analog Integrated Circuits, Sergio Franco, McGraw Hill.
4. Digital Fundamentals - Floyd and Jain, Pearson Education.

EC407PC: IC APPLICATIONS LAB

B.Tech. II Year II Semester

L	T	P	C
0	0	3	1.5

Note: Verify the functionality of the IC in the given application

Design and Implementation of:

1. Inverting and Non-Inverting Amplifiers using Op Amps
2. Adder and Subtractor using Op Amp.
3. Comparators using Op Amp.
4. Integrator Circuit using IC 741.
5. Differentiator Circuit using Op Amp.
6. Active filter Applications-LPF, HPF (First Order)
7. IC 741 waveform Generators-Sine, Square wave and Triangular Waves.
8. Mono-Stable Multivibrator using IC 555.
9. Astable multivibrator using IC 555.
10. Schmitt Trigger Circuits using IC 741.
11. IC 565-PLL Applications.
12. Voltage Regulator using IC 723
13. Three terminal voltage regulators-7805, 7809, 7912

Major Equipments required for Laboratories:

1. 5 V Fixed Regulated Power Supply/ 0-5V or more Regulated Power Supply.
2. 20 MHz Oscilloscope with Dual Channel.
3. Bread board and components/ Trainer Kit.
4. Multimeter.

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EC405PC: ELECTRONIC CIRCUIT ANALYSIS

B.Tech. II Year II Sem.

L	T	P	C
3	0	0	3

Pre-requisite: Electronic Devices and Circuits

Course Objectives:
- Learn the concepts of high frequency analysis of transistors.
- To give understanding of various types of amplifier circuits such as small signal, cascaded, large signal and tuned amplifiers.
- To familiarize the Concept of feedback in amplifiers so as to differentiate between negative and positive feedback
- To construct various multivibrators using transistors and sweep circuits.

Course Outcomes: Upon completing this course, the student will be able to
- Design the multistage amplifiers and understand the concepts of High Frequency Analysis of Transistors.
- Utilize the Concepts of negative feedback to improve the stability of amplifiers and positive feedback to generate sustained oscillations
- Design and realize different classes of Power Amplifiers and tuned amplifiers useable for audio and Radio applications.
- Design Multivibrators and sweep circuits for various applications.

UNIT – I

Multistage Amplifiers: Classification of Amplifiers, Distortion in amplifiers, Different coupling schemes used in amplifiers, Frequency response and Analysis of multistage amplifiers, Casca RC Coupled amplifiers, Cascode amplifier, Darlington pair.

Transistor at High Frequency: Hybrid -πmodel of Common Emitter transistor model, f_α, f_β and unity gain bandwidth, Gain-bandwidth product.

UNIT II

Feedback Amplifiers: Concepts of feedback – Classification of feedback amplifiers – General characteristics of Negative feedback amplifiers – Effect of Feedback on Amplifier characteristics – Voltage series, Voltage shunt, Current series and Current shunt Feedback configurations – Simple problems.

UNIT -III

Oscillators: Condition for Oscillations, RC type Oscillators-RC phase shift and Wien-bridge Oscillators, LC type Oscillators –Generalized analysis of LC Oscillators, Hartley and Colpitts Oscillators, Frequency and amplitude stability of Oscillators, Crystal Oscillator.

UNIT -IV

Large Signal Amplifiers: Class A Power Amplifier- Series fed and Transformer coupled, Conversion Efficiency, Class B Power Amplifier- Push Pull and Complimentary Symmetry configurations, Conversion Efficiency, Principle of operation of Class AB and Class –C Amplifiers.

Tuned Amplifiers: Introduction, single Tuned Amplifiers – Q-factor, frequency response of tuned amplifiers, Concept of stagger tuning and synchronous tuning.

UNIT –V

Multivibrators: Analysis and Design of Bistable, Monostable, Astable Multivibrators and Schmitt trigger using Transistors.

Time Base Generators: General features of a Time base Signal, Methods of Generating Time Base Waveform, concepts of Transistor Miller and Bootstrap Time Base Generator, Methods of Linearity improvement.

TEXT BOOKS:

1. Integrated Electronics, Jacob Millman, Christos C Halkias, McGraw Hill Education.
2. Electronic Devices Conventional and current version -Thomas L. Floyd 2015, Pearson.

REFERENCE BOOKS:

1. Electronic Devices and Circuits, David A. Bell – 5th Edition, Oxford.
2. Electronic Devices and Circuits theory– Robert L. Boylestead, Louis Nashelsky, 11th Edition, 2009, Pearson

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EC408PC: ELECTRONIC CIRCUIT ANALYSIS LAB

B.Tech. II Year II Sem.

L	T	P	C
0	0	2	1

Note:

- Experiments marked with * has to be designed, simulated and verified in hardware.
- Minimum of 9 experiments to be done in hardware.

Hardware Testing in Laboratory:

1. Common Emitter Amplifier (*)
2. Two Stage RC Coupled Amplifier
3. Cascode amplifier Circuit (*)
4. Darlington Pair Circuit
5. Current Shunt Feedback amplifier Circuit
6. Voltage Series Feedback amplifier Circuit (*)
7. RC Phase shift Oscillator Circuit (*)
8. Hartley and Colpitt's Oscillators Circuit
9. Class A power amplifier
10. Class B Complementary symmetry amplifier (*)
11. Design a Monostable Multivibrator
12. The output voltage waveform of Miller Sweep Circuit

Major Equipments required for Laboratories:

1. Computer System with latest specifications connected
2. Window XP or equivalent
3. Simulation software-Multisim or any equivalent simulation software
4. Regulated Power Suppliers, 0-30V
5. 20 MHz, Dual Channel Cathode Ray Oscilloscopes.
6. Functions Generators-Sine and Square wave signals
7. Multimeters
8. Electronic Components

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*MC409/*MC309: GENDER SENSITIZATION LAB
(An Activity-based Course)

B.Tech. II Year II Sem.

L	T	P	C
0	0	2	0

COURSE DESCRIPTION

This course offers an introduction to Gender Studies, an interdisciplinary field that asks critical questions about the meanings of sex and gender in society. The primary goal of this course is to familiarize students with key issues, questions and debates in Gender Studies, both historical and contemporary. It draws on multiple disciplines – such as literature, history, economics, psychology, sociology, philosophy, political science, anthropology and media studies – to examine cultural assumptions about sex, gender, and sexuality.

This course integrates analysis of current events through student presentations, aiming to increase awareness of contemporary and historical experiences of women, and of the multiple ways that sex and gender interact with race, class, caste, nationality and other social identities. This course also seeks to build an understanding and initiate and strengthen programmes combating gender-based violence and discrimination. The course also features several exercises and reflective activities designed to examine the concepts of gender, gender-based violence, sexuality, and rights. It will further explore the impact of gender-based violence on education, health and development.

Objectives of the Course:
- To develop students' sensibility with regard to issues of gender in contemporary India.
- To provide a critical perspective on the socialization of men and women.
- To introduce students to information about some key biological aspects of genders.
- To expose the students to debates on the politics and economics of work.
- To help students reflect critically on gender violence.
- To expose students to more egalitarian interactions between men and women.

Learning Outcomes:
- Students will have developed a better understanding of important issues related to gender in contemporary India.
- Students will be sensitized to basic dimensions of the biological, sociological, psychological and legal aspects of gender. This will be achieved through discussion of materials derived from research, facts, everyday life, literature and film.
- Students will attain a finer grasp of how gender discrimination works in our society and how to counter it.
- Students will acquire insight into the gendered division of labour and its relation to politics and economics.
- Men and women students and professionals will be better equipped to work and live together as equals.
- Students will develop a sense of appreciation of women in all walks of life.
- Through providing accounts of studies and movements as well as the new laws that provide protection and relief to women, the textbook will empower students to understand and respond to gender violence.

UNIT - I: UNDERSTANDING GENDER
Introduction: Definition of Gender-Basic Gender Concepts and Terminology-Exploring Attitudes towards Gender-Construction of Gender-Socialization: Making Women, Making Men
- Preparing for Womanhood. Growing up Male. First lessons in Caste.

UNIT – II: GENDER ROLES AND RELATIONS
Two or Many? -Struggles with Discrimination-Gender Roles and Relations-Types of Gender Roles-Gender Roles and Relationships Matrix-Missing Women-Sex Selection and Its Consequences-Declining Sex Ratio. Demographic Consequences-Gender Spectrum: Beyond the Binary

UNIT – III: GENDER AND LABOUR

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Division and Valuation of Labour-Housework: The Invisible Labor- "My Mother doesn't Work." "Share the Load."-Work: Its Politics and Economics -Fact and Fiction. Unrecognized and Unaccounted work. - Gender Development Issues-Gender, Governance and Sustainable Development-Gender and Human Rights-Gender and Mainstreaming

UNIT – IV: GENDER - BASED VIOLENCE

The Concept of Violence- Types of Gender-based Violence-Gender-based Violence from a Human Rights Perspective-Sexual Harassment: Say No! -Sexual Harassment, not Eve-teasing- Coping with Everyday Harassment- Further Reading: "*Chupulu*".

Domestic Violence: Speaking OutIs Home a Safe Place? -When Women Unite [Film]. Rebuilding Lives. Thinking about Sexual Violence Blaming the Victim-"I Fought for my Life...."

UNIT – V: GENDER AND CULTURE

Gender and Film-Gender and Electronic Media-Gender and Advertisement-Gender and Popular Literature- Gender Development Issues-Gender Issues-Gender Sensitive Language-Gender and Popular Literature - Just Relationships: Being Together as Equals

Mary Kom and Onler. Love and Acid just do not Mix. Love Letters. Mothers and Fathers. Rosa Parks-The Brave Heart.

Note: **Since it is Interdisciplinary Course, Resource Persons can be drawn from the fields of English Literature or Sociology or Political Science or any other qualified faculty who has expertise in this field from engineering departments.**

- ***Classes will consist of a combination of activities: dialogue-based lectures, discussions, collaborative learning activities, group work and in-class assignments. Apart from the above prescribed book, Teachers can make use of any authentic materials related to the topics given in the syllabus on "Gender".***

- **ESSENTIAL READING**: The Textbook, "*Towards a World of Equals: A Bilingual Textbook on Gender*" *written* by A.Suneetha, Uma Bhrugubanda, DuggiralaVasanta, Rama Melkote, Vasudha Nagaraj, Asma Rasheed, Gogu Shyamala, Deepa Sreenivas and Susie Tharu **published by Telugu Akademi, Telangana Government in 2015.**

ASSESSMENT AND GRADING:

- Discussion & Classroom Participation: 20%
- Project/Assignment: 30%
- End Term Exam: 50%

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

B.TECH. ELECTRONICS AND COMMUNICATION ENGINEERING
III YEAR COURSE STRUCTURE & SYLLABUS (R16)

Applicable From 2016-17 Admitted Batch

III YEAR I SEMESTER

S. No	Course Code	Course Title	L	T	P	Credits
1	EC501PC	Electromagnetic Theory and Transmission Lines	4	1	0	4
2	EC502PC	Linear and Digital IC Applications	4	0	0	4
3	EC503PC	Digital Communications	4	1	0	4
4	SM504MS	Fundamentals of Management	3	0	0	3
5		Open Elective – I	3	0	0	3
6	EC505PC	Linear IC Applications Lab	0	0	3	2
7	EC506PC	Digital IC Applications Lab	0	0	3	2
8	EC507PC	Digital Communications Lab	0	0	3	2
9	*MC500HS	Professional Ethics	3	0	0	0
		Total Credits	**21**	**2**	**9**	**24**

III YEAR II SEMESTER

S. No	Course Code	Course Title	L	T	P	Credits
1		Open Elective-II	3	0	0	3
2		Professional Elective-I	3	0	0	3
3	EC601PC	Antennas and Wave Propagation	4	0	0	4
4	EC602PC	Microprocessors and Microcontrollers	4	0	0	4
5	EC603PC	Digital Signal Processing	4	0	0	4
6	EC604PC	Digital Signal Processing Lab	0	0	3	2
7	EC605PC	Microprocessors and Microcontrollers Lab	0	0	3	2
8	EN606HS	Advanced English Communication Skills Lab	0	0	3	2
		Total Credits	**18**	**0**	**9**	**24**

During Summer Vacation between III and IV Years: Industry Oriented Mini Project

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LINEAR AND DIGITAL IC APPLICATIONS

B.Tech. III Year I Sem.
Course Code: EC502PC

L	T	P	C
4	0	0	4

Course Objectives:

1. The main objectives of the course are:
2. To introduce the basic building blocks of linear integrated circuits.
3. To teach the linear and non - linear applications of operational amplifiers.
4. To introduce the theory and applications of analog multipliers and PLL.
5. To teach the theory of ADC and DAC.
6. To introduce the concepts of waveform generation and introduce some special function ICs.
7. To understand and implement the working of basic digital circuits

Course Outcomes: On completion of this course, the students will have:

1. A thorough understanding of operational amplifiers with linear integrated circuits.
2. Understanding of the different families of digital integrated circuits and their characteristics.
3. Also students will be able to design circuits using operational amplifiers for various applications.

UNIT - I

Operational Amplifier: Ideal and Practical Op-Amp, Op-Amp Characteristics, DC and AC Characteristics, Features of 741 Op-Amp, Modes of Operation - Inverting, Non-Inverting, Differential, Instrumentation Amplifier, AC Amplifier, Differentiators and Integrators, Comparators, Schmitt Trigger, Introduction to Voltage Regulators, Features of 723 Regulator, Three Terminal Voltage Regulators.

UNIT - II

Op-Amp, IC-555 & IC 565 Applications: Introduction to Active Filters, Characteristics of Band pass, Band reject and All Pass Filters, Analysis of 1st order LPF & HPF Butterworth Filters, Waveform Generators – Triangular, Saw tooth, Square Wave, IC555 Timer - Functional Diagram, Monostable, and Astable Operations, Applications, IC565 PLL - Block Schematic, Description of Individual Blocks, Applications.

UNIT - III

Data Converters: Introduction, Basic DAC techniques, Different types of DACs-Weighted resistor DAC, R-2R ladder DAC, Inverted R-2R DAC, Different Types of ADCs - Parallel Comparator Type ADC, Counter Type ADC, Successive Approximation ADC and Dual Slope ADC, DAC and ADC Specifications.

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UNIT - IV

Digital Integrated Circuits: Classification of Integrated Circuits, Comparison of Various Logic Families Combinational Logic ICs – Specifications and Applications of TTL-74XX & Code Converters, Decoders, Demultiplexers, LED & LCD Decoders with Drivers, Encoders, Priority Encoders, Multiplexers, Demultiplexers, Priority Generators/Checkers, Parallel Binary Adder/Subtractor, Magnitude Comparators.

UNIT - V

Sequential Logic IC's and Memories: Familiarity with commonly available 74XX & CMOS 40XX Series ICs – All Types of Flip-flops, Synchronous Counters, Decade Counters, Shift Registers.

Memories - ROM Architecture, Types of ROMS & Applications, RAM Architecture, Static & Dynamic RAMs.

TEXT BOOKS:

1. Op-Amps & Linear ICs – Ramakanth A. Gayakwad, PHI, 2003.
2. Digital Fundamentals – Floyd and Jain, Pearson Education, 8th Edition, 2005.

REFERENCE BOOKS:

1. Linear Integrated Circuits –D. Roy Chowdhury, New Age International (p) Ltd, 2nd Ed., 2003.
2. Op Amps and Linear Integrated Circuits-Concepts and Applications James M. Fiore, Cengage Learning/ Jaico, 2009.
3. Operational Amplifiers with Linear Integrated Circuits by K. Lal Kishore – Pearson, 2009.
4. Linear Integrated Circuits and Applications – Salivahanan, MC GRAW HILL EDUCATION.
5. Modern Digital Electronics – RP Jain – 4/e – MC GRAW HILL EDUCATION, 2010.

LINEAR IC APPLICATIONS LAB

B.Tech. III Year I Sem.	**L T P C**
Course Code: EC505PC	**0 0 3 2**

Note:

- To perform any twelve experiments
- Verify the functionality of the IC in the given application.

Design and Implementation of:

1. Inverting and Non-inverting Amplifiers using Op Amps.
2. Adder and Subtractor using Op Amp.
3. Comparators using Op Amp.
4. Integrator Circuit using IC 741.
5. Differentiator circuit using Op Amp.
6. Active Filter Applications – LPF, HPF (first order)
7. IC 741 Waveform Generators – Sine, Square wave and Triangular waves.
8. Mono-stable Multivibrator using IC 555.
9. Astable Multivibrator using IC 555.
10. Schmitt Trigger Circuits – using IC 741.
11. IC 565 – PLL Applications.
12. Voltage Regulator using IC 723.
13. Three Terminal Voltage Regulators –7805, 7809, 7912.

DIGITAL IC APPLICATIONS LAB

B.Tech. III Year I Sem.
Course Code: EC506PC

L	T	P	C
0	0	3	2

Note:

- To perform any twelve experiments
- Verify the functionality of the IC in the given application.

Design and Implementation of:

1. Design a 16 x 4 priority encoder using two 8 x 3 priority encoder.
2. Design a 16 bit comparator using 4 bit Comparators.
3. Design a model to 53 counter using two decade counters.
4. Design a 450 KHz clock using NAND / NOR gates.
5. Design a 4 bit pseudo random sequence generator using 4 – bit ring counter.
6. Design a 16 x 1 multiplexer using 8 x 1 multiplexer.
7. Design a 16 bit Adder / Subtractor using 4 – bit Adder / Subtractor IC's
8. Plot the transform Characteristics of 74H, LS, HS series IC's.
9. Design a 4 – bit Gray to Binary and Binary to Gray Converter.
10. Design a two Digit 7 segment display unit using this display the Mod counter output of experiment 3.
11. Design an 8 bit parallel load and serial out shift register using two 4 bit shift register.
12. Design an 8 bit Serial in and serial out shift register using two 4 bit shift register.
13. Design a Ring counter and Twisted ring counter using a 4-bit shift register
14. Design a 4 digit hex counter using synchronous one digit hex counters.
15. Design a 4 digit hex counter using Asynchronous one digit hex counters.

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DIGITAL COMMUNICATIONS

B.Tech. III Year I Sem. **L T P C**
Course Code: EC503PC **4 1 0 4**

Course Objectives:

- To understand the functional block diagram of Digital communication system.
- To understand the need for source and channel coding.
- To study various source and channel coding techniques.
- To understand a mathematical model of digital communication system for bit error rate analysis of different digital communication systems.

Course Outcomes: At the end of the course, the student will be able to:

- Understand basic components of Digital Communication Systems.
- Design optimum receiver for Digital Modulation techniques.
- Analyze the error performance of Digital Modulation Techniques.
- Understand the redundancy present in Digital Communication by using various source coding techniques.
- Know about different error detecting and error correction codes like block codes, cyclic codes and convolution codes.

UNIT - I

Elements of Digital Communication Systems: Model of Digital Communication Systems, Digital Representation of Analog Signal, Certain Issues in Digital Transmission, Advantages of Digital Communication Systems, Sampling Theorem, Types of Sampling – Impulse Sampling , Natural Sampling , Flat – Top Sampling. Introduction to Baseband Sampling.

Waveform Coding Techniques: PCM Generation and Reconstruction, Quantization Noise, Non Uniform Quantization and Companding, DPCM, Adaptive DPCM, DM and Adaptive DM, Noise in PCM and DM.

UNIT - II

Information theory: Information and Entropy, Conditional Entropy and Redundancy, Shannon-Fano Coding Mutual information, Information Loss due to Noise, Source coding- Huffman Code, Variable Length Coding, Lempel-ziv coding, Source coding to increase average information per bit, Lossy Source coding, Bandwidth-S/N Trade off, Hartley Shannon Law.

Error Control Codes

Linear Block Codes: Matrix Description of Linear Block Codes, Error Detection and Error Correction Capabilities of Linear Block Codes. Cyclic Codes: Algebraic Structure, Encoding, Syndrome Calculation, Decoding. **Convolution Codes:** Encoding, Decoding,

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UNIT - III

Baseband Pulse Transmission: Introduction, Matched Filter, Error Rate Due to Noise, intersymbol interference Nyquist's criterion for Distortionless Baseband Binary Transmission, Correlative -Level Coding Baseband M-Array PAM Transmission PAM Transmission, Digital subscriber Lines, Optimal Liner Receiver, Adaptive Equalization, Eye patterns.

Digital pass band transmission: pass band transmission model, Gram-Schmidt orthogonalization procedure, Geometric interpretation of signals Coherent detection of signals in noise, probability of error, Correlation receiver.

UNIT - IV

Digital Modulation Techniques: Introduction, ASK, ASK Modulator, Coherent ASK Detector, Non-Coherent ASK Detector, FSK, Bandwidth and Frequency Spectrum of FSK, Non Coherent FSK Detector, Coherent FSK Detector, FSK Detection using PLL, BPSK, Coherent PSK Detection, QPSK, 8-PSK, 16-PSK Differential PSK, QAM .

UNIT - V

Spread Spectrum Modulation: Use of Spread Spectrum, Direct Sequence Spread (DSSS), and Code Division Multiple Access, Ranging using DSSS, Frequency Hopping Spread Spectrum, PN - Sequence: Generation and characteristics, Synchronization in Spread Spectrum Systems.

TEXT BOOKS:

1. Communications system, S. Haykin, Wiley, 4 edition 2009.
2. Digital and Analog Communication Systems – Sam Shanmugam, John Wiley, 2005.

REFERENCES:

1. Principles of Communication Systems - Herbert Taub, Donald L Schiling, Goutam Saha, 3rd Edition, McGraw-Hill, 2008
2. Electronic communication systems, Wayne Tomasi, 5 edition, Pearson
3. Communication Systems: Analog and Digital, R. P. Singh , S. Sapre, McGraw-Hill Education, 2012
4. Digital Communications – John G. Proakis , Masoud Salehi – 5th Edition, McGraw-Hill, 2008.

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DIGITAL COMMUNICATIONS LAB

B.Tech. III Year I Sem.
Course Code: EC507PC

L	T	P	C
0	0	3	2

Note:

- Perform any twelve experiments.
- Hardware Testing to be done

List of Experiments:

1. PCM Generation and Detection
2. Differential Pulse Code Modulation
3. Delta Modulation
4. Adaptive Delta modulation
5. Time Division Multiplexing of 2 Band Limited Signals
6. Frequency Shift Keying: Generation and Detection
7. Phase Shift Keying: Generation and Detection
8. Amplitude Shift Keying: Generation and Detection
9. Study of the spectral characteristics of PAM
10. Study of the spectral characteristics of PWM
11. Study of the spectral characteristics of QAM.
12. DPSK :Generation and Detection
13. QPSK : Generation and Detection
14. OFDM: Generation and Detection

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MICROPROCESSORS AND MICROCONTROLLERS

B.Tech. III Year II Sem. | **L T P C**
Course Code: EC602PC | **4 0 0 4**

Course Objectives:

- To develop an understanding of the operations of microprocessors and micro controllers; machine language programming and interfacing techniques.

Course Outcomes:

- Understands the internal architecture and organization of 8086, 8051 and ARM processors/controllers.
- Understands the interfacing techniques to 8086 and 8051 and can develop assembly language programming to design microprocessor/ micro controller based systems.

UNIT - I

8086 Architecture: 8086 Architecture-Functional diagram, Register Organization, Memory Segmentation, Programming Model, Memory addresses, Physical Memory Organization, Architecture of 8086, Signal descriptions of 8086, interrupts of 8086.

Instruction Set and Assembly Language Programming of 8086: Instruction formats, Addressing modes, Instruction Set, Assembler Directives, Macros, and Simple Programs involving Logical, Branch and Call Instructions, Sorting, String Manipulations.

UNIT - II

Introduction to Microcontrollers: Overview of 8051 Microcontroller, Architecture, I/O Ports, Memory Organization, Addressing Modes and Instruction set of 8051.

8051 Real Time Control: Programming Timer Interrupts, Programming External Hardware Interrupts, Programming the Serial Communication Interrupts, Programming 8051 Timers and Counters

UNIT – III

I/O And Memory Interface: LCD, Keyboard, External Memory RAM, ROM Interface, ADC, DAC Interface to 8051.

Serial Communication and Bus Interface: Serial Communication Standards, Serial Data Transfer Scheme, On board Communication Interfaces-I2C Bus, SPI Bus, UART; External Communication Interfaces-RS232,USB.

UNIT – IV

ARM Architecture: ARM Processor fundamentals, ARM Architecture – Register, CPSR, Pipeline, exceptions and interrupts interrupt vector table, ARM instruction set – Data processing, Branch instructions, load store instructions, Software interrupt instructions, Program status register instructions, loading constants, Conditional execution, Introduction to Thumb instructions.

UNIT – V

Advanced ARM Processors: Introduction to CORTEX Processor and its architecture, OMAP Processor and its Architecture.

TEXT BOOKS:

1. Advanced Microprocessors and Peripherals – A. K. Ray and K.M. Bhurchandani, MHE, 2nd Edition 2006.
2. The 8051 Microcontroller, Kenneth. J. Ayala, Cengage Learning, 3rd Ed.
3. ARM System Developers guide, Andrew N SLOSS, Dominic SYMES, Chris WRIGHT, Elsevier, 2012

REFERENCE BOOKS:

1. Microprocessors and Interfacing, D. V. Hall, MGH, 2nd Edition 2006.
2. Introduction to Embedded Systems, Shibu K.V, MHE, 2009
3. The 8051Microcontrollers, Architecture and Programming and Applications -K.Uma Rao, Andhe Pallavi, Pearson, 2009.

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MICROPROCESSORS AND MICROCONTROLLERS LAB

B.Tech. III Year II Sem.	**L T P C**
Course Code: EC605PC	**0 0 3 2**

Note: - Minimum of 12 experiments to be conducted.
The following programs/experiments are to be written for assembler and to be executed the same with 8086 and 8051 kits.

List of Experiments:

1. Programs for 16 bit arithmetic operations 8086(using various addressing modes)
2. Programs for sorting an array for 8086.
3. Programs for searching for a number of characters in a string for 8086.
4. Programs for string manipulation for 8086.
5. Programs for digital clock design using 8086.
6. Interfacing ADC and DAC to 8086.
7. Parallel communication between two microprocessor kits using 8255.
8. Serial communication between two microprocessor kits using 8251.
9. Interfacing to 8086 and programming to control stepper motor.
10. Programming using arithmetic, logical and bit manipulation instructions of 8051.
11. Program and verify Timer/Counter in 8051.
12. Program and verify interrupt handling in 8051.
13. UART operation in 8051.
14. Communication between 8051 kit and PC
15. Interfacing LCD to 8051
16. Interfacing Matrix/Keyboard to 8051
17. Data transfer from peripheral to memory through DMA controller 8237/8257

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DIGITAL SIGNAL PROCESSING

B.Tech. III Year II Sem. — L T P C
Course Code: EC603PC — 4 0 0 4

Course Objectives: This course is an essential course that provides design techniques for processing all type of signals in various fields. The main objectives are:

- To provide background and fundamental material for the analysis and processing of digital signals.
- To familiarize the relationships between continuous-time and discrete time signals and systems.
- To study fundamentals of time, frequency and Z-plane analysis and to discuss the inter-relationships of these analytic method.
- To study the designs and structures of digital (IIR and FIR) filters from analysis to synthesis for a given specifications.
- The impetus is to introduce a few real-world signal processing applications.
- To acquaint in FFT algorithms, Multi-rate signal processing techniques and finite word length effects.

Course Outcomes: On completion of this subject, the student should be able to:

- Perform time, frequency, and Z -transform analysis on signals and systems.
- Understand the inter-relationship between DFT and various transforms.
- Understand the significance of various filter structures and effects of round off errors.
- Design a digital filter for a given specification.
- Understand the fast computation of DFT and appreciate the FFT processing.
- Understand the tradeoffs between normal and multi rate DSP techniques and finite length word effects.

UNIT - I

Introduction: Introduction to Digital Signal Processing: Discrete Time Signals & Sequences, conversion of continuous to discrete signal, Normalized Frequency, Linear Shift Invariant Systems, Stability, and Causality, linear differential equation to difference equation, Linear Constant Coefficient Difference Equations, Frequency Domain Representation of Discrete Time Signals and Systems

Realization of Digital Filters: Applications of Z – Transforms, Solution of Difference Equations of Digital Filters, System Function, Stability Criterion, Frequency Response of Stable Systems, Realization of Digital Filters – Direct, Canonic, Cascade and Parallel Forms.

UNIT - II

Discrete Fourier Transforms: Properties of DFT, Linear Convolution of Sequences using DFT, Computation of DFT: Over-Lap Add Method, Over-Lap Save Method, Relation between DTFT, DFS, DFT and Z-Transform.

Fast Fourier Transforms: Fast Fourier Transforms (FFT) - Radix-2 Decimation-in-Time and Decimation-in-Frequency FFT Algorithms, Inverse FFT, and FFT with General Radix-N.

UNIT - III

IIR Digital Filters: Analog filter approximations – Butterworth and Chebyshev, Design of IIR Digital Filters from Analog Filters, Step and Impulse Invariant Techniques, Bilinear Transformation Method, Spectral Transformations.

UNIT - IV

FIR Digital Filters: Characteristics of FIR Digital Filters, Frequency Response, Design of FIR Filters: Fourier Method, Digital Filters using Window Techniques, Frequency Sampling Technique, Comparison of IIR & FIR filters.

UNIT - V

Multirate Digital Signal Processing: Introduction, Down Sampling, Decimation, Upsampling, Interpolation, Sampling Rate Conversion, Conversion of Band Pass Signals, Concept of Resampling, Applications of Multi Rate Signal Processing.

Finite Word Length Effects: Limit cycles, Overflow Oscillations, Round-off Noise in IIR Digital Filters, Computational Output Round off Noise, Methods to Prevent Overflow, Trade off between Round Off and Overflow Noise, Measurement of Coefficient Quantization Effects through Pole-Zero Movement, Dead Band Effects.

TEXT BOOKS:

1. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G. Manolakis, Pearson Education / PHI, 2007.
2. Discrete Time Signal Processing – A. V. Oppenheim and R.W. Schaffer, PHI, 2009
3. Fundamentals of Digital Signal Processing – Loney Ludeman, John Wiley, 2009

REFERENCES:

1. Digital Signal Processing – Fundamentals and Applications – Li Tan, Elsevier, 2008
2. Fundamentals of Digital Signal Processing using MATLAB – Robert J. Schilling, Sandra L. Harris, Thomson, 2007
3. Digital Signal Processing - A Practical approach, Emmanuel C. Ifeachor and Barrie W. Jervis, 2nd Edition, Pearson Education, 2009

DIGITAL SIGNAL PROCESSING LAB

B.Tech. III Year II Sem.	**L**	**T**	**P**	**C**
Course Code: EC604PC	**0**	**0**	**3**	**2**

Note:

1. The Programs shall be implemented in Software (Using MATLAB / Lab View / C Programming/ Equivalent) and Hardware (Using TI / Analog Devices / Motorola / Equivalent DSP processors).
2. Minimum of 12 experiments to be conducted.

List of Experiments

1. Generation of Sinusoidal Waveform / Signal based on Recursive Difference Equations
2. Histogram of White Gaussian Noise and Uniformly Distributed Noise.
3. To find DFT / IDFT of given DT Signal
4. To find Frequency Response of a given System given in Transfer Function/ Differential equation form.
5. Obtain Fourier series coefficients by formula and using FET and compare for half sine wave.
6. Implementation of FFT of given Sequence
7. Determination of Power Spectrum of a given Signal(s).
8. Implementation of LP FIR Filter for a given Sequence/Signal.
9. Implementation of HP IIR Filter for a given Sequence/Signal
10. Generation of Narrow Band Signal through Filtering
11. Generation of DTMF Signals
12. Implementation of Decimation Process
13. Implementation of Interpolation Process
14. Implementation of I/D Sampling Rate Converters
15. Impulse Response of First order and Second Order Systems.

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ADVANCED ENGLISH COMMUNICATION SKILLS (AECS) LAB

B.Tech. III Year II Sem. **L T P C**
Course Code: EN606HS **0 0 3 2**

Introduction
A course on *Advanced English Communication Skills (AECS) Lab* is considered essential at the third year level of B.Tech and B.Pharmacy courses. At this stage, the students need to prepare themselves for their career which requires them to listen to, read, speak and write in English both for their professional and interpersonal communication. The main purpose of this course is to prepare the students of Engineering for their placements.

Course Objectives: This Lab focuses on using multi-media instruction for language development to meet the following targets:

- To improve students' fluency in spoken English
- To enable them to listen to English spoken at normal conversational speed
- To help students develop their vocabulary
- To read and comprehend texts in different contexts
- To communicate their ideas relevantly and coherently in writing
- To make students industry-ready
- To help students acquire behavioural skills for their personal and professional life
- To respond appropriately in different socio-cultural and professional contexts

Course Outcomes: Students will be able to:

- Acquire vocabulary and use it contextually
- Listen and speak effectively
- Develop proficiency in academic reading and writing
- Increase possibilities of job prospects
- Communicate confidently in formal and informal contexts

Syllabus
The following course activities will be conducted as part of the Advanced English Communication Skills (AECS) Lab:

1. **Inter-personal Communication and Building Vocabulary** - Starting a Conversation – Responding Appropriately and Relevantly – Using Appropriate Body Language – Role Play in Different Situations - Synonyms and Antonyms, One-word Substitutes, Prefixes and Suffixes, Idioms and Phrases and Collocations.
2. **Reading Comprehension** –General Vs Local Comprehension, Reading for Facts, Guessing Meanings from Context, , Skimming, Scanning, Inferring Meaning.
3. **Writing Skills** – Structure and Presentation of Different Types of Writing – Letter Writing/Resume Writing/ e-correspondence/ Technical Report Writing.
4. **Presentation Skills** – Oral Presentations (individual or group) through JAM Sessions/Seminars/PPTs and Written Presentations through Posters/Projects/Reports/ e-mails/Assignments… etc.,
5. **Group Discussion and Interview Skills** – Dynamics of Group Discussion, Intervention, Summarizing, Modulation of Voice, Body Language, Relevance, Fluency and Organization of Ideas and Rubrics of Evaluation- Concept and Process,

Pre-interview Planning, Opening Strategies, Answering Strategies, Interview through Tele-conference & Video-conference and Mock Interviews.

Minimum Hardware Requirement:
Advanced English Communication Skills (AECS) Laboratory shall have the following infrastructural facilities to accommodate at least 35 students in the lab:

- **Spacious room with appropriate acoustics**
- **Eight round tables with five movable chairs for each table.**
- **Audio-visual aids**
- **LCD Projector**
- **Public Address system**
- **Computer with suitable configuration**

Suggested Software: The software consisting of the prescribed topics elaborated above should be procured and used.

- **Oxford Advanced Learner's Compass**, 8th Edition
- **DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice.**

REFERENCES:

1. Kumar, Sanjay and Pushp Lata. ***English for Effective Communication***, Oxford University Press, 2015.
2. Konar, Nira. ***English Language Laboratories – A Comprehensive Manual,*** PHI Learning Pvt. Ltd., 2011.

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

B.TECH. ELECTRONICS AND COMMUNICATION ENGINEERING
IV YEAR COURSE STRUCTURE & SYLLABUS (R16)

Applicable From 2016-17 Admitted Batch

IV YEAR I SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1	EC701PC	Microwave Engineering	4	0	0	4
2		Professional Elective - II	3	0	0	3
3		Professional Elective - III	3	0	0	3
4		Professional Elective - IV	3	0	0	3
5	EC702PC	VLSI Design	4	0	0	4
6	EC703PC	VLSI and E-CAD Lab	0	0	3	2
7	EC704PC	Microwave Engineering Lab	0	0	3	2
8	EC705PC	Industry Oriented Mini Project	0	0	3	2
9	EC706PC	Seminar	0	0	2	1
		Total Credits	**17**	**0**	**11**	**24**

IV YEAR II SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1		Open Elective – III	3	0	0	3
2		Professional Elective -V	3	0	0	3
3		Professional Elective -VI	3	0	0	3
4	EC801PC	Major Project	0	0	30	15
		Total Credits	**9**	**0**	**30**	24

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MICROWAVE ENGINEERING

B.Tech. IV Year I Sem. **L T P C**
Course Code: EC701PC/ET743PE **4 0 0 4**

Course Objectives: This is a core course in Microwave Communications domain, and covers contents related to Microwave Theory and Techniques. The main objectives of the course are:

- To get familiarized with microwave frequency bands, their applications and to understand the limitations and losses of conventional tubes at these frequencies.
- To develop the theory related to microwave transmission lines, and to determine the characteristics of rectangular waveguides, microstrip lines, and different types of waveguide components and ferrite devices.
- To distinguish between different types of microwave tubes, their structures and principles of microwave power generation, and to characterize their performance features and applications - at tube levels as well as with solid state devices.
- To impart the knowledge of Scattering Matrix, its formulation and utility, and establish the S-Matrix for various types of microwave junctions.
- To understand the concepts of microwave measurements, identify the equipment required and precautions to be taken, and get familiarized with the methods of measurement of microwave power and various other microwave parameters.

Course Outcomes: Having gone through this course covering different aspects of microwave theory and techniques, the students would be able to

- To analyze completely the rectangular waveguides, their mode characteristics, and design waveguides for solving practical microwave transmission line problems.
- To distinguish between the different types of waveguide and ferrite components, explain their functioning and select proper components for engineering applications.
- To distinguish between the methods of power generation at microwave frequencies, derive the performance characteristics of 2-Cavity and Relfex Klystrons, Magnetrons, TWTs and estimate their efficiency levels, and solve related numerical problems
- To realize the need for solid state microwave sources, understand the concepts of TEDs, RWH Theory and explain the salient features of Gunn Diodes and ATT Devices.
- To establish the properties of Scattering Matrix, formulate the S-Matrix for various microwave junctions, and understand the utility of S-parameters in microwave component design.
- To set up a microwave bench, establish the measurement procedure and conduct the experiments in microwave lab for measurement of various microwave parameters.

UNIT - I

Microwave Transmission Lines - I: Introduction, Microwave Spectrum and Bands, Applications of Microwaves. Rectangular Waveguides – Solution of Wave Equations in

Rectangular Coordinates, TE/TM mode analysis, Expressions for Fields, Characteristic Equation and Cut-off Frequencies, Filter Characteristics, Dominant and Degenerate Modes, Sketches of TE and TM mode fields in the cross-section, Mode Characteristics – Phase and Group Velocities, Wavelengths and Impedance Relations, Power Transmission, Impossibility of TEM Mode. Illustrative Problems, Micro strip Lines– Introduction, Z_o Relations, Effective Dielectric Constant.

UNIT - II

Cavity Resonators– Introduction, Rectangular Cavities, Dominant Modes and Resonant Frequencies, Q Factor and Coupling Coefficients, Illustrative Problems

Waveguide Components and Applications: Coupling Mechanisms – Probe, Loop, Aperture types. Waveguide Discontinuities – Waveguide Windows, Tuning Screws and Posts, Matched Loads. Waveguide Attenuators – Different Types, Resistive Card and Rotary Vane Attenuators; Waveguide Phase Shifters – Types, Dielectric and Rotary Vane Phase Shifters, Waveguide Multiport Junctions – E plane and H plane Tees, Magic Tee. Directional Couplers – 2 Hole, Bethe Hole types, Illustrative Problems

Ferrites– Composition and Characteristics, Faraday Rotation, Ferrite Components – Gyrator, Isolator, Circulator.

UNIT - III

Microwave Tubes: Limitations and Losses of conventional Tubes at Microwave Frequencies, Microwave Tubes – O Type and M Type Classifications, O-type Tubes : 2 Cavity Klystrons – Structure, Reentrant Cavities, Velocity Modulation Process and Applegate Diagram, Bunching Process and Small Signal Theory – Expressions for O/P Power and Efficiency. Reflex Klystrons – Structure, Velocity Modulation and Applegate Diagram, Mathematical Theory of Bunching, Power Output, Efficiency, Oscillating Modes and O/P Characteristics, Illustrative Problems.

Helix TWTs: Significance, Types and Characteristics of Slow Wave Structures; Structure of TWT and Amplification Process (qualitative treatment), Suppression of Oscillations, Gain Considerations.

UNIT - IV

M-Type Tubes:

Introduction, Cross-field Effects, Magnetrons – Different Types, Cylindrical Traveling Wave Magnetron – Hull Cut-off and Hartree Conditions, Modes of Resonance and PI-Mode Operation, Separation of PI-Mode, o/p characteristics, Illustrative Problems

Microwave Solid State Devices: Introduction, Classification, Applications. TEDs – Introduction, Gunn Diodes – Principle, RWH Theory, Characteristics, Modes of Operation - Gunn Oscillation Modes, Introduction to Avalanche Transit Time Devices.

UNIT - V

Scattering Matrix– Significance, Formulation and Properties, S Matrix Calculations for – 2 port Junctions, E plane and H plane Tees, Magic Tee, Circulator and Isolator, Illustrative Problems.

Microwave Measurements: Description of Microwave Bench – Different Blocks and their Features, Errors and Precautions, Microwave Power Measurement, Bolometers. Measurement of Attenuation, Frequency. Standing Wave Measurements – Measurement of Low and High VSWR, Cavity Q, Impedance Measurements.

TEXT BOOKS:

1. Microwave Devices and Circuits – Samuel Y. Liao, Pearson, 3rd Edition, 2003.
2. Microwave Principles – Herbert J. Reich, J.G. Skalnik, P.F. Ordung and H.L. Krauss, CBS Publishers and Distributors, New Delhi, 2004.

REFERENCES:

1. Foundations for Microwave Engineering – R.E. Collin, IEEE Press, John Wiley, 2nd Edition, 2002.
2. Microwave Engineering - G.S. Raghuvanshi, Cengage Learning India Pvt. Ltd., 2012.
3. Microwave Engineering Passive Circuits – Peter A. Rizzi, PHI, 1999.
4. Microwave Engineering - David M. Pozar, John Wiley & Sons (Asia) Pvt Ltd., 1989, 3r ed., 2011 Reprint.

MICROWAVE ENGINEERING LAB

B.Tech. IV Year I Sem.
Course Code: EC704PC

L	T	P	C
0	0	3	2

Note: Minimum of 12 experments to be conducted

1. Reflex Klystron Characteristics
2. Gunn Diode Characteristics
3. Directional Coupler Characteristics
4. VSWR Measurement of Mached load
5. VSWR mesurement of with open and short circuit loads
6. Measurement of Waveguide Parameters
7. Measurement of Impedance of a given Load
8. Measurement of Scattering Parameters of a E plane Tee
9. Measurement of Scattering Parameters of a H plane Tee
10. Measurement of Scattering Parameters of a Magic Tee
11. Measurement of Scattering Parameters of a Circulator
12. Attenuation Measurement
13. Microwave Frequency Measurement
14. Antenna Pattern Measurements.

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VLSI DESIGN

B.Tech. IV Year I Sem. **L T P C**
Course Code: EC702PC/ET721PE/EI741PE 4 0 0 4

Course Objectives: The objectives of the course are to:

- Give exposure to different steps involved in the fabrication of ICs using MOS transistor, CMOS/BICMOS transistors, and passive components.
- Explain electrical properties of MOS and BiCMOS devices to analyze the behavior of inverters designed with various loads.
- Give exposure to the design rules to be followed to draw the layout of any logic circuit.
- Provide concept to design different types of logic gates using CMOS inverter and analyze their transfer characteristics.
- Provide design concepts to design building blocks of data path of any system using gates.
- Understand basic programmable logic devices and testing of CMOS circuits.

Course Outcomes: Upon successfully completing the course, the student should be able to:

- Acquire qualitative knowledge about the fabrication process of integrated circuit using MOS transistors.
- Choose an appropriate inverter depending on specifications required for a circuit
- Draw the layout of any logic circuit which helps to understand and estimate parasitic of any logic circuit
- Design different types of logic gates using CMOS inverter and analyze their transfer characteristics
- Provide design concepts required to design building blocks of data path using gates.
- Design simple memories using MOS transistors and can understand design of large memories.
- Design simple logic circuit using PLA, PAL, FPGA and CPLD.
- Understand different types of faults that can occur in a system and learn the concept of testing and adding extra hardware to improve testability of system

UNIT – I

Introduction: Introduction to IC Technology – MOS, PMOS, NMOS, CMOS & BiCMOS
Basic Electrical Properties: Basic Electrical Properties of MOS and BiCMOS Circuits: I_{ds}-V_{ds} relationships, MOS transistor threshold Voltage, g_m, g_{ds}, Figure of merit ωo; Pass transistor, NMOS Inverter, Various pull ups, CMOS Inverter analysis and design, Bi-CMOS Inverters.

UNIT - II

VLSI Circuit Design Processes: VLSI Design Flow, MOS Layers, Stick Diagrams, Design Rules and Layout, 2 μm CMOS Design rules for wires, Contacts and Transistors Layout Diagrams for NMOS and CMOS Inverters and Gates, Scaling of MOS circuits.

UNIT – III

Gate Level Design: Logic Gates and Other complex gates, Switch logic, Alternate gate circuits, Time delays, Driving large capacitive loads, Wiring capacitance, Fan – in, Fan – out, Choice of layers.

UNIT - IV

Data Path Subsystems: Subsystem Design, Shifters, Adders, ALUs, Multipliers, Parity generators, Comparators, Zero/One Detectors, Counters.

Array Subsystems: SRAM, DRAM, ROM, Serial Access Memories.

UNIT - V

Programmable Logic Devices: PLAs, FPGAs, CPLDs, Standard Cells, Programmable Array Logic, Design Approach, Parameters influencing low power design.

CMOS Testing: CMOS Testing, Need for testing, Test Principles, Design Strategies for test, Chip level Test Techniques.

TEXT BOOKS:

1. Essentials of VLSI circuits and systems – Kamran Eshraghian, Eshraghian Dougles and A. Pucknell, PHI, 2005 Edition
2. CMOS VLSI Design – A Circuits and Systems Perspective, Neil H. E Weste, David Harris, Ayan Banerjee, 3rd Ed, Pearson, 2009.

REFERENCE BOOKS:

1. CMOS logic circuit Design - John .P. Uyemura, Springer, 2007.
2. Modern VLSI Design - Wayne Wolf, Pearson Education, 3rd Edition, 1997.

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VLSI & E-CAD LAB

B.Tech. IV Year I Sem. **L T P C**
Course Code: EC703PC **0 0 3 2**

List of Experiments
Design and implementation of the following CMOS digital/analog circuits using **Cadence / Mentor Graphics / Synopsys /Equivalent** CAD tools. The design shall include Gate-level design, Transistor-level design, Hierarchical design, Verilog HDL/VHDL design, Logic synthesis, Simulation and verification, Scaling of CMOS Inverter for different technologies, study of secondary effects (temperature, power supply and process corners), Circuit optimization with respect to area, performance and/or power, Layout, Extraction of parasitics and back annotation, modifications in circuit parameters and layout consumption, DC/transient analysis, Verification of layouts (DRC, LVS)

E-CAD programs:
Programming can be done using any complier. Down load the programs on FPGA/CPLD boards and performance testing may be done using pattern generator (32 channels) and logic analyzer apart from verification by simulation with any of the front end tools.

1. HDL code to realize all the logic gates
2. Design of 2-to-4 decoder
3. Design of 8-to-3 encoder (without and with priority)
4. Design of 8-to-1 multiplexer and 1-to-8 demultiplexer
5. Design of 4 bit binary to gray code converter
6. Design of 4 bit comparator
7. Design of Full adder using 3 modeling styles
8. Design of flip flops: SR, D, JK, T
9. Design of 4-bit binary, BCD counters (synchronous/ asynchronous reset) or any sequence counter
10. Finite State Machine Design

VLSI programs:
- Introduction to layout design rules. Layout, physical verification, placement & route for complex design, static timing analysis, IR drop analysis and crosstalk analysis of the following:
  1. Basic logic gates
  2. CMOS inverter
  3. CMOS NOR/ NAND gates
  4. CMOS XOR and MUX gates
  5. Static / Dynamic logic circuit (register cell)
  6. Latch
  7. Pass transistor
  8. Layout of any combinational circuit (complex CMOS logic gate).
  9. Analog Circuit simulation (AC analysis) – CS & CD amplifier

Note: Any SIX of the above experiments from each part are to be conducted (Total 12)

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ELECTRONICS AND COMMUNICATION ENGINEERING

2020-2021

S. No	Regulations	Number of Courses	Year of Study
1	R18	33	I, II and III years I & II Semesters
2	R16	7	IV year I & II Semesters

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
B.Tech. in ELECTRONICS AND COMMUNICATION ENGINEERING
COURSE STRUCTURE & SYLLABUS (R18)

Applicable From 2018-19 Admitted Batch

I YEAR I SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA101BS	Mathematics - I	3	1	0	4
2	AP102BS	Applied Physics	3	1	0	4
3	CS103ES	Programming for Problem Solving	3	1	0	4
4	ME104ES	Engineering Graphics	1	0	4	3
5	AP105BS	Applied Physics Lab	0	0	3	1.5
6	CS106ES	Programming for Problem Solving Lab	0	0	3	1.5
7	*MC109ES	Environmental Science	3	0	0	0
		Induction Programme				
		Total Credits	**13**	**3**	**10**	**18**

I YEAR II SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA201BS	Mathematics - II	3	1	0	4
2	CH202BS	Chemistry	3	1	0	4
3	EE203ES	Basic Electrical Engineering	3	0	0	3
4	ME205ES	Engineering Workshop	1	0	3	2.5
5	EN205HS	English	2	0	0	2
6	CH206BS	Engineering Chemistry Lab	0	0	3	1.5
7	EN207HS	English Language and Communication Skills Lab	0	0	2	1
8	EE208ES	Basic Electrical Engineering Lab	0	0	2	1
		Total Credits	**12**	**2**	**10**	**19**

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AP102BS/AP202BS: APPLIED PHYSICS

B.Tech. I Year I Sem.

L	T	P	C
3	1	0	4

Course Objectives:

- Students will demonstrate skills in scientific inquiry, problem solving and laboratory techniques.
- Students will be able to demonstrate competency and understanding of the concepts found in Quantum Mechanics, Fiber optics and lasers, Semiconductor physics and Electromagnetic theory and a broad base of knowledge in physics.
- The graduates will be able to solve non-traditional problems that potentially draw on knowledge in multiple areas of physics.
- To study applications in engineering like memory devices, transformer core and electromagnetic machinery.

Course Outcomes: Upon graduation:

- The student would be able to learn the fundamental concepts on Quantum behaviour of matter in its micro state.
- The knowledge of fundamentals of Semiconductor physics, Optoelectronics, Lasers and fibre optics enable the students to apply to various systems like communications, solar cell, photo cells and so on.
- Design, characterization and study of properties of material help the students to prepare new materials for various engineering applications.
- The course also helps the students to be exposed to the phenomena of electromagnetism and also to have exposure on magnetic materials and dielectric materials.

UNIT-I: Quantum Mechanics

Introduction to quantum physics, Black body radiation, Planck's law, Photoelectric effect, Compton effect, de-Broglie's hypothesis, Wave-particle duality, Davisson and Germer experiment, Heisenberg's Uncertainty principle, Born's interpretation of the wave function, Schrodinger's time independent wave equation, Particle in one dimensional box.

UNIT-II: Semiconductor Physics

Intrinsic and Extrinsic semiconductors, Dependence of Fermi level on carrier-concentration and temperature, Carrier generation and recombination, Carrier transport: diffusion and drift, Hall effect, p- n junction diode, Zener diode and their V-I Characteristics, Bipolar Junction Transistor (BJT): Construction, Principle of operation.

UNIT-III: Optoelectronics

Radiative and non-radiative recombination mechanisms in semiconductors, LED and semiconductor lasers: Device structure, Materials, Characteristics and figures of merit, Semiconductor photodetectors: Solar cell, PIN and Avalanche and their structure, Materials, working principle and Characteristics.

UNIT-IV: Lasers and Fibre Optics

Lasers: Introduction to interaction of radiation with matter, Coherence, Principle and working of Laser, Population inversion, Pumping, Types of Lasers: Ruby laser, Carbon dioxide (CO_2) laser, He-Ne laser, Applications of laser. Fibre Optics: Introduction, Optical fibre as a dielectric wave guide, Total internal reflection, Acceptance angle, Acceptance cone and Numerical aperture, Step and Graded index fibres, Losses associated with optical fibres, Applications of optical fibres.

UNIT-V: Electromagnetism and Magnetic Properties of Materials

Laws of electrostatics, Electric current and the continuity equation, Ampere's and Faraday's laws, Maxwell's equations, Polarisation, Permittivity and Dielectric constant, Internal fields in a solid, Clausius-Mossotti equation, Ferroelectrics and Piezoelectrics. Magnetisation, permeability and susceptibility, Classification of magnetic materials, Ferromagnetism and ferromagnetic domains, Hysteresis, Applications of magnetic materials.

TEXT BOOKS:

1. Engineering Physics, B.K. Pandey, S. Chaturvedi - Cengage Learing.
2. Halliday and Resnick, Physics - Wiley.
3. A textbook of Engineering Physics, Dr. M. N. Avadhanulu, Dr. P.G. Kshirsagar - S. Chand

REFERENCE BOOKS:

1. Richard Robinett, Quantum Mechanics
2. J. Singh, Semiconductor Optoelectronics: Physics and Technology, Mc Graw-Hill inc. (1995).
3. Online Course: "Optoelectronic Materials and Devices" by Monica Katiyar and Deepak Gupta on NPTEL

AP105BS/AP205BS: APPLIED PHYSICS LAB

B.Tech. I Year I Sem.

L	T	P	C
0	0	3	1.5

List of Experiments:

1. Energy gap of P-N junction diode:
   To determine the energy gap of a semiconductor diode.

2. Solar Cell:
   To study the V-I Characteristics of solar cell.

3. Light emitting diode:
   Plot V-I and P-I characteristics of light emitting diode.

4. Stewart – Gee's experiment:
   Determination of magnetic field along the axis of a current carrying coil.

5. Hall effect:
   To determine Hall co-efficient of a given semiconductor.

6. Photoelectric effect:
   To determine work function of a given material.

7. LASER:
   To study the characteristics of LASER sources.

8. Optical fibre:
   To determine the bending losses of Optical fibres.

9. LCR Circuit:
   To determine the Quality factor of LCR Circuit.

10. R-C Circuit:
    To determine the time constant of R-C circuit.

Note: Any 8 experiments are to be performed

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CS103ES/CS203ES: PROGRAMMING FOR PROBLEM SOLVING

B.Tech. I Year I Sem.

L	T	P	C
3	1	0	4

Course Objectives:

- To learn the fundamentals of computers.
- To understand the various steps in program development.
- To learn the syntax and semantics of C programming language.
- To learn the usage of structured programming approach in solving problems.

Course Outcomes: The student will learn

- To write algorithms and to draw flowcharts for solving problems.
- To convert the algorithms/flowcharts to C programs.
- To code and test a given logic in C programming language.
- To decompose a problem into functions and to develop modular reusable code.
- To use arrays, pointers, strings and structures to write C programs.
- Searching and sorting problems.

UNIT - I: Introduction to Programming

Introduction to components of a computer system: disks, primary and secondary memory, processor, operating system, compilers, creating, compiling and executing a program etc., Number systems Introduction to Algorithms: steps to solve logical and numerical problems. Representation of Algorithm, Flowchart/Pseudo code with examples, Program design and structured programming Introduction to C Programming Language: variables (with data types and space requirements), Syntax and Logical Errors in compilation, object and executable code , Operators, expressions and precedence, Expression evaluation, Storage classes (auto, extern, static and register), type conversion, The main method and command line arguments

Bitwise operations: Bitwise AND, OR, XOR and NOT operators

Conditional Branching and Loops: Writing and evaluation of conditionals and consequent branching with if, if-else, switch-case, ternary operator, goto, Iteration with for, while, do-while loops

I/O: Simple input and output with scanf and printf, formatted I/O, Introduction to stdin, stdout and stderr. Command line arguments

UNIT - II: Arrays, Strings, Structures and Pointers:

Arrays: one and two dimensional arrays, creating, accessing and manipulating elements of arrays Strings: Introduction to strings, handling strings as array of characters, basic string functions available in C (strlen, strcat, strcpy, strstr etc.), arrays of strings

Structures: Defining structures, initializing structures, unions, Array of structures

Pointers: Idea of pointers, Defining pointers, Pointers to Arrays and Structures, Use of Pointers in self-referential structures, usage of self referential structures in linked list (no implementation) Enumeration data type

UNIT - III: Preprocessor and File handling in C:

Preprocessor: Commonly used Preprocessor commands like include, define, undef, if, ifdef, ifndef Files: Text and Binary files, Creating and Reading and writing text and binary files, Appending data to existing files, Writing and reading structures using binary files, Random access using fseek, ftell and rewind functions.

UNIT - IV: Function and Dynamic Memory Allocation:

Functions: Designing structured programs, Declaring a function, Signature of a function, Parameters and return type of a function, passing parameters to functions, call by value, Passing arrays to functions, passing pointers to functions, idea of call by reference, Some C standard functions and libraries

Recursion: Simple programs, such as Finding Factorial, Fibonacci series etc., Limitations of Recursive functions

Dynamic memory allocation: Allocating and freeing memory, Allocating memory for arrays of different data types.

UNIT - V: Introduction to Algorithms:

Algorithms for finding roots of a quadratic equations, finding minimum and maximum numbers of a given set, finding if a number is prime number, etc.

Basic searching in an array of elements (linear and binary search techniques),

Basic algorithms to sort array of elements (Bubble, Insertion and Selection sort algorithms), Basic concept of order of complexity through the example programs

CS106ES/CS206ES: PROGRAMMING FOR PROBLEM SOLVING LAB

B.Tech. I Year I Sem.

L	T	P	C
0	0	3	1.5

[Note:The programs may be executed using any available Open Source/ Freely available IDE
Some of the Tools available are:
CodeLite: https://codelite.org/
Code::Blocks: http://www.codeblocks.org/
DevCpp : http://www.bloodshed.net/devcpp.html
Eclipse: http://www.eclipse.org
This list is not exhaustive and is NOT in any order of preference]

Course Objectives: The students will learn the following:

- To work with an IDE to create, edit, compile, run and debug programs
- To analyze the various steps in program development.
- To develop programs to solve basic problems by understanding basic concepts in C like operators, control statements etc.
- To develop modular, reusable and readable C Programs using the concepts like functions, arrays etc.
- To Write programs using the Dynamic Memory Allocation concept.
- To create, read from and write to text and binary files

Course Outcomes: The candidate is expected to be able to:

- formulate the algorithms for simple problems
- translate given algorithms to a working and correct program
- correct syntax errors as reported by the compilers
- identify and correct logical errors encountered during execution
- represent and manipulate data with arrays, strings and structures
- use pointers of different types
- create, read and write to and from simple text and binary files
- modularize the code with functions so that they can be reused

Practice sessions:

a. Write a simple program that prints the results of all the operators available in C (including pre/ post increment, bitwise and/or/not, etc.). Read required operand values from standard input.
b. Write a simple program that converts one given data type to another using auto conversion and casting. Take the values form standard input.

Simple numeric problems:

a. Write a program for fiend the max and min from the three numbers.
b. Write the program for the simple, compound interest.
c. Write program that declares Class awarded for a given percentage of marks, where mark <40%= Failed, 40% to <60% = Second class, 60% to <70%=First class, >= 70% = Distinction. Read percentage from standard input.
d. Write a program that prints a multiplication table for a given number and the number of rows in the table. For example, for a number 5 and rows = 3, the output should be:
e. 5 x 1 = 5
f. 5 x 2 = 10
g. 5 x 3 = 15
h. Write a program that shows the binary equivalent of a given positive number between 0 to 255.

Expression Evaluation:

a. A building has 10 floors with a floor height of 3 meters each. A ball is dropped from the top of the building. Find the time taken by the ball to reach each floor. (Use the formula s = ut+(1/2)at^2 where u and a are the initial velocity in m/sec (= 0) and acceleration in m/sec^2 (= 9.8 m/s^2)).
b. Write a C program, which takes two integer operands and one operator from the user, performs the operation and then prints the result. (Consider the operators +,-,*, /, % and use Switch Statement)

c. Write a program that finds if a given number is a prime number
d. Write a C program to find the sum of individual digits of a positive integer and test given number is palindrome.
e. A Fibonacci sequence is defined as follows: the first and second terms in the sequence are 0 and 1. Subsequent terms are found by adding the preceding two terms in the sequence. Write a C program to generate the first n terms of the sequence.
f. Write a C program to generate all the prime numbers between 1 and n, where n is a value supplied by the user.
g. Write a C program to find the roots of a Quadratic equation.
h. Write a C program to calculate the following, where x is a fractional value.
i. 1-x/2 +x^2/4-x^3/6
j. Write a C program to read in two numbers, x and n, and then compute the sum of this geometric progression: 1+x+x^2+x^3+............+x^n. For example: if n is 3 and x is 5, then the program computes 1+5+25+125.

Arrays and Pointers and Functions:

a. Write a C program to find the minimum, maximum and average in an array of integers.
b. Write a functions to compute mean, variance, Standard Deviation, sorting of n elements in single dimension array.
c. Write a C program that uses functions to perform the following:
d. Addition of Two Matrices
e. ii. Multiplication of Two Matrices
f. iii. Transpose of a matrix with memory dynamically allocated for the new matrix as row and column counts may not be same.
g. Write C programs that use both recursive and non-recursive functions
h. To find the factorial of a given integer.
i. ii. To find the GCD (greatest common divisor) of two given integers.
j. iii. To find x^n
k. Write a program for reading elements using pointer into array and display the values using array.
l. Write a program for display values reverse order from array using pointer.
m. Write a program through pointer variable to sum of n elements from array.

Files:

a. Write a C program to display the contents of a file to standard output device.
b. Write a C program which copies one file to another, replacing all lowercase characters with their uppercase equivalents.
c. Write a C program to count the number of times a character occurs in a text file. The file name and the character are supplied as command line arguments.
d. Write a C program that does the following:
It should first create a binary file and store 10 integers, where the file name and 10 values are given in the command line. (hint: convert the strings using atoi function)
Now the program asks for an index and a value from the user and the value at that index should be changed to the new value in the file. (hint: use fseek function)
The program should then read all 10 values and print them back.
e. Write a C program to merge two files into a third file (i.e., the contents of the firs t file followed by those of the second are put in the third file).

Strings:

a. Write a C program to convert a Roman numeral ranging from I to L to its decimal equivalent.
b. Write a C program that converts a number ranging from 1 to 50 to Roman equivalent
c. Write a C program that uses functions to perform the following operations:
d. To insert a sub-string in to a given main string from a given position.
e. ii. To delete n Characters from a given position in a given string.
f. Write a C program to determine if the given string is a palindrome or not (Spelled same in both directions with or without a meaning like madam, civic, noon, abcba, etc.)
g. Write a C program that displays the position of a character ch in the string S or – 1 if S doesn't contain ch.
h. Write a C program to count the lines, words and characters in a given text.

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Miscellaneous:

a. Write a menu driven C program that allows a user to enter n numbers and then choose between finding the smallest, largest, sum, or average. The menu and all the choices are to be functions. Use a switch statement to determine what action to take. Display an error message if an invalid choice is entered.

b. Write a C program to construct a pyramid of numbers as follows:

1	*	1	1	*
1 2	* *	2 3	2 2	* *
1 2 3	* * *	4 5 6	3 3 3	* * *
			4 4 4 4	* *
				*

Sorting and Searching:

a. Write a C program that uses non recursive function to search for a Key value in a given
b. list of integers using linear search method.
c. Write a C program that uses non recursive function to search for a Key value in a given
d. sorted list of integers using binary search method.
e. Write a C program that implements the Bubble sort method to sort a given list of
f. integers in ascending order.
g. Write a C program that sorts the given array of integers using selection sort in descending order
h. Write a C program that sorts the given array of integers using insertion sort in ascending order
i. Write a C program that sorts a given array of names

Suggested Reference Books for solving the problems:

i. Byron Gottfried, Schaum's Outline of Programming with C, McGraw-Hill
ii. B.A. Forouzan and R.F. Gilberg C Programming and Data Structures, Cengage Learning, (3rd Edition)
iii. Brian W. Kernighan and Dennis M. Ritchie, The C Programming Language, Prentice
iv. Hall of India
v. R.G. Dromey, How to solve it by Computer, Pearson (16th Impression)
vi. Programming in C, Stephen G. Kochan, Fourth Edition, Pearson Education.
vii. Herbert Schildt, C: The Complete Reference, Mc Graw Hill, 4th Edition

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CH102BS/CH202BS: CHEMISTRY

B.Tech. I Year II Sem.

L	T	P	C
3	1	0	4

Course Objectives:
- To bring adaptability to the concepts of chemistry and to acquire the required skills to become a perfect engineer.
- To impart the basic knowledge of atomic, molecular and electronic modifications which makes the student to understand the technology based on them.
- To acquire the knowledge of electrochemistry, corrosion and water treatment which are essential for the Engineers and in industry.
- To acquire the skills pertaining to spectroscopy and to apply them for medical and other fields.
- To impart the knowledge of stereochemistry and synthetic aspects useful for understanding reaction pathways

Course Outcomes: The basic concepts included in this course will help the student to gain:
- The knowledge of atomic, molecular and electronic changes, band theory related to conductivity.
- The required principles and concepts of electrochemistry, corrosion and in understanding the problem of water and its treatments.
- The required skills to get clear concepts on basic spectroscopy and application to medical and other fields.
- The knowledge of configurational and conformational analysis of molecules and reaction mechanisms.

UNIT - I:
Molecular structure and Theories of Bonding: Atomic and Molecular orbitals. Linear Combination of Atomic Orbitals (LCAO), molecular orbitals of diatomic molecules, molecular orbital energy level diagrams of N_2, O_2 and F_2 molecules. π molecular orbitals of butadiene and benzene.
Crystal Field Theory (CFT): Salient Features of CFT – Crystal Field Splitting of transition metal ion d-orbitals in Tetrahedral, Octahedral and square planar geometries. Band structure of solids and effect of doping on conductance.

UNIT - II:
Water and its treatment: Introduction – hardness of water – Causes of hardness - Types of hardness: temporary and permanent – expression and units of hardness – Estimation of hardness of water by complexometric method. Potable water and its specifications. Steps involved in treatment of water – Disinfection of water by chlorination and ozonization. Boiler feed water and its treatment – Calgon conditioning, Phosphate conditioning and Colloidal conditioning. External treatment of water – Ion exchange process. Desalination of water – Reverse osmosis. Numerical problems.

UNIT - III:
Electrochemistry and corrosion: Electro chemical cells – electrode potential, standard electrode potential, types of electrodes – calomel, Quinhydrone and glass electrode. Nernst equation Determination of pH of a solution by using quinhydrone and glass electrode. Electrochemical series and its applications. Numerical problems. Potentiometric titrations. Batteries – Primary (Lithium cell) and secondary batteries (Lead – acid storage battery and Lithium ion battery).
Causes and effects of corrosion – theories of chemical and electrochemical corrosion – mechanism of electrochemical corrosion, Types of corrosion: Galvanic, water-line and pitting corrosion. Factors affecting rate of corrosion, Corrosion control methods- Cathodic protection – Sacrificial anode and impressed current cathodic methods. Surface coatings – metallic coatings – methods of application. Electroless plating of Nickel.

UNIT - IV:
Stereochemistry, Reaction Mechanism and synthesis of drug molecules: Introduction to representation of 3-dimensional structures, Structural and stereoisomers, configurations, symmetry and chirality. Enantiomers, diastereomers, optical activity and Absolute configuration. Conformation alanalysis of n- butane.
Substitution reactions: Nucleophilic substitution reactions: Mechanism of S_N1, S_N2 reactions. Electrophilic and nucleophilic addition reactions: Addition of HBr to propene. Markownikoff and anti

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Markownikoff's additions. Grignard additions on carbonyl compounds. Elimination reactions: Dehydro halogenation of alkylhalides. Saytzeff rule. Oxidation reactions: Oxidation of alcohols using $KMnO_4$ and chromic acid.
Reduction reactions: reduction of carbonyl compounds using $LiAlH_4$ & $NaBH_4$. Hydroboration of olefins. Structure, synthesis and pharmaceutical applications of Paracetamol and Aspirin.

UNIT - V:
Spectroscopic techniques and applications: Principles of spectroscopy, selection rules and applications of electronic spectroscopy. vibrational and rotational spectroscopy. Basic concepts of Nuclear magnetic resonance Spectroscopy, chemical shift. Introduction to Magnetic resonance imaging.

TEXT BOOKS:
1. Physical Chemistry, P.W. Atkins, 10th Edn, Oxford University Press.
2. Engineering Chemistry by P.C.Jain & M.Jain; Dhanpat Rai Publishing Company (P) Ltd., New Delhi.
3. Fundamentals of Molecular Spectroscopy, by C.N. Banwell, 4th Edn, McGraw Hill Publishing.
4. Organic Chemistry: Structure and Function by K.P.C. Volhardt and N.E.Schore, 5th Edition, Macmillan International Higher Education.
5. University Chemistry, by B.M. Mahan, Pearson IV Edition.
6. Engineering Chemistry (NPTEL Web-book), by B.L. Tembe, Kamaluddin and M.S. Krishnan

CH106BS/CH206BS: ENGINEERING CHEMISTRY LAB

B.Tech. I Year II Sem.

L	T	P	C
0	0	3	1.5

Course Objectives: The course consists of experiments related to the principles of chemistry required for engineering student. The student will learn:

- Estimation of hardness and chloride content in water to check its suitability for drinking purpose.
- To determine the rate constant of reactions from concentrations as an function of time.
- The measurement of physical properties like adsorption and viscosity.
- To synthesize the drug molecules and check the purity of organic molecules by thin layer chromatographic (TLC) technique.

Course Outcomes: The experiments will make the student gain skills on:

- Determination of parameters like hardness and chloride content in water.
- Estimation of rate constant of a reaction from concentration – time relationships.
- Determination of physical properties like adsorption and viscosity.
- Calculation of R_f values of some organic molecules by TLC technique.

List of Experiments:

1. Determination of total hardness of water by complexometric method using EDTA
2. Determination of chloride content of water by Argentometry
3. Estimation of an HCl by Conductometric titrations
4. Estimation of Acetic acid by Conductometric titrations
5. Estimation of HCl by Potentiometric titrations
6. Estimation of Fe^{2+} by Potentiometry using $KMnO_4$
7. Determination of rate constant of acid catalysed hydrolysis of methyl acetate
8. Synthesis of Aspirin and Paracetamol
9. Thin layer chromatography calculation of R_f values. eg ortho and para nitro phenols
10. Determination of acid value of coconut oil
11. Verification of freundlich adsorption isotherm-adsorption of acetic acid on charcoal
12. Determination of viscosity of castor oil and ground nut oil by using Ostwald's viscometer.
13. Determination of partition coefficient of acetic acid between n-butanol and water.
14. Determination of surface tension of a give liquid using stalagmometer.

References

1. Senior practical physical chemistry, B.D. Khosla, A. Gulati and V. Garg (R. Chand & Co., Delhi)
2. An introduction to practical chemistry, K.K. Sharma and D. S. Sharma (Vikas publishing, N. Delhi)
3. Vogel's text book of practical organic chemistry 5th edition
4. Text book on Experiments and calculations in Engineering chemistry – S.S. Dara

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EE103ES/EE203ES: BASIC ELECTRICAL ENGINEERING

B.Tech. I Year II Sem.

L	T	P	C
3	0	0	3

Course Objectives:
- To introduce the concepts of electrical circuits and its components
- To understand magnetic circuits, DC circuits and AC single phase & three phase circuits
- To study and understand the different types of DC/AC machines and Transformers.
- To import the knowledge of various electrical installations.
- To introduce the concept of power, power factor and its improvement.

Course Outcomes:
- To analyze and solve electrical circuits using network laws and theorems.
- To understand and analyze basic Electric and Magnetic circuits
- To study the working principles of Electrical Machines
- To introduce components of Low Voltage Electrical Installations

UNIT-I: D.C. Circuits
Electrical circuit elements (R, L and C), voltage and current sources, KVL&KCL, analysis of simple circuits with dc excitation. Superposition, Thevenin and Norton Theorems.
Time-domain analysis of first-order RL and RC circuits.

UNIT-II: A.C. Circuits
Representation of sinusoidal waveforms, peak and rms values, phasor representation, real power, reactive power, apparent power, power factor, Analysis of single-phase ac circuits consisting of R, L, C, RL, RC, RLC combinations (series and parallel), resonance in series R-L-C circuit.
Three-phase balanced circuits, voltage and current relations in star and delta connections.

UNIT-III: Transformers
Ideal and practical transformer, equivalent circuit, losses in transformers, regulation and efficiency. Auto-transformer and three-phase transformer connections.

UNIT-IV: Electrical Machines
Generation of rotating magnetic fields, Construction and working of a three-phase induction motor, Significance of torque-slip characteristic. Loss components and efficiency, starting and speed control of induction motor. Single-phase induction motor. Construction, working, torque-speed characteristic and speed control of separately excited dc motor.
Construction and working of synchronous generators.

UNIT-V: Electrical Installations
Components of LT Switchgear: Switch Fuse Unit (SFU), MCB, ELCB, MCCB, Types of Wires and Cables, Earthing. Types of Batteries, Important Characteristics for Batteries. Elementary calculations for energy consumption, power factor improvement and battery backup.

TEXT /REFERENCE BOOKS:

1. Basic Electrical Engineering - D.P. Kothari and I.J. Nagrath, 3rd edition 2010, Tata McGraw Hill.
2. D.C. Kulshreshtha, "Basic Electrical Engineering", McGraw Hill, 2009.
3. L.S. Bobrow, Fundamentals of Electrical Engineering", Oxford University Press, 2011
4. Electrical and Electronics Technology, E. Hughes, 10th Edition, Pearson, 2010
5. Electrical Engineering Fundamentals, Vincent Deltoro, Second Edition, Prentice Hall India, 1989.

EE108ES/EE208ES: BASIC ELECTRICAL ENGINEERING LAB

B.Tech. I Year II Sem.

L	T	P	C
0	0	2	1

Course Objectives:
- To analyze a given network by applying various electrical laws and network theorems
- To know the response of electrical circuits for different excitations
- To calculate, measure and know the relation between basic electrical parameters.
- To analyze the performance characteristics of DC and AC electrical machines

Course Outcomes:
- Get an exposure to basic electrical laws.
- Understand the response of different types of electrical circuits to different excitations.
- Understand the measurement, calculation and relation between the basic electrical parameters
- Understand the basic characteristics of transformers and electrical machines.

List of experiments/demonstrations:
1. Verification of Ohms Law
2. Verification of KVL and KCL
3. Transient Response of Series RL and RC circuits using DC excitation
4. Transient Response of RLC Series circuit using DC excitation
5. Resonance in series RLC circuit
6. Calculations and Verification of Impedance and Current of RL, RC and RLC series circuits
7. Measurement of Voltage, Current and Real Power in primary and Secondary Circuits of a Single-Phase Transformer
8. Load Test on Single Phase Transformer (Calculate Efficiency and Regulation)
9. Three Phase Transformer: Verification of Relationship between Voltages and Currents (Star-Delta, Delta-Delta, Delta-star, Star-Star)
10. Measurement of Active and Reactive Power in a balanced Three-phase circuit
11. Performance Characteristics of a Separately/Self Excited DC Shunt/Compound Motor
12. Torque-Speed Characteristics of a Separately/Self Excited DC Shunt/Compound Motor
13. Performance Characteristics of a Three-phase Induction Motor
14. Torque-Speed Characteristics of a Three-phase Induction Motor
15. No-Load Characteristics of a Three-phase Alternator

EN105HS/EN205HS: ENGLISH

B.Tech. I Year II Sem.

L	T	P	C
2	0	0	2

INTRODUCTION

In view of the growing importance of English as a tool for global communication and the consequent emphasis on training students to acquire language skills, the syllabus of English has been designed to develop linguistic, communicative and critical thinking competencies of Engineering students.

In English classes, the focus should be on the skills development in the areas of vocabulary, grammar, reading and writing. For this, the teachers should use the prescribed text for detailed study. The students should be encouraged to read the texts leading to reading comprehension and different passages may be given for practice in the class. The time should be utilized for working out the exercises given after each excerpt, and also for supplementing the exercises with authentic materials of a similar kind, for example, newspaper articles, advertisements, promotional material etc. *The focus in this syllabus is on skill development, fostering ideas and practice of language skills in various contexts and cultures.*

Learning Objectives: The course will help to

a. Improve the language proficiency of students in English with an emphasis on Vocabulary, Grammar, Reading and Writing skills.
b. Equip students to study academic subjects more effectively and critically using the theoretical and practical components of English syllabus.
c. Develop study skills and communication skills in formal and informal situations.

Course Outcomes: Students should be able to

- Use English Language effectively in spoken and written forms.
- Comprehend the given texts and respond appropriately.
- Communicate confidently in various contexts and different cultures.
- Acquire basic proficiency in English including reading and listening comprehension, writing and speaking skills.

SYLLABUS

UNIT –I

'The Raman Effect' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.

Vocabulary Building: The Concept of Word Formation --The Use of Prefixes and Suffixes.

Grammar: Identifying Common Errors in Writing with Reference to Articles and Prepositions.

Reading: Reading and Its Importance- Techniques for Effective Reading.

Basic Writing Skills: Sentence Structures -Use of Phrases and Clauses in Sentences- Importance of Proper Punctuation- Techniques for writing precisely – **Paragraph writing** – Types, Structures and Features of a Paragraph - Creating Coherence-Organizing Principles of Paragraphs in Documents.

UNIT –II

'Ancient Architecture in India' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.

Vocabulary: Synonyms and Antonyms.

Grammar: Identifying Common Errors in Writing with Reference to Noun-pronoun Agreement and Subject-verb Agreement.

Reading: Improving Comprehension Skills – Techniques for Good Comprehension

Writing: Format of a Formal Letter-**Writing Formal Letters** E.g.., Letter of Complaint, Letter of Requisition, Job Application with Resume.

UNIT –III

'Blue Jeans' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.

Vocabulary: Acquaintance with Prefixes and Suffixes from Foreign Languages in English to form Derivatives-Words from Foreign Languages and their Use in English.

Grammar: Identifying Common Errors in Writing with Reference to Misplaced Modifiers and Tenses.
Reading: Sub-skills of Reading- Skimming and Scanning
Writing: Nature and Style of Sensible Writing- **Defining**- **Describing** Objects, Places and Events – **Classifying**- Providing Examples or Evidence

UNIT –IV
'What Should You Be Eating' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.
Vocabulary: Standard Abbreviations in English
Grammar: Redundancies and Clichés in Oral and Written Communication.
Reading: Comprehension- Intensive Reading and Extensive Reading
Writing: Writing Practices--Writing Introduction and Conclusion - Essay Writing-Précis Writing.

UNIT –V
'How a Chinese Billionaire Built Her Fortune' from the prescribed textbook 'English for Engineers' published by Cambridge University Press.
Vocabulary: Technical Vocabulary and their usage
Grammar: Common Errors in English
Reading: Reading Comprehension-Exercises for Practice
Writing: **Technical Reports**- Introduction – Characteristics of a Report – Categories of Reports
Formats- Structure of Reports (Manuscript Format) -Types of Reports - Writing aReport.

TEXT BOOK:
1. Sudarshana, N.P. and Savitha, C. (2018). English for Engineers. Cambridge University Press.

REFERENCE BOOKS:
1. Swan, M. (2016). Practical English Usage. Oxford University Press.
2. Kumar, S and Lata, P. (2018). Communication Skills. Oxford University Press.
3. Wood, F.T. (2007). Remedial English Grammar. Macmillan.
4. Zinsser, William. (2001). On Writing Well. Harper Resource Book.
5. Hamp-Lyons, L. (2006). Study Writing. Cambridge University Press.
6. Exercises in Spoken English. Parts I –III. CIEFL, Hyderabad. Oxford University Press.

EN508HS: ADVANCED COMMUNICATION SKILLS LAB

B.Tech. III Year I Semester

L	T	P	C
0	0	2	1

1. INTRODUCTION:

The introduction of the Advanced Communication Skills Lab is considered essential at 3rd year level. At this stage, the students need to prepare themselves for their careers which may require them to listen to, read, speak and write in English both for their professional and interpersonal communication in the globalized context.

The proposed course should be a laboratory course to enable students to use 'good' English and perform the following:

- Gathering ideas and information to organize ideas relevantly and coherently.
- Engaging in debates.
- Participating in group discussions.
- Facing interviews.
- Writing project/research reports/technical reports.
- Making oral presentations.
- Writing formal letters.
- Transferring information from non-verbal to verbal texts and vice-versa.
- Taking part in social and professional communication.

2. OBJECTIVES:

This Lab focuses on using multi-media instruction for language development to meet the following targets:

- To improve the students' fluency in English, through a well-developed vocabulary and enable them to listen to English spoken at normal conversational speed by educated English speakers and respond appropriately in different socio-cultural and professional contexts.
- Further, they would be required to communicate their ideas relevantly and coherently in writing.
- To prepare all the students for their placements.

3. SYLLABUS:

The following course content to conduct the activities is prescribed for the Advanced English Communication Skills (AECS) Lab:

1. **Activities on Fundamentals of Inter-personal Communication and Building Vocabulary** - Starting a conversation – responding appropriately and relevantly – using the right body language – Role Play in different situations & Discourse Skills- using visuals - Synonyms and antonyms, word roots, one-word substitutes, prefixes and suffixes, study of word origin, business vocabulary, analogy, idioms and phrases, collocations & usage of vocabulary.
2. **Activities on Reading Comprehension** –General Vs Local comprehension, reading for facts, guessing meanings from context, scanning, skimming, inferring meaning, critical reading& effective googling.
3. **Activities on Writing Skills** – Structure and presentation of different types of writing – *letter writing/Resume writing/ e-correspondence/Technical report writing/* – planning for writing – improving one's writing.
4. **Activities on Presentation Skills** – Oral presentations (individual and group) through JAM sessions/seminars/**PPTs** and written presentations through posters/projects/reports/ e-mails/assignments etc.
5. **Activities on Group Discussion and Interview Skills** – Dynamics of group discussion, intervention, summarizing, modulation of voice, body language, relevance, fluency and organization of ideas and rubrics for evaluation- Concept and process, pre-interview planning, opening strategies, answering strategies, interview through tele-conference & video-conference and Mock Interviews.

4. MINIMUM REQUIREMENT:

The Advanced English Communication Skills (AECS) Laboratory shall have the following infrastructural facilities to accommodate at least 35 students in the lab:

- Spacious room with appropriate acoustics.
- Round Tables with movable chairs

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- Audio-visual aids
- LCD Projector
- Public Address system
- P – IV Processor, Hard Disk – 80 GB, RAM–512 MB Minimum, Speed – 2.8 GHZ
- T. V, a digital stereo & Camcorder
- Headphones of High quality

5. SUGGESTED SOFTWARE:

The software consisting of the prescribed topics elaborated above should be procured and used.

- Oxford Advanced Learner's Compass, 7th Edition
- DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice.
- Lingua TOEFL CBT Insider, by Dream tech
- TOEFL & GRE (KAPLAN, AARCO & BARRONS, USA, Cracking GRE by CLIFFS)

TEXT BOOKS:

1. Effective Technical Communication by M Asharaf Rizvi. McGraw Hill Education (India) Pvt. Ltd. 2nd Edition
2. Academic Writing: A Handbook for International Students by Stephen Bailey, Routledge, 5th Edition.

REFERENCES:

1. Learn Correct English – A Book of Grammar, Usage and Composition by Shiv K. Kumar and Hemalatha Nagarajan. Pearson 2007
2. Professional Communication by Aruna Koneru, McGraw Hill Education (India) Pvt. Ltd, 2016.
3. Technical Communication by Meenakshi Raman & Sangeeta Sharma, Oxford University Press 2009.
4. Technical Communication by Paul V. Anderson. 2007. Cengage Learning pvt. Ltd. New Delhi.
5. English Vocabulary in Use series, Cambridge University Press 2008.
6. Handbook for Technical Communication by David A. McMurrey & Joanne Buckley. 2012. Cengage Learning.
7. Communication Skills by Leena Sen, PHI Learning Pvt Ltd., New Delhi, 2009.
8. Job Hunting by Colm Downes, Cambridge University Press 2008.
9. English for Technical Communication for Engineering Students, Aysha Vishwamohan, Tata Mc Graw-Hill 2009.

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II YEAR I SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	EC301PC	Electronic Devices and Circuits	3	1	0	4
2	EC302PC	Network Analysis and Transmission Lines	3	0	0	3
3	EC303PC	Digital System Design	3	1	0	4
4	EC304PC	Signals and Systems	3	1	0	4
5	EC305ES	Probability Theory and Stochastic Processes	3	0	0	3
6	EC306PC	Electronic Devices and Circuits Lab	0	0	2	1
7	EC307PC	Digital System Design Lab	0	0	2	1
8	EC308ES	Basic Simulation Lab	0	0	2	1
9	*MC309	Constitution of India	3	0	0	0
		Total Credits	**18**	**3**	**6**	**21**

II YEAR II SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA401BS	Laplace Transforms, Numerical Methods & Complex Variables	3	1	0	4
2	EC402PC	Electromagnetic Fields and Waves	3	0	0	3
3	EC403PC	Analog and Digital Communications	3	1	0	4
4	EC404PC	Linear IC Applications	3	0	0	3
5	EC405PC	Electronic Circuit Analysis	3	0	0	3
6	EC406PC	Analog and Digital Communications Lab	0	0	3	1.5
7	EC407PC	IC Applications Lab	0	0	3	1.5
8	EC408PC	Electronic Circuit Analysis Lab	0	0	2	1
9	*MC409	Gender Sensitization Lab	0	0	2	0
		Total Credits	**15**	**2**	**10**	**21**

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EC301PC: ELECTRONIC DEVICES AND CIRCUITS

B.Tech. II Year I Sem.

L	T	P	C
3	1	0	4

Course Objectives:

- To introduce components such as diodes, BJTs and FETs.
- To know the applications of components.
- To know the switching characteristics of components
- To give understanding of various types of amplifier circuits

Course Outcomes: Upon completion of the Course, the students will be able to:

- Know the characteristics of various components.
- Understand the utilization of components.
- Understand the biasing techniques
- Design and analyze small signal amplifier circuits.

UNIT - I

Diode and Applications: Diode - Static and Dynamic resistances, Equivalent circuit, Load line analysis, Diffusion and Transition Capacitances, Diode Applications: Switch-Switching times.
Rectifier - Half Wave Rectifier, Full Wave Rectifier, Bridge Rectifier, Rectifiers with Capacitive and Inductive Filters, Clippers-Clipping at two independent levels, Clamper-Clamping Circuit Theorem, Clamping Operation, Types of Clampers.

UNIT - II

Bipolar Junction Transistor (BJT): Principle of Operation, Common Emitter, Common Base and Common Collector Configurations, Transistor as a switch, switching times, Transistor Biasing and Stabilization - Operating point, DC & AC load lines, Biasing - Fixed Bias, Self Bias, Bias Stability, Bias Compensation using Diodes.

UNIT - III

Junction Field Effect Transistor (FET): Construction, Principle of Operation, Pinch-Off Voltage, Volt-Ampere Characteristic, Comparison of BJT and FET, Biasing of FET, FET as Voltage Variable Resistor.
Special Purpose Devices: Zener Diode - Characteristics, Voltage Regulator. Principle of Operation - SCR, Tunnel diode, UJT, Varactor Diode.

UNIT – IV

Analysis and Design of Small Signal Low Frequency BJT Amplifiers: Transistor Hybrid model, Determination of h-parameters from transistor characteristics, Typical values of h- parameters in CE, CB and CC configurations, Transistor amplifying action, Analysis of CE, CC, CB Amplifiers and CE Amplifier with emitter resistance, low frequency response of BJT Amplifiers, effect of coupling and bypass capacitors on CE Amplifier.

UNIT – V

FET Amplifiers: Small Signal Model, Analysis of JFET Amplifiers, Analysis of CS, CD, CG JFET Amplifiers. MOSFET Characteristics in Enhancement and Depletion mode, Basic Concepts of MOS Amplifiers.

TEXT BOOKS:

1. Electronic Devices and Circuits- Jacob Millman, McGraw Hill Education
2. Electronic Devices and Circuits theory– Robert L. Boylestead, Louis Nashelsky, 11th Edition, 2009, Pearson.

REFERENCE BOOKS:

1. The Art of Electronics, Horowitz, 3rd Edition Cambridge University Press
2. Electronic Devices and Circuits, David A. Bell – 5th Edition, Oxford.
3. Pulse, Digital and Switching Waveforms –J. Millman, H. Taub and Mothiki S. Prakash Rao, 2Ed., 2008, Mc Graw Hill.

EC306PC: ELECTRONIC DEVICES AND CIRCUITS LAB

B.Tech. II Year I Sem.

L	T	P	C
0	0	2	1

List of Experiments (Twelve experiments to be done):

Verify any twelve experiments in H/W Laboratory

1. PN Junction diode characteristics A) Forward bias B) Reverse bias.
2. Zener diode characteristics and Zener as voltage Regulator
3. Full Wave Rectifier with & without filters
4. Input and output characteristics of BJT in CE Configuration
5. Input and output characteristics of FE in CS Configuration
6. Common Emitter Amplifier Characteristics
7. Common Base Amplifier Characteristics
8. Common Source amplifier Characteristics
9. Measurement of h-parameters of transistor in CB, CE, CC configurations
10. Switching characteristics of a transistor
11. SCR Characteristics.
12. Types of Clippers at different reference voltages
13. Types of Clampers at different reference voltages
14. The steady state output waveform of clampers for a square wave input

Major Equipment required for Laboratories:

1. Regulated Power Suppliers, 0-30V
2. 20 MHz, Dual Channel Cathode Ray Oscilloscopes.
3. Functions Generators-Sine and Square wave signals
4. Multimeters
5. Electronic Components

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EC303PC: DIGITAL SYSTEM DESIGN

B.Tech. II Year I Sem.

L	T	P	C
3	1	0	4

Pre-Requisites: Nil

Course Objectives:
- To understand common forms of number representation in logic circuits
- To learn basic techniques for the design of digital circuits and fundamental concepts used in the design of digital systems.
- To understand the concepts of combinational logic circuits and sequential circuits.
- To understand the Realization of Logic Gates Using Diodes & Transistors.

Course Outcomes: Upon completing this course, the student will be able to
- Understand the numerical information in different forms and Boolean Algebra theorems
- Postulates of Boolean algebra and to minimize combinational functions
- Design and analyze combinational and sequential circuits
- Known about the logic families and realization of logic gates.

UNIT - I:
Number Systems: Number systems, Complements of Numbers, Codes- Weighted and Non-weighted codes and its Properties, Parity check code and Hamming code.
Boolean Algebra: Basic Theorems and Properties, Switching Functions- Canonical and Standard Form, Algebraic Simplification, Digital Logic Gates, EX-OR gates, Universal Gates, Multilevel NAND/NOR realizations.

UNIT - II:
Minimization of Boolean functions: Karnaugh Map Method - Up to five Variables, Don't Care Map Entries, Tabular Method,
Combinational Logic Circuits: Adders, Subtractors, Comparators, Multiplexers, Demultiplexers, Encoders, Decoders and Code converters, Hazards and Hazard Free Relations.

UNIT - III
Sequential Circuits Fundamentals: Basic Architectural Distinctions between Combinational and Sequential circuits, SR Latch, Flip Flops: SR, JK, JK Master Slave, D and T Type Flip Flops, Excitation Table of all Flip Flops, Timing and Triggering Consideration, Conversion from one type of Flip-Flop to another.
Registers and Counters: Shift Registers – Left, Right and Bidirectional Shift Registers, Applications of Shift Registers - Design and Operation of Ring and Twisted Ring Counter, Operation of Asynchronous and Synchronous Counters.

UNIT - IV
Sequential Machines: Finite State Machines, Synthesis of Synchronous Sequential Circuits- Serial Binary Adder, Sequence Detector, Parity-bit Generator, Synchronous Modulo N –Counters. Finite state machine-capabilities and limitations, Mealy and Moore models.

UNIT - V
Realization of Logic Gates Using Diodes & Transistors: AND, OR and NOT Gates using Diodes and Transistors, DCTL, RTL, DTL, TTL, CML and CMOS Logic Families and its Comparison, Classification of Integrated circuits, comparison of various logic families, standard TTL NAND Gate-Analysis & characteristics, TTL open collector O/Ps, Tristate TTL, MOS & CMOS open drain and tri-state outputs, CMOS transmission gate, IC interfacing- TTL driving CMOS & CMOS driving TTL.

TEXT BOOKS:
1. Switching and Finite Automata Theory - Zvi Kohavi & Niraj K. Jha, 3rd Edition, Cambridge, 2010.
2. Modern Digital Electronics – R. P. Jain, 3rd Edition, 2007- Tata McGraw-Hill

REFERENCE BOOKS:
1. Digital Design- Morris Mano, PHI, 4th Edition,2006

2. Introduction to Switching Theory and Logic Design – Fredriac J. Hill, Gerald R. Peterson, 3rd Ed, John Wiley & Sons Inc.
3. Fundamentals of Logic Design- Charles H. Roth, Cengage Learning, 5th, Edition, 2004.
4. Switching Theory and Logic Design – A Anand Kumar, PHI, 2013

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EC304PC: SIGNALS AND SYSTEMS

B.Tech. II Year I Sem.

L	T	P	C
3	1	0	4

Pre-requisite: Nil

Course Objectives:
- This gives the basics of Signals and Systems required for all Electrical Engineering related courses.
- To understand the behavior of signal in time and frequency domain
- To understand the characteristics of LTI systems
- This gives concepts of Signals and Systems and its analysis using different transform techniques.

Course Outcomes: Upon completing this course, the student will be able to
- Differentiate various signal functions.
- Represent any arbitrary signal in time and frequency domain.
- Understand the characteristics of linear time invariant systems.
- Analyze the signals with different transform technique

UNIT - I
Signal Analysis: Analogy between Vectors and Signals, Orthogonal Signal Space, Signal approximation using Orthogonal functions, Mean Square Error, Closed or complete set of Orthogonal functions, Orthogonality in Complex functions, Classification of Signals and systems, Exponential and Sinusoidal signals, Concepts of Impulse function, Unit Step function, Signum function.

UNIT – II
Fourier series: Representation of Fourier series, Continuous time periodic signals, Properties of Fourier Series, Dirichlet's conditions, Trigonometric Fourier Series and Exponential Fourier Series, Complex Fourier spectrum.
Fourier Transforms: Deriving Fourier Transform from Fourier series, Fourier Transform of arbitrary signal, Fourier Transform of standard signals, Fourier Transform of Periodic Signals, Properties of Fourier Transform, Fourier Transforms involving Impulse function and Signum function, Introduction to Hilbert Transform.

UNIT - III
Signal Transmission through Linear Systems: Linear System, Impulse response, Response of a Linear System, Linear Time Invariant(LTI) System, Linear Time Variant (LTV) System, Transfer function of a LTI System, Filter characteristic of Linear System, Distortion less transmission through a system, Signal bandwidth, System Bandwidth, Ideal LPF, HPF, and BPF characteristics, Causality and Paley-Wiener criterion for physical realization, Relationship between Bandwidth and rise time, Convolution and Correlation of Signals, Concept of convolution in Time domain and Frequency domain, Graphical representation of Convolution.

UNIT – IV
Laplace Transforms: Laplace Transforms (L.T), Inverse Laplace Transform, Concept of Region of Convergence (ROC) for Laplace Transforms, Properties of L.T, Relation between L.T and F.T of a signal, Laplace Transform of certain signals using waveform synthesis.
Z–Transforms: Concept of Z- Transform of a Discrete Sequence, Distinction between Laplace, Fourier and Z Transforms, Region of Convergence in Z-Transform, Constraints on ROC for various classes of signals, Inverse Z-transform, Properties of Z-transforms.

UNIT - V
Sampling theorem: Graphical and analytical proof for Band Limited Signals, Impulse Sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, Effect of under sampling – Aliasing, Introduction to Band Pass Sampling.
Correlation: Cross Correlation and Auto Correlation of Functions, Properties of Correlation Functions, Energy Density Spectrum, Parsevals Theorem, Power Density Spectrum, Relation between Autocorrelation Function and Energy/Power Spectral Density Function, Relation between Convolution

and Correlation, Detection of Periodic Signals in the presence of Noise by Correlation, Extraction of Signal from Noise by Filtering.

TEXT BOOKS:

1. Signals, Systems & Communications - B.P. Lathi, 2013, BSP.
2. Signals and Systems - A.V. Oppenheim, A.S. Willsky and S.H. Nawabi, 2 Ed.

REFERENCE BOOKS:

1. Signals and Systems – Simon Haykin and Van Veen, Wiley 2 Ed.,
2. Signals and Systems – A. Rama Krishna Rao, 2008, TMH
3. Fundamentals of Signals and Systems - Michel J. Robert, 2008, MGH International Edition.
4. Signals, Systems and Transforms - C. L. Philips, J.M.Parr and Eve A.Riskin, 3 Ed., 2004, PE.
5. Signals and Systems – K. Deergha Rao, Birkhauser, 2018.

EC308ES: BASIC SIMULATION LAB

B.Tech. II Year I Sem.

L	T	P	C
0	0	2	1

Note:
- All the experiments are to be simulated using MATLAB or equivalent software
- Minimum of 15 experiment are to be completed

List of Experiments:
1. Basic Operations on Matrices.
2. Generation of Various Signals and Sequences (Periodic and Aperiodic), such as Unit Impulse, Unit Step, Square, Saw tooth, Triangular, Sinusoidal, Ramp, Sinc.
3. Operations on Signals and Sequences such as Addition, Multiplication, Scaling, Shifting, Folding, Computation of Energy and Average Power.
4. Finding the Even and Odd parts of Signal/Sequence and Real and Imaginary parts of Signal.
5. Convolution for Signals and sequences.
6. Auto Correlation and Cross Correlation for Signals and Sequences.
7. Verification of Linearity and Time Invariance Properties of a given Continuous/Discrete System.
8. Computation of Unit sample, Unit step and Sinusoidal responses of the given LTI system and verifying its physical realiazability and stability properties.
9. Gibbs Phenomenon Simulation.
10. Finding the Fourier Transform of a given signal and plotting its magnitude and phase spectrum.
11. Waveform Synthesis using Laplace Transform.
12. Locating the Zeros and Poles and plotting the Pole-Zero maps in S-plane and Z-Plane for the given transfer function.
13. Generation of Gaussian noise (Real and Complex), Computation of its mean, M.S. Value and its Skew, Kurtosis, and PSD, Probability Distribution Function.
14. Verification of Sampling Theorem.
15. Removal of noise by Autocorrelation / Cross correlation.
16. Extraction of Periodic Signal masked by noise using Correlation.
17. Verification of Weiner-Khinchine Relations.
18. Checking a Random Process for Stationarity in Wide sense.

Major Equipments required for Laboratories:
1. Computer System with latest specifications connected
2. Window Xp or equivalent
3. Simulation software-MAT Lab or any equivalent simulation software

EC403PC: ANALOG AND DIGITAL COMMUNICATIONS

B.Tech. II Year II Semester

L	T	P	C
3	1	0	4

Prerequisite: Probability theory and Stochastic Processes

Course Objectives:
- To develop ability to analyze system requirements of analog and digital communication systems.
- To understand the generation, detection of various analog and digital modulation techniques.
- To acquire theoretical knowledge of each block in AM, FM transmitters and receivers.
- To understand the concepts of baseband transmissions.

Course Outcomes: Upon completing this course, the student will be able to
- Analyze and design of various continuous wave and angle modulation and demodulation techniques
- Understand the effect of noise present in continuous wave and angle modulation techniques.
- Attain the knowledge about AM , FM Transmitters and Receivers
- Analyze and design the various Pulse Modulation Techniques.
- Understand the concepts of Digital Modulation Techniques and Baseband transmission.

UNIT - I
Amplitude Modulation: Need for modulation, Amplitude Modulation - Time and frequency domain description, single tone modulation, power relations in AM waves, Generation of AM waves - Switching modulator, Detection of AM Waves - Envelope detector, DSBSC modulation - time and frequency domain description, Generation of DSBSC Waves - Balanced Modulators, Coherent detection of DSB-SC Modulated waves, COSTAS Loop, SSB modulation - time and frequency domain description, frequency discrimination and Phase discrimination methods for generating SSB, Demodulation of SSB Waves, principle of Vestigial side band modulation.

UNIT - II
Angle Modulation: Basic concepts of Phase Modulation, Frequency Modulation: Single tone frequency modulation, Spectrum Analysis of Sinusoidal FM Wave using Bessel functions, Narrow band FM, Wide band FM, Constant Average Power, Transmission bandwidth of FM Wave - Generation of FM Signal- Armstrong Method, Detection of FM Signal: Balanced slope detector, Phase locked loop, Comparison of FM and AM., Concept of Pre-emphasis and de-emphasis.

UNIT - III
Transmitters: Classification of Transmitters, AM Transmitters, FM Transmitters
Receivers: Radio Receiver - Receiver Types - Tuned radio frequency receiver, Superhetrodyne receiver, RF section and Characteristics - Frequency changing and tracking, Intermediate frequency, Image frequency, AGC, Amplitude limiting, FM Receiver, Comparison of AM and FM Receivers.

UNIT - IV
Pulse Modulation: Types of Pulse modulation- PAM, PWM and PPM. Comparison of FDM and TDM.
Pulse Code Modulation: PCM Generation and Reconstruction, Quantization Noise, Non-Uniform Quantization and Companding, DPCM, Adaptive DPCM, DM and Adaptive DM, Noise in PCM and DM.

UNIT - V
Digital Modulation Techniques: ASK- Modulator, Coherent ASK Detector, FSK- Modulator, Non-Coherent FSK Detector, BPSK- Modulator, Coherent BPSK Detection. Principles of QPSK, Differential PSK and QAM.
Baseband Transmission and Optimal Reception of Digital Signal: A Baseband Signal Receiver, Probability of Error, Optimum Receiver, Coherent Reception, ISI, Eye Diagrams.

TEXT BOOKS:
1. Analog and Digital Communications – Simon Haykin, John Wiley, 2005.
2. Electronics Communication Systems-Fundamentals through Advanced-Wayne Tomasi, 5th Edition, 2009, PHI.

REFERENCE BOOKS:

1. Principles of Communication Systems - Herbert Taub, Donald L Schilling, Goutam Saha, 3rd Edition, McGraw-Hill, 2008.
2. Electronic Communications – Dennis Roddy and John Coolean , 4th Edition , PEA, 2004
3. Electronics & Communication System – George Kennedy and Bernard Davis, TMH 2004
4. Analog and Digital Communication – K. Sam Shanmugam, Willey ,2005

EC406PC: ANALOG AND DIGITAL COMMUNICATIONS LAB

B.Tech. II Year II Sem.

L	T	P	C
0	0	3	1.5

Note:

- Minimum 12 experiments should be conducted:
- All these experiments are to be simulated first either using MATLAB, COMSIM or any other simulation package and then to be realized in hardware

List of Experiments:

1. (i) Amplitude modulation and demodulation (ii) Spectrum analysis of AM
2. (i) Frequency modulation and demodulation (ii) Spectrum analysis of FM
3. DSB-SC Modulator & Detector
4. SSB-SC Modulator & Detector (Phase Shift Method)
5. Frequency Division Multiplexing & De multiplexing
6. Pulse Amplitude Modulation & Demodulation
7. Pulse Width Modulation & Demodulation
8. Pulse Position Modulation & Demodulation
9. PCM Generation and Detection
10. Delta Modulation
11. Frequency Shift Keying: Generation and Detection
12. Binary Phase Shift Keying: Generation and Detection
13. Generation and Detection (i) DPSK (ii) QPSK

Major Equipments required for Laboratories:

1. CROs: 20MHz
2. Function Generators: 2MHz
3. Spectrum Analyzer
4. Regulated Power Supplies: 0-30V
5. MAT Lab/Equivalent Simulation Package with Communication tool box
6. Analog and Digital Modulation and Demodulation Trainer Kits.

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EC404PC: LINEAR IC APPLICATIONS

B.Tech. II Year II Sem.

L	T	P	C
3	0	0	3

Pre-requisite: Electronic Devices & Circuits

Course Objectives: The main objectives of the course are:
- To introduce the basic building blocks of linear integrated circuits.
- To introduce the theory and applications of analog multipliers and PLL.
- To introduce the concepts of waveform generation and introduce some special function ICs.

Course Outcomes: Upon completing this course, the student will be able to
- A thorough understanding of operational amplifiers with linear integrated circuits.
- Attain the knowledge of functional diagrams and applications of IC 555 and IC 565
- Acquire the knowledge about the Data converters.

UNIT - I

Integrated Circuits: Classification, chip size and circuit complexity, basic information of Op-amp, ideal and practical Op-amp, internal circuits, Op-amp characteristics, DC and AC Characteristics, 741 op-amp and its features, modes of operation-inverting, non-inverting, differential.

UNIT - II

Op-amp and Applications: Basic information of Op-amp, instrumentation amplifier, ac amplifier, V to I and I to V converters, Sample & hold circuits, multipliers and dividers, differentiators and integrators, comparators, Schmitt trigger, Multivibrators, introduction to voltage regulators, features of 723

UNIT - III

Active Filters & Oscillators: Introduction, 1st order LPF, HPF filters, Band pass, Band reject and all pass filters. Oscillator types and principle of operation - RC, Wien and quadrature type, waveform generators - triangular, sawtooth, square wave and VCO.

UNIT - IV

Timers & Phase Locked Loops: Introduction to 555 timer, functional diagram, monostable and astable operations and applications, Schmitt Trigger. PLL - introduction, block schematic, principles and description of individual blocks of 565.

UNIT - V

D-A and A-D Converters: Introduction, basic DAC techniques, weighted resistor DAC, R-2R ladder DAC, inverted R-2R DAC, and IC 1408 DAC, Different types of ADCs - parallel comparator type ADC, counter type ADC, successive approximation ADC dual slope integration type ADC, DAC and ADC specifications.

TEXT BOOKS:

1. Linear Integrated Circuits, D. Roy Chowdhury, New Age International(p) Ltd.
2. Op-Amps & Linear ICs, Ramakanth A. Gayakwad, PHI

REFERENCES BOOKS:

1. Operational Amplifiers & Linear Integrated Circuits, R.F. Coughlin & Fredrick F. Driscoll, PHI.
2. Operational Amplifiers & Linear Integrated Circuits: Theory & Applications, Denton J. Daibey, TMH.
3. Design with Operational Amplifiers & Analog Integrated Circuits, Sergio Franco, McGraw Hill.
4. Digital Fundamentals - Floyd and Jain, Pearson Education.

EC407PC: IC APPLICATIONS LAB

B.Tech. II Year II Semester

L	T	P	C
0	0	3	1.5

Note: Verify the functionality of the IC in the given application

Design and Implementation of:

1. Inverting and Non-Inverting Amplifiers using Op Amps
2. Adder and Subtractor using Op Amp.
3. Comparators using Op Amp.
4. Integrator Circuit using IC 741.
5. Differentiator Circuit using Op Amp.
6. Active filter Applications-LPF, HPF (First Order)
7. IC 741 waveform Generators-Sine, Square wave and Triangular Waves.
8. Mono-Stable Multivibrator using IC 555.
9. Astable multivibrator using IC 555.
10. Schmitt Trigger Circuits using IC 741.
11. IC 565-PLL Applications.
12. Voltage Regulator using IC 723
13. Three terminal voltage regulators-7805, 7809, 7912

Major Equipments required for Laboratories:

1. 5 V Fixed Regulated Power Supply/ 0-5V or more Regulated Power Supply.
2. 20 MHz Oscilloscope with Dual Channel.
3. Bread board and components/ Trainer Kit.
4. Multimeter.

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EC405PC: ELECTRONIC CIRCUIT ANALYSIS

B.Tech. II Year II Sem.

L	T	P	C
3	0	0	3

Pre-requisite: Electronic Devices and Circuits

Course Objectives:
- Learn the concepts of high frequency analysis of transistors.
- To give understanding of various types of amplifier circuits such as small signal, cascaded, large signal and tuned amplifiers.
- To familiarize the Concept of feedback in amplifiers so as to differentiate between negative and positive feedback
- To construct various multivibrators using transistors and sweep circuits.

Course Outcomes: Upon completing this course, the student will be able to
- Design the multistage amplifiers and understand the concepts of High Frequency Analysis of Transistors.
- Utilize the Concepts of negative feedback to improve the stability of amplifiers and positive feedback to generate sustained oscillations
- Design and realize different classes of Power Amplifiers and tuned amplifiers useable for audio and Radio applications.
- Design Multivibrators and sweep circuits for various applications.

UNIT – I

Multistage Amplifiers: Classification of Amplifiers, Distortion in amplifiers, Different coupling schemes used in amplifiers, Frequency response and Analysis of multistage amplifiers, Casca RC Coupled amplifiers, Cascode amplifier, Darlington pair.

Transistor at High Frequency: Hybrid -πmodel of Common Emitter transistor model, f_α, f_β and unity gain bandwidth, Gain-bandwidth product.

UNIT II

Feedback Amplifiers: Concepts of feedback – Classification of feedback amplifiers – General characteristics of Negative feedback amplifiers – Effect of Feedback on Amplifier characteristics – Voltage series, Voltage shunt, Current series and Current shunt Feedback configurations – Simple problems.

UNIT -III

Oscillators: Condition for Oscillations, RC type Oscillators-RC phase shift and Wien-bridge Oscillators, LC type Oscillators –Generalized analysis of LC Oscillators, Hartley and Colpitts Oscillators, Frequency and amplitude stability of Oscillators, Crystal Oscillator.

UNIT -IV

Large Signal Amplifiers: Class A Power Amplifier- Series fed and Transformer coupled, Conversion Efficiency, Class B Power Amplifier- Push Pull and Complimentary Symmetry configurations, Conversion Efficiency, Principle of operation of Class AB and Class –C Amplifiers.

Tuned Amplifiers: Introduction, single Tuned Amplifiers – Q-factor, frequency response of tuned amplifiers, Concept of stagger tuning and synchronous tuning.

UNIT –V

Multivibrators: Analysis and Design of Bistable, Monostable, Astable Multivibrators and Schmitt trigger using Transistors.

Time Base Generators: General features of a Time base Signal, Methods of Generating Time Base Waveform, concepts of Transistor Miller and Bootstrap Time Base Generator, Methods of Linearity improvement.

TEXT BOOKS:

1. Integrated Electronics, Jacob Millman, Christos C Halkias, McGraw Hill Education.
2. Electronic Devices Conventional and current version -Thomas L. Floyd 2015, Pearson.

REFERENCE BOOKS:

1. Electronic Devices and Circuits, David A. Bell – 5th Edition, Oxford.
2. Electronic Devices and Circuits theory– Robert L. Boylestead, Louis Nashelsky, 11th Edition, 2009, Pearson

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EC408PC: ELECTRONIC CIRCUIT ANALYSIS LAB

B.Tech. II Year II Sem.

L	T	P	C
0	0	2	1

Note:

- Experiments marked with * has to be designed, simulated and verified in hardware.
- Minimum of 9 experiments to be done in hardware.

Hardware Testing in Laboratory:

1. Common Emitter Amplifier (*)
2. Two Stage RC Coupled Amplifier
3. Cascode amplifier Circuit (*)
4. Darlington Pair Circuit
5. Current Shunt Feedback amplifier Circuit
6. Voltage Series Feedback amplifier Circuit (*)
7. RC Phase shift Oscillator Circuit (*)
8. Hartley and Colpitt's Oscillators Circuit
9. Class A power amplifier
10. Class B Complementary symmetry amplifier (*)
11. Design a Monostable Multivibrator
12. The output voltage waveform of Miller Sweep Circuit

Major Equipments required for Laboratories:

1. Computer System with latest specifications connected
2. Window XP or equivalent
3. Simulation software-Multisim or any equivalent simulation software
4. Regulated Power Suppliers, 0-30V
5. 20 MHz, Dual Channel Cathode Ray Oscilloscopes.
6. Functions Generators-Sine and Square wave signals
7. Multimeters
8. Electronic Components

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***MC409/*MC309: GENDER SENSITIZATION LAB**
(An Activity-based Course)

B.Tech. II Year II Sem.

L	T	P	C
0	0	2	0

COURSE DESCRIPTION

This course offers an introduction to Gender Studies, an interdisciplinary field that asks critical questions about the meanings of sex and gender in society. The primary goal of this course is to familiarize students with key issues, questions and debates in Gender Studies, both historical and contemporary. It draws on multiple disciplines – such as literature, history, economics, psychology, sociology, philosophy, political science, anthropology and media studies – to examine cultural assumptions about sex, gender, and sexuality.

This course integrates analysis of current events through student presentations, aiming to increase awareness of contemporary and historical experiences of women, and of the multiple ways that sex and gender interact with race, class, caste, nationality and other social identities. This course also seeks to build an understanding and initiate and strengthen programmes combating gender-based violence and discrimination. The course also features several exercises and reflective activities designed to examine the concepts of gender, gender-based violence, sexuality, and rights. It will further explore the impact of gender-based violence on education, health and development.

Objectives of the Course:
- To develop students' sensibility with regard to issues of gender in contemporary India.
- To provide a critical perspective on the socialization of men and women.
- To introduce students to information about some key biological aspects of genders.
- To expose the students to debates on the politics and economics of work.
- To help students reflect critically on gender violence.
- To expose students to more egalitarian interactions between men and women.

Learning Outcomes:
- Students will have developed a better understanding of important issues related to gender in contemporary India.
- Students will be sensitized to basic dimensions of the biological, sociological, psychological and legal aspects of gender. This will be achieved through discussion of materials derived from research, facts, everyday life, literature and film.
- Students will attain a finer grasp of how gender discrimination works in our society and how to counter it.
- Students will acquire insight into the gendered division of labour and its relation to politics and economics.
- Men and women students and professionals will be better equipped to work and live together as equals.
- Students will develop a sense of appreciation of women in all walks of life.
- Through providing accounts of studies and movements as well as the new laws that provide protection and relief to women, the textbook will empower students to understand and respond to gender violence.

UNIT - I: UNDERSTANDING GENDER

Introduction: Definition of Gender-Basic Gender Concepts and Terminology-Exploring Attitudes towards Gender-Construction of Gender-Socialization: Making Women, Making Men
- Preparing for Womanhood. Growing up Male. First lessons in Caste.

UNIT – II: GENDER ROLES AND RELATIONS

Two or Many? -Struggles with Discrimination-Gender Roles and Relations-Types of Gender Roles-Gender Roles and Relationships Matrix-Missing Women-Sex Selection and Its Consequences-Declining Sex Ratio. Demographic Consequences-Gender Spectrum: Beyond the Binary

UNIT – III: GENDER AND LABOUR

Division and Valuation of Labour-Housework: The Invisible Labor- "My Mother doesn't Work." "Share the Load."-Work: Its Politics and Economics -Fact and Fiction. Unrecognized and Unaccounted work. - Gender Development Issues-Gender, Governance and Sustainable Development-Gender and Human Rights-Gender and Mainstreaming

UNIT – IV: GENDER - BASED VIOLENCE

The Concept of Violence- Types of Gender-based Violence-Gender-based Violence from a Human Rights Perspective-Sexual Harassment: Say No! -Sexual Harassment, not Eve-teasing- Coping with Everyday Harassment- Further Reading: "*Chupulu*".

Domestic Violence: Speaking OutIs Home a Safe Place? -When Women Unite [Film]. Rebuilding Lives. Thinking about Sexual Violence Blaming the Victim-"I Fought for my Life...."

UNIT – V: GENDER AND CULTURE

Gender and Film-Gender and Electronic Media-Gender and Advertisement-Gender and Popular Literature- Gender Development Issues-Gender Issues-Gender Sensitive Language-Gender and Popular Literature - Just Relationships: Being Together as Equals

Mary Kom and Onler. Love and Acid just do not Mix. Love Letters. Mothers and Fathers. Rosa Parks-The Brave Heart.

Note: **Since it is Interdisciplinary Course, Resource Persons can be drawn from the fields of English Literature or Sociology or Political Science or any other qualified faculty who has expertise in this field from engineering departments.**

- ***Classes will consist of a combination of activities: dialogue-based lectures, discussions, collaborative learning activities, group work and in-class assignments. Apart from the above prescribed book, Teachers can make use of any authentic materials related to the topics given in the syllabus on "Gender".***

- **ESSENTIAL READING**: The Textbook, "*Towards a World of Equals: A Bilingual Textbook on Gender*" *written* by A.Suneetha, Uma Bhrugubanda, DuggiralaVasanta, Rama Melkote, Vasudha Nagaraj, Asma Rasheed, Gogu Shyamala, Deepa Sreenivas and Susie Tharu **published by Telugu Akademi, Telangana Government in 2015.**

ASSESSMENT AND GRADING:

- Discussion & Classroom Participation: 20%
- Project/Assignment: 30%
- End Term Exam: 50%

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III YEAR I SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	EC501PC	Microprocessors & Microcontrollers	3	1	0	4
2	EC502PC	Data Communications and Networks	3	1	0	4
3	EC503PC	Control Systems	3	1	0	4
4	SM504MS	Business Economics & Financial Analysis	3	0	0	3
5		Professional Elective - I	3	0	0	3
6	EC505PC	Microprocessors & Microcontrollers Lab	0	0	3	1.5
7	EC506PC	Data Communications and Networks Lab	0	0	3	1.5
8	EN508HS	Advanced Communication Skills Lab	0	0	2	1
9	*MC510	Intellectual Property Rights	3	0	0	0
		Total Credits	**18**	**3**	**8**	**22**

III YEAR II SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	EC601PC	Antennas and Propagation	3	1	0	4
2	EC602PC	Digital Signal Processing	3	1	0	4
3	EC603PC	VLSI Design	3	1	0	4
4		Professional Elective - II	3	0	0	3
5		Open Elective - I	3	0	0	3
6	EC604PC	Digital Signal Processing Lab	0	0	3	1.5
7	EC605PC	e – CAD Lab	0	0	3	1.5
8	EC606PC	Scripting Languages Lab	0	0	2	1
9	*MC609	Environmental Science	3	0	0	0
		Total Credits	**18**	**3**	**8**	**22**

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EC501PC: MICROPROCESSORS AND MICROCONTROLLERS

B.Tech. III Year I Semester

L	T	P	C
3	1	0	4

Prerequisite: Nil

Course Objectives:
1. To familiarize the architecture of microprocessors and micro controllers
2. To provide the knowledge about interfacing techniques of bus & memory.
3. To understand the concepts of ARM architecture
4. To study the basic concepts of Advanced ARM processors

Course Outcomes: Upon completing this course, the student will be able to
1. Understands the internal architecture, organization and assembly language programming of 8086 processors.
2. Understands the internal architecture, organization and assembly language programming of 8051/controllers
3. Understands the interfacing techniques to 8086 and 8051 based systems.
4. Understands the internal architecture of ARM processors and basic concepts of advanced ARM processors.

UNIT -I:
8086 Architecture: 8086 Architecture-Functional diagram, Register Organization, Memory Segmentation, Programming Model, Memory addresses, Physical Memory Organization, Architecture of 8086, Signal descriptions of 8086, interrupts of 8086.
Instruction Set and Assembly Language Programming of 8086: Instruction formats, Addressing modes, Instruction Set, Assembler Directives, Macros, and Simple Programs involving Logical, Branch and Call Instructions, Sorting, String Manipulations.

UNIT -II:
Introduction to Microcontrollers: Overview of 8051 Microcontroller, Architecture, I/O Ports, Memory Organization, Addressing Modes and Instruction set of 8051.
8051 Real Time Control: Programming Timer Interrupts, Programming External Hardware Interrupts, Programming the Serial Communication Interrupts, Programming 8051 Timers and Counters

UNIT –III:
I/O And Memory Interface: LCD, Keyboard, External Memory RAM, ROM Interface, ADC, DAC Interface to 8051.
Serial Communication and Bus Interface: Serial Communication Standards, Serial Data Transfer Scheme, On board Communication Interfaces-I2C Bus, SPI Bus, UART; External Communication Interfaces-RS232,USB.

UNIT –IV:
ARM Architecture: ARM Processor fundamentals, ARM Architecture – Register, CPSR, Pipeline, exceptions and interrupts interrupt vector table, ARM instruction set – Data processing, Branch instructions, load store instructions, Software interrupt instructions, Program status register instructions, loading constants, Conditional execution, Introduction to Thumb instructions.

UNIT – V:
Advanced ARM Processors: Introduction to CORTEX Processor and its architecture, OMAP Processor and its Architecture.

TEXT BOOKS:
1. Advanced Microprocessors and Peripherals – A. K. Ray and K. M. Bhurchandani, TMH, 2nd Edition 2006.
2. ARM System Developers guide, Andrew N SLOSS, Dominic SYMES, Chris WRIGHT, Elsevier, 2012

REFERENCE BOOKS:
1. The 8051 Microcontroller, Kenneth. J. Ayala, Cengage Learning, 3rd Ed, 2004.

2. Microprocessors and Interfacing, D. V. Hall, TMGH, 2nd Edition 2006.
3. The 8051 Microcontrollers, Architecture and Programming and Applications -K. Uma Rao, Andhe Pallavi, Pearson, 2009.
4. Digital Signal Processing and Applications with the OMAP- L138 Experimenter, Donald Reay, WILEY 2012.

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EC505PC: MICROPROCESSORS AND MICROCONTROLLERS LAB

B.Tech. III Year I Semester

L	T	P	C
0	0	3	1.5

Cycle 1: Using 8086 Processor Kits and/or Assembler (5 Weeks)

- Assembly Language Programs to 8086 to Perform
    1. Arithmetic, Logical, String Operations on 16 Bit and 32-Bit Data.
    2. Bit level Logical Operations, Rotate, Shift, Swap and Branch Operations.

Cycle 2: Using 8051 Microcontroller Kit (6 weeks)

- Introduction to IDE
    1. Assembly Language Programs to Perform Arithmetic (Both Signed and Unsigned) 16 Bit Data Operations, Logical Operations (Byte and Bit Level Operations), Rotate, Shift, Swap and Branch Instructions
    2. Time delay Generation Using Timers of 8051.
    3. Serial Communication from / to 8051 to / from I/O devices.
    4. Program Using Interrupts to Generate Square Wave 10 KHZ Frequency on P2.1 Using Timer 0 8051 in 8 bit Auto reload Mode and Connect a 1 HZ Pulse to INT1 pin and Display on Port 0. Assume Crystal Frequency as 11.0592 MHZ

Cycle 3: Interfacing I/O Devices to 8051(5 Weeks)

1. 7 Segment Display to 8051.
2. Matrix Keypad to 8051.
3. Sequence Generator Using Serial Interface in 8051.
4. 8 bit ADC Interface to 8051.
5. Triangular Wave Generator through DAC interfaces to 8051.

TEXT BOOKS:

1. Advanced Microprocessors and Peripherals by A K Ray, Tata McGraw-Hill Education, 2006
2. The 8051 *Microcontrollers*: Architecture, Programming & Applications by Dr. K. Uma Rao, Andhe Pallavi, Pearson, 2009.

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EC502PC: DATA COMMUNICATIONS AND NETWORKS

B.Tech. III Year I Semester

L	T	P	C
3	1	0	4

Pre-requisite: Digital Communications

Course Objectives:
1. To introduce the Fundamentals of data communication networks
2. To demonstrate the Functions of various protocols of Data link layer.
3. To demonstrate Functioning of various Routing protocols.
4. To introduce the Functions of various Transport layer protocols.
5. To understand the significance of application layer protocols

Course Outcomes: Upon completing this course, the student will be able to
1. Know the Categories and functions of various Data communication Networks
2. Design and analyze various error detection techniques.
3. Demonstrate the mechanism of routing the data in network layer
4. Know the significance of various Flow control and Congestion control Mechanisms
5. Know the Functioning of various Application layer Protocols.

UNIT - I:
Introduction to Data Communications: Components, Data Representation, Data Flow, Networks-Distributed Processing, Network Criteria, Physical Structures, Network Models, Categories of Networks Interconnection of Networks, The Internet - A Brief History, The Internet Today, Protocol and Standards - Protocols, Standards, Standards Organizations, Internet Standards. Network Models, Layered Tasks, OSI model, Layers in OSI model, TCP/IP Protocol Suite, Addressing Introduction, Wireless Links and Network Characteristics, WiFi: 802.11 Wireless LANs -The 802.11 Architecture,

UNIT - II:
Data Link Layer: Links, Access Networks, and LANs- Introduction to the Link Layer, The Services Provided by the Link Layer, Types of errors, Redundancy, Detection vs Correction, Forward error correction Versus Retransmission Error-Detection and Correction Techniques, Parity Checks, Check summing Methods, Cyclic Redundancy Check (CRC) , Framing, Flow Control and Error Control protocols , Noisy less Channels and Noisy Channels, HDLC, Multiple Access Protocols, Random Access ,ALOHA, Controlled access, Channelization Protocols. 802.11 MAC Protocol, IEEE 802.11 Frame

UNIT - III:
The Network Layer: Introduction, Forwarding and Routing, Network Service Models, Virtual Circuit and Datagram Networks-Virtual-Circuit Networks, Datagram Networks, Origins of VC and Datagram Networks, Inside a Router-Input Processing, Switching, Output Processing, Queuing, The Routing Control Plane, The Internet Protocol(IP):Forwarding and Addressing in the Internet- Datagram format, Ipv4 Addressing, Internet Control Message Protocol(ICMP), IPv6

UNIT - IV:
Transport Layer: Introduction and Transport Layer Services : Relationship Between Transport and Network Layers, Overview of the Transport Layer in the Internet, Multiplexing and Demultiplexing, Connectionless Transport: UDP -UDP Segment Structure, UDP Checksum, Principles of Reliable Data Transfer-Building a Reliable Data Transfer Protocol, Pipelined Reliable Data Transfer Protocols, Go-Back-N(GBN), Selective Repeat(SR), Connection Oriented Transport: TCP - The TCP Connection, TCP Segment Structure, Round-Trip Time Estimation and Timeout, Reliable Data Transfer, Flow Control, TCP Connection Management, Principles of Congestion Control - The Cause and the Costs of Congestion, Approaches to Congestion Control

UNIT - V:
Application Layer:
Principles of Networking Applications – Network Application Architectures, Processes Communicating, Transport Services Available to Applications, Transport Services Provided by the File Transfer: FTP,- FTP Commands and Replies, Electronic Mail in the Internet- STMP, Comparison with HTTP, DNS-The

Internet's Directory Service – Service Provided by DNS, Overview of How DNS Works, DNS Records and messages.

TEXTBOOKS:

1. Computer Networking A Top-Down Approach – Kurose James F, Keith W, 6th Edition, Pearson.
2. Data Communications and Networking Behrouz A. Forouzan 4th Edition McGraw-Hill Education

REFERENCES:

1. Data communication and Networks - Bhusan Trivedi, Oxford university press, 2016
2. Computer Networks -- Andrew S Tanenbaum, 4th Edition, Pearson Education
3. Understanding Communications and Networks, 3rd Edition, W. A. Shay, Cengage Learning.

EC506PC: DATA COMMUNICATIONS AND NETWORKS LAB

B.Tech. III Year I Semester

L	T	P	C
0	0	3	1.5

Note:

A. Minimum of 12 Experiments have to be conducted
B. All the Experiments may be Conducted using Network Simulation software like NS-2, NSG-2.1 and Wire SHARK/equivalent software.

Note: For Experiments 2 to 10 Performance may be evaluated through simulation by using the parameters Throughput, Packet Delivery Ratio, Delay etc.

1. Writing a TCL Script to create two nodes and links between nodes
2. Writing a TCL Script to transmit data between nodes
3. Evaluate the performance of various LAN Topologies
4. Evaluate the performance of Drop Tail and RED queue management schemes
5. Evaluate the performance of CBQ and FQ Scheduling Mechanisms
6. Evaluate the performance of TCP and UDP Protocols
7. Evaluate the performance of TCP, New Reno and Vegas
8. Evaluate the performance of AODV and DSR routing protocols
9. Evaluate the performance of AODV and DSDV routing protocols
10. Evaluate the performance of IEEE 802.11 and IEEE 802.15.4
11. Evaluate the performance of IEEE 802.11 and SMAC
12. Capturing and Analysis of TCP and IP Packets
13. Simulation and Analysis of ICMP and IGMP Packets
14. Analyze the Protocols SCTP, ARP, NetBIOS, IPX VINES
15. Analysis of HTTP, DNS and DHCP Protocols

Major Equipment Required:
Required software (Open Source) like NS-2, NSG-2.1 and Wire SHARK

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EN508HS: ADVANCED COMMUNICATION SKILLS LAB

B.Tech. III Year I Semester

L	T	P	C
0	0	2	1

1. INTRODUCTION:

The introduction of the Advanced Communication Skills Lab is considered essential at 3rd year level. At this stage, the students need to prepare themselves for their careers which may require them to listen to, read, speak and write in English both for their professional and interpersonal communication in the globalized context.

The proposed course should be a laboratory course to enable students to use 'good' English and perform the following:

- Gathering ideas and information to organize ideas relevantly and coherently.
- Engaging in debates.
- Participating in group discussions.
- Facing interviews.
- Writing project/research reports/technical reports.
- Making oral presentations.
- Writing formal letters.
- Transferring information from non-verbal to verbal texts and vice-versa.
- Taking part in social and professional communication.

2. OBJECTIVES:

This Lab focuses on using multi-media instruction for language development to meet the following targets:

- To improve the students' fluency in English, through a well-developed vocabulary and enable them to listen to English spoken at normal conversational speed by educated English speakers and respond appropriately in different socio-cultural and professional contexts.
- Further, they would be required to communicate their ideas relevantly and coherently in writing.
- To prepare all the students for their placements.

3. SYLLABUS:

The following course content to conduct the activities is prescribed for the Advanced English Communication Skills (AECS) Lab:

1. **Activities on Fundamentals of Inter-personal Communication and Building Vocabulary** - Starting a conversation – responding appropriately and relevantly – using the right body language – Role Play in different situations & Discourse Skills- using visuals - Synonyms and antonyms, word roots, one-word substitutes, prefixes and suffixes, study of word origin, business vocabulary, analogy, idioms and phrases, collocations & usage of vocabulary.
2. **Activities on Reading Comprehension** –General Vs Local comprehension, reading for facts, guessing meanings from context, scanning, skimming, inferring meaning, critical reading& effective googling.
3. **Activities on Writing Skills** – Structure and presentation of different types of writing – *letter writing/Resume writing/ e-correspondence/Technical report writing/* – planning for writing – improving one's writing.
4. **Activities on Presentation Skills** – Oral presentations (individual and group) through JAM sessions/seminars/**PPTs** and written presentations through posters/projects/reports/ e-mails/assignments etc.
5. **Activities on Group Discussion and Interview Skills** – Dynamics of group discussion, intervention, summarizing, modulation of voice, body language, relevance, fluency and organization of ideas and rubrics for evaluation- Concept and process, pre-interview planning, opening strategies, answering strategies, interview through tele-conference & video-conference and Mock Interviews.

4. MINIMUM REQUIREMENT:

The Advanced English Communication Skills (AECS) Laboratory shall have the following infrastructural facilities to accommodate at least 35 students in the lab:

- Spacious room with appropriate acoustics.
- Round Tables with movable chairs

- Audio-visual aids
- LCD Projector
- Public Address system
- P – IV Processor, Hard Disk – 80 GB, RAM–512 MB Minimum, Speed – 2.8 GHZ
- T. V, a digital stereo & Camcorder
- Headphones of High quality

5. SUGGESTED SOFTWARE:

The software consisting of the prescribed topics elaborated above should be procured and used.

- Oxford Advanced Learner's Compass, 7th Edition
- DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice.
- Lingua TOEFL CBT Insider, by Dream tech
- TOEFL & GRE (KAPLAN, AARCO & BARRONS, USA, Cracking GRE by CLIFFS)

TEXT BOOKS:

1. Effective Technical Communication by M Asharaf Rizvi. McGraw Hill Education (India) Pvt. Ltd. 2nd Edition
2. Academic Writing: A Handbook for International Students by Stephen Bailey, Routledge, 5th Edition.

REFERENCES:

1. Learn Correct English – A Book of Grammar, Usage and Composition by Shiv K. Kumar and Hemalatha Nagarajan. Pearson 2007
2. Professional Communication by Aruna Koneru, McGraw Hill Education (India) Pvt. Ltd, 2016.
3. Technical Communication by Meenakshi Raman & Sangeeta Sharma, Oxford University Press 2009.
4. Technical Communication by Paul V. Anderson. 2007. Cengage Learning pvt. Ltd. New Delhi.
5. English Vocabulary in Use series, Cambridge University Press 2008.
6. Handbook for Technical Communication by David A. McMurrey & Joanne Buckley. 2012. Cengage Learning.
7. Communication Skills by Leena Sen, PHI Learning Pvt Ltd., New Delhi, 2009.
8. Job Hunting by Colm Downes, Cambridge University Press 2008.
9. English for Technical Communication for Engineering Students, Aysha Vishwamohan, Tata Mc Graw-Hill 2009.

EC602PC: DIGITAL SIGNAL PROCESSING

B.Tech. III Year II Semester

L	T	P	C
3	1	0	4

Prerequisite: Signals and Systems

Course Objectives:
1. To provide background and fundamental material for the analysis and processing of digital signals.
2. To understand the fast computation of DFT and appreciate the FFT processing.
3. To study the designs and structures of digital (IIR and FIR) filters and analyze and synthesize for a given specifications.
4. To acquaint in Multi-rate signal processing techniques and finite word length effects.

Course Outcomes: Upon completing this course, the student will be able to
1. Understand the LTI system characteristics and Multirate signal processing.
2. Understand the inter-relationship between DFT and various transforms.
3. Design a digital filter for a given specification.
4. Understand the significance of various filter structures and effects of round off errors.

UNIT - I:
Introduction: Introduction to Digital Signal Processing: Discrete Time Signals & Sequences, conversion of continuous to discrete signal, Normalized Frequency, Linear Shift Invariant Systems, Stability, and Causality, linear differential equation to difference equation, Linear Constant Coefficient Difference Equations, Frequency Domain Representation of Discrete Time Signals and Systems
Multirate Digital Signal Processing: Introduction, Down Sampling, Decimation, Up sampling, Interpolation, Sampling Rate Conversion.

UNIT - II:
Discrete Fourier series: Fourier Series, Fourier Transform, Laplace Transform and Z-Transform relation, DFS Representation of Periodic Sequences, Properties of Discrete Fourier Series, Discrete Fourier Transforms: Properties of DFT, Linear Convolution of Sequences using DFT, Computation of DFT: Over-Lap Add Method, Over-Lap Save Method, Relation between DTFT, DFS, DFT and Z-Transform.
Fast Fourier Transforms: Fast Fourier Transforms (FFT) - Radix-2 Decimation-in-Time and Decimation-in-Frequency FFT Algorithms, Inverse FFT.

UNIT - III
IIR Digital Filters: Analog filter approximations – Butterworth and Chebyshev, Design of IIR Digital Filters from Analog Filters, Step and Impulse Invariant Techniques, Bilinear Transformation Method, Spectral Transformations.

UNIT - IV
FIR Digital Filters: Characteristics of FIR Digital Filters, Frequency Response. Design of FIR Filters: Fourier Method, Digital Filters using Window Techniques, Frequency Sampling Technique, Comparison of IIR & FIR filters.

UNIT - V
Realization of Digital Filters: Applications of Z – Transforms, Solution of Difference Equations of Digital Filters, System Function, Stability Criterion, Frequency Response of Stable Systems, Realization of Digital Filters – Direct, Canonic, Cascade and Parallel Forms.
Finite Word Length Effects: Limit cycles, Overflow Oscillations, Round-off Noise in IIR Digital Filters, Computational Output Round Off Noise, Methods to Prevent Overflow, Trade Off Between Round Off and Overflow Noise, Measurement of Coefficient Quantization Effects through Pole-Zero Movement, Dead Band Effects.

TEXT BOOKS:
1. Discrete Time Signal Processing – A. V. Oppenheim and R.W. Schaffer, PHI, 2009
2. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G. Manolakis, Pearson Education / PHI, 2007.

REFERENCE BOOKS:

1. Digital Signal Processing – Fundamentals and Applications – Li Tan, Elsevier, 2008
2. Fundamentals of Digital Signal Processing using MATLAB – Robert J. Schilling, Sandra L. Harris, Thomson, 2007
3. Digital Signal Processing – S. Salivahanan, A. Vallavaraj and C. Gnanapriya, TMH, 2009
4. Digital Signal Processing - A Practical approach, Emmanuel C. Ifeachor and Barrie W. Jervis, 2nd Edition, Pearson Education, 2009

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EC604PC: DIGITAL SIGNAL PROCESSING LAB

B.Tech. III Year II Semester

L	T	P	C
0	0	3	1.5

The Programs shall be implemented in Software (Using MATLAB / Lab View / C Programming/ Equivalent) and Hardware (Using TI / Analog Devices / Motorola / Equivalent DSP processors).

Note: - Minimum of 12 experiments has to be conducted.

List of Experiments:

1. Generation of Sinusoidal Waveform / Signal based on Recursive Difference Equations
2. Histogram of White Gaussian Noise and Uniformly Distributed Noise.
3. To find DFT / IDFT of given DT Signal
4. To find Frequency Response of a given System given in Transfer Function/ Differential equation form.
5. Obtain Fourier series coefficients by formula and using FET and compare for half sine wave.
6. Implementation of FFT of given Sequence
7. Determination of Power Spectrum of a given Signal(s).
8. Implementation of LP FIR Filter for a given Sequence/Signal.
9. Implementation of HP IIR Filter for a given Sequence/Signal
10. Generation of Narrow Band Signal through Filtering
11. Generation of DTMF Signals
12. Implementation of Decimation Process
13. Implementation of Interpolation Process
14. Implementation of I/D Sampling Rate Converters
15. Impulse Response of First order and Second Order Systems.

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EC603PC: VLSI DESIGN

B.Tech. III Year II Semester

L	T	P	C
3	1	0	4

Prerequisite: Electronic Circuit Analysis; Switching Theory and Logic Design

Course Objectives: The objectives of the course are to:

1. Give exposure to different steps involved in the fabrication of ICs.
2. Explain electrical properties of MOS and BiCMOS devices to analyze the behavior of inverters designed with various loads.
3. Give exposure to the design rules to be followed to draw the layout of any logic circuit.
4. Provide design concepts to design building blocks of data path of any system using gates.
5. Understand basic programmable logic devices and testing of CMOS circuits.

Course Outcomes: Upon completing this course, the student will be able to

1. Acquire qualitative knowledge about the fabrication process of integrated circuits using MOS transistors.
2. Draw the layout of any logic circuit which helps to understand and estimate parasitic effect of any logic circuit
3. Design building blocks of data path systems, memories and simple logic circuits using PLA, PAL, FPGA and CPLD.
4. Understand different types of faults that can occur in a system and learn the concept of testing and adding extra hardware to improve testability of system.

UNIT – I

Introduction: Introduction to IC Technology – MOS, PMOS, NMOS, CMOS & BiCMOS

Basic Electrical Properties: Basic Electrical Properties of MOS and BiCMOS Circuits: I_{ds}-V_{ds} relationships, MOS transistor threshold Voltage, g_m, g_{ds}, Figure of merit; Pass transistor, NMOS Inverter, Various pull ups, CMOS Inverter analysis and design, Bi-CMOS Inverters.

UNIT - II

VLSI Circuit Design Processes: VLSI Design Flow, MOS Layers, Stick Diagrams, Design Rules and Layout, Transistors Layout Diagrams for NMOS and CMOS Inverters and Gates, Scaling of MOS circuits.

UNIT – III

Gate Level Design: Logic Gates and Other complex gates, Switch logic, Alternate gate circuits, Time delays, Driving large capacitive loads, Wiring capacitance, Fan – in, Fan – out.

UNIT - IV

Data Path Subsystems: Subsystem Design, Shifters, Adders, ALUs, Multipliers, Parity generators, Comparators, Zero/One Detectors, Counters.

Array Subsystems: SRAM, DRAM, ROM, Serial Access Memories.

UNIT - V

Programmable Logic Devices: Design Approach – PLA, PAL, Standard Cells FPGAs, CPLDs.

CMOS Testing: CMOS Testing, Test Principles, Design Strategies for test, Chip level Test Techniques.

TEXT BOOKS:

1. Essentials of VLSI circuits and systems – Kamran Eshraghian, Eshraghian Dougles and A. Pucknell, PHI, 2005 Edition
2. CMOS VLSI Design – A Circuits and Systems Perspective, Neil H. E Weste, David Harris, Ayan Banerjee, 3rd Ed, Pearson, 2009.

REFERENCE BOOKS:

1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRC Press, 2011
2. CMOS logic circuit Design - John. P. Uyemura, Springer, 2007.
3. Modern VLSI Design - Wayne Wolf, Pearson Education, 3rd Edition, 1997.
4. VLSI Design- K. Lal Kishore, V. S. V. Prabhakar, I.K International, 2009.

EC605PC: e - CAD LAB

B.Tech. III Year II Semester

L	T	P	C
0	0	3	1.5

Note: Any **SIX of** the following experiments from each part are to be conducted (Total 12)

Part - I

All the following experiments have to be implemented using HDL

1. Realize all the logic gates
2. Design of 8-to-3 encoder (without and with priority) and 2-to-4 decoder
3. Design of 8-to-1 multiplexer and 1-to-8 demultiplexer
4. Design of 4 bit binary to gray code converter
5. Design of 4 bit comparator
6. Design of Full adder using 3 modeling styles
7. Design of flip flops: SR, D, JK, T
8. Design of 4-bit binary, BCD counters (synchronous/ asynchronous reset) or any sequence counter
9. Finite State Machine Design

Part-II

Layout, physical verification, placement & route for complex design, static timing analysis, IR drop analysis and crosstalk analysis for the following:

1. Basic logic gates
2. CMOS inverter
3. CMOS NOR/ NAND gates
4. CMOS XOR and MUX gates
5. Static / Dynamic logic circuit (register cell)
6. Latch
7. Pass transistor
8. Layout of any combinational circuit (complex CMOS logic gate).

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EC606PC: SCRIPTING LANGUAGES LAB

B.Tech. III Year II Semester

L	T	P	C
0	0	2	1

Prerequisites: Any High-level programming language (C, C++)

Course Objectives:
- To Understand the concepts of scripting languages for developing web-based projects
- To understand the applications the of Ruby, TCL, Perl scripting languages

Course Outcomes:
- Ability to understand the differences between Scripting languages and programming languages
- Able to gain some fluency programming in Ruby, Perl, TCL

List of Experiments
1. Write a Ruby script to create a new string which is n copies of a given string where n is a non-negative integer
2. Write a Ruby script which accept the radius of a circle from the user and compute the parameter and area.
3. Write a Ruby script which accept the user's first and last name and print them in reverse order with a space between them
4. Write a Ruby script to accept a filename from the user print the extension of that
5. Write a Ruby script to find the greatest of three numbers
6. Write a Ruby script to print odd numbers from 10 to 1
7. Write a Ruby scirpt to check two integers and return true if one of them is 20 otherwise return their sum
8. Write a Ruby script to check two temperatures and return true if one is less than 0 and the other is greater than 100
9. Write a Ruby script to print the elements of a given array
10. Write a Ruby program to retrieve the total marks where subject name and marks of a student stored in a hash
11. Write a TCL script to find the factorial of a number
12. Write a TCL script that multiplies the numbers from 1 to 10
13. Write a TCL script for Sorting a list using a comparison function
14. Write a TCL script to (i)create a list (ii)append elements to the list (iii)Traverse the list (iv)Concatenate the list
15. Write a TCL script to comparing the file modified times.
16. Write a TCL script to Copy a file and translate to native format.
17. a) Write a Perl script to find the largest number among three numbers.
    b) Write a Perl script to print the multiplication tables from 1-10 using subroutines.
18. Write a Perl program to implement the following list of manipulating functions
    a)Shift
    b)Unshift
    c)Push
19. a) Write a Perl script to substitute a word, with another word in a string.
    b) Write a Perl script to validate IP address and email address.
20. Write a Perl script to print the file in reverse order using command line arguments

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

B.TECH. ELECTRONICS AND COMMUNICATION ENGINEERING
IV YEAR COURSE STRUCTURE & SYLLABUS (R16)

Applicable From 2016-17 Admitted Batch

IV YEAR I SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1	EC701PC	Microwave Engineering	4	0	0	4
2		Professional Elective - II	3	0	0	3
3		Professional Elective - III	3	0	0	3
4		Professional Elective - IV	3	0	0	3
5	EC702PC	VLSI Design	4	0	0	4
6	EC703PC	VLSI and E-CAD Lab	0	0	3	2
7	EC704PC	Microwave Engineering Lab	0	0	3	2
8	EC705PC	Industry Oriented Mini Project	0	0	3	2
9	EC706PC	Seminar	0	0	2	1
		Total Credits	**17**	**0**	**11**	**24**

IV YEAR II SEMESTER

S.No.	Course Code	Course Title	L	T	P	Credits
1		Open Elective – III	3	0	0	3
2		Professional Elective -V	3	0	0	3
3		Professional Elective -VI	3	0	0	3
4	EC801PC	Major Project	0	0	30	15
		Total Credits	**9**	**0**	**30**	24

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MICROWAVE ENGINEERING

B.Tech. IV Year I Sem. **L T P C**
Course Code: EC701PC/ET743PE **4 0 0 4**

Course Objectives: This is a core course in Microwave Communications domain, and covers contents related to Microwave Theory and Techniques. The main objectives of the course are:

- To get familiarized with microwave frequency bands, their applications and to understand the limitations and losses of conventional tubes at these frequencies.
- To develop the theory related to microwave transmission lines, and to determine the characteristics of rectangular waveguides, microstrip lines, and different types of waveguide components and ferrite devices.
- To distinguish between different types of microwave tubes, their structures and principles of microwave power generation, and to characterize their performance features and applications - at tube levels as well as with solid state devices.
- To impart the knowledge of Scattering Matrix, its formulation and utility, and establish the S-Matrix for various types of microwave junctions.
- To understand the concepts of microwave measurements, identify the equipment required and precautions to be taken, and get familiarized with the methods of measurement of microwave power and various other microwave parameters.

Course Outcomes: Having gone through this course covering different aspects of microwave theory and techniques, the students would be able to

- To analyze completely the rectangular waveguides, their mode characteristics, and design waveguides for solving practical microwave transmission line problems.
- To distinguish between the different types of waveguide and ferrite components, explain their functioning and select proper components for engineering applications.
- To distinguish between the methods of power generation at microwave frequencies, derive the performance characteristics of 2-Cavity and Relfex Klystrons, Magnetrons, TWTs and estimate their efficiency levels, and solve related numerical problems
- To realize the need for solid state microwave sources, understand the concepts of TEDs, RWH Theory and explain the salient features of Gunn Diodes and ATT Devices.
- To establish the properties of Scattering Matrix, formulate the S-Matrix for various microwave junctions, and understand the utility of S-parameters in microwave component design.
- To set up a microwave bench, establish the measurement procedure and conduct the experiments in microwave lab for measurement of various microwave parameters.

UNIT - I

Microwave Transmission Lines - I: Introduction, Microwave Spectrum and Bands, Applications of Microwaves. Rectangular Waveguides – Solution of Wave Equations in

Rectangular Coordinates, TE/TM mode analysis, Expressions for Fields, Characteristic Equation and Cut-off Frequencies, Filter Characteristics, Dominant and Degenerate Modes, Sketches of TE and TM mode fields in the cross-section, Mode Characteristics – Phase and Group Velocities, Wavelengths and Impedance Relations, Power Transmission, Impossibility of TEM Mode. Illustrative Problems, Micro strip Lines– Introduction, Z_0 Relations, Effective Dielectric Constant.

UNIT - II

Cavity Resonators– Introduction, Rectangular Cavities, Dominant Modes and Resonant Frequencies, Q Factor and Coupling Coefficients, Illustrative Problems

Waveguide Components and Applications: Coupling Mechanisms – Probe, Loop, Aperture types. Waveguide Discontinuities – Waveguide Windows, Tuning Screws and Posts, Matched Loads. Waveguide Attenuators – Different Types, Resistive Card and Rotary Vane Attenuators; Waveguide Phase Shifters – Types, Dielectric and Rotary Vane Phase Shifters, Waveguide Multiport Junctions – E plane and H plane Tees, Magic Tee. Directional Couplers – 2 Hole, Bethe Hole types, Illustrative Problems

Ferrites– Composition and Characteristics, Faraday Rotation, Ferrite Components – Gyrator, Isolator, Circulator.

UNIT - III

Microwave Tubes: Limitations and Losses of conventional Tubes at Microwave Frequencies, Microwave Tubes – O Type and M Type Classifications, O-type Tubes : 2 Cavity Klystrons – Structure, Reentrant Cavities, Velocity Modulation Process and Applegate Diagram, Bunching Process and Small Signal Theory – Expressions for O/P Power and Efficiency. Reflex Klystrons – Structure, Velocity Modulation and Applegate Diagram, Mathematical Theory of Bunching, Power Output, Efficiency, Oscillating Modes and O/P Characteristics, Illustrative Problems.

Helix TWTs: Significance, Types and Characteristics of Slow Wave Structures; Structure of TWT and Amplification Process (qualitative treatment), Suppression of Oscillations, Gain Considerations.

UNIT - IV

M-Type Tubes:

Introduction, Cross-field Effects, Magnetrons – Different Types, Cylindrical Traveling Wave Magnetron – Hull Cut-off and Hartree Conditions, Modes of Resonance and PI-Mode Operation, Separation of PI-Mode, o/p characteristics, Illustrative Problems

Microwave Solid State Devices: Introduction, Classification, Applications. TEDs – Introduction, Gunn Diodes – Principle, RWH Theory, Characteristics, Modes of Operation - Gunn Oscillation Modes, Introduction to Avalanche Transit Time Devices.

UNIT - V

Scattering Matrix– Significance, Formulation and Properties, S Matrix Calculations for – 2 port Junctions, E plane and H plane Tees, Magic Tee, Circulator and Isolator, Illustrative Problems.

Microwave Measurements: Description of Microwave Bench – Different Blocks and their Features, Errors and Precautions, Microwave Power Measurement, Bolometers. Measurement of Attenuation, Frequency. Standing Wave Measurements – Measurement of Low and High VSWR, Cavity Q, Impedance Measurements.

TEXT BOOKS:

1. Microwave Devices and Circuits – Samuel Y. Liao, Pearson, 3rd Edition, 2003.
2. Microwave Principles – Herbert J. Reich, J.G. Skalnik, P.F. Ordung and H.L. Krauss, CBS Publishers and Distributors, New Delhi, 2004.

REFERENCES:

1. Foundations for Microwave Engineering – R.E. Collin, IEEE Press, John Wiley, 2nd Edition, 2002.
2. Microwave Engineering - G.S. Raghuvanshi, Cengage Learning India Pvt. Ltd., 2012.
3. Microwave Engineering Passive Circuits – Peter A. Rizzi, PHI, 1999.
4. Microwave Engineering - David M. Pozar, John Wiley & Sons (Asia) Pvt Ltd., 1989, 3r ed., 2011 Reprint.

MICROWAVE ENGINEERING LAB

B.Tech. IV Year I Sem. **L T P C**
Course Code: EC704PC **0 0 3 2**

Note: Minimum of 12 experments to be conducted

1. Reflex Klystron Characteristics
2. Gunn Diode Characteristics
3. Directional Coupler Characteristics
4. VSWR Measurement of Mached load
5. VSWR mesurement of with open and short circuit loads
6. Measurement of Waveguide Parameters
7. Measurement of Impedance of a given Load
8. Measurement of Scattering Parameters of a E plane Tee
9. Measurement of Scattering Parameters of a H plane Tee
10. Measurement of Scattering Parameters of a Magic Tee
11. Measurement of Scattering Parameters of a Circulator
12. Attenuation Measurement
13. Microwave Frequency Measurement
14. Antenna Pattern Measurements.

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VLSI DESIGN

B.Tech. IV Year I Sem. **L T P C**
Course Code: EC702PC/ET721PE/EI741PE **4 0 0 4**

Course Objectives: The objectives of the course are to:

- Give exposure to different steps involved in the fabrication of ICs using MOS transistor, CMOS/BICMOS transistors, and passive components.
- Explain electrical properties of MOS and BiCMOS devices to analyze the behavior of inverters designed with various loads.
- Give exposure to the design rules to be followed to draw the layout of any logic circuit.
- Provide concept to design different types of logic gates using CMOS inverter and analyze their transfer characteristics.
- Provide design concepts to design building blocks of data path of any system using gates.
- Understand basic programmable logic devices and testing of CMOS circuits.

Course Outcomes: Upon successfully completing the course, the student should be able to:

- Acquire qualitative knowledge about the fabrication process of integrated circuit using MOS transistors.
- Choose an appropriate inverter depending on specifications required for a circuit
- Draw the layout of any logic circuit which helps to understand and estimate parasitic of any logic circuit
- Design different types of logic gates using CMOS inverter and analyze their transfer characteristics
- Provide design concepts required to design building blocks of data path using gates.
- Design simple memories using MOS transistors and can understand design of large memories.
- Design simple logic circuit using PLA, PAL, FPGA and CPLD.
- Understand different types of faults that can occur in a system and learn the concept of testing and adding extra hardware to improve testability of system

UNIT – I

Introduction: Introduction to IC Technology – MOS, PMOS, NMOS, CMOS & BiCMOS
Basic Electrical Properties: Basic Electrical Properties of MOS and BiCMOS Circuits: I_{ds}-V_{ds} relationships, MOS transistor threshold Voltage, g_m, g_{ds}, Figure of merit ωo; Pass transistor, NMOS Inverter, Various pull ups, CMOS Inverter analysis and design, Bi-CMOS Inverters.

UNIT - II

VLSI Circuit Design Processes: VLSI Design Flow, MOS Layers, Stick Diagrams, Design Rules and Layout, 2 µm CMOS Design rules for wires, Contacts and Transistors Layout Diagrams for NMOS and CMOS Inverters and Gates, Scaling of MOS circuits.

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UNIT – III

Gate Level Design: Logic Gates and Other complex gates, Switch logic, Alternate gate circuits, Time delays, Driving large capacitive loads, Wiring capacitance, Fan – in, Fan – out, Choice of layers.

UNIT - IV

Data Path Subsystems: Subsystem Design, Shifters, Adders, ALUs, Multipliers, Parity generators, Comparators, Zero/One Detectors, Counters.

Array Subsystems: SRAM, DRAM, ROM, Serial Access Memories.

UNIT - V

Programmable Logic Devices: PLAs, FPGAs, CPLDs, Standard Cells, Programmable Array Logic, Design Approach, Parameters influencing low power design.

CMOS Testing: CMOS Testing, Need for testing, Test Principles, Design Strategies for test, Chip level Test Techniques.

TEXT BOOKS:

1. Essentials of VLSI circuits and systems – Kamran Eshraghian, Eshraghian Dougles and A. Pucknell, PHI, 2005 Edition
2. CMOS VLSI Design – A Circuits and Systems Perspective, Neil H. E Weste, David Harris, Ayan Banerjee, 3rd Ed, Pearson, 2009.

REFERENCE BOOKS:

1. CMOS logic circuit Design - John .P. Uyemura, Springer, 2007.
2. Modern VLSI Design - Wayne Wolf, Pearson Education, 3rd Edition, 1997.

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VLSI & E-CAD LAB

B.Tech. IV Year I Sem. **L T P C**
Course Code: EC703PC **0 0 3 2**

List of Experiments

Design and implementation of the following CMOS digital/analog circuits using **Cadence / Mentor Graphics / Synopsys /Equivalent** CAD tools. The design shall include Gate-level design, Transistor-level design, Hierarchical design, Verilog HDL/VHDL design, Logic synthesis, Simulation and verification, Scaling of CMOS Inverter for different technologies, study of secondary effects (temperature, power supply and process corners), Circuit optimization with respect to area, performance and/or power, Layout, Extraction of parasitics and back annotation, modifications in circuit parameters and layout consumption, DC/transient analysis, Verification of layouts (DRC, LVS)

E-CAD programs:

Programming can be done using any complier. Down load the programs on FPGA/CPLD boards and performance testing may be done using pattern generator (32 channels) and logic analyzer apart from verification by simulation with any of the front end tools.

1. HDL code to realize all the logic gates
2. Design of 2-to-4 decoder
3. Design of 8-to-3 encoder (without and with priority)
4. Design of 8-to-1 multiplexer and 1-to-8 demultiplexer
5. Design of 4 bit binary to gray code converter
6. Design of 4 bit comparator
7. Design of Full adder using 3 modeling styles
8. Design of flip flops: SR, D, JK, T
9. Design of 4-bit binary, BCD counters (synchronous/ asynchronous reset) or any sequence counter
10. Finite State Machine Design

VLSI programs:

- Introduction to layout design rules. Layout, physical verification, placement & route for complex design, static timing analysis, IR drop analysis and crosstalk analysis of the following:
  1. Basic logic gates
  2. CMOS inverter
  3. CMOS NOR/ NAND gates
  4. CMOS XOR and MUX gates
  5. Static / Dynamic logic circuit (register cell)
  6. Latch
  7. Pass transistor
  8. Layout of any combinational circuit (complex CMOS logic gate).
  9. Analog Circuit simulation (AC analysis) – CS & CD amplifier

***Note:** Any **SIX** of the above experiments from each part are to be conducted (Total 12)*

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M. Tech. (EMBEDDED SYSTEMS & VLSI DESIGN/VLSI AND EMBEDDED SYSTEMS/ ELECTRONICS DESIGN TECHNOLOGY)

COURSE STRUCTURE AND SYLLABUS

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Low Power VLSI Design	25	75	4	--	4
Core Course V	CMOS Mixed Signal Circuit Design	25	75	4	--	4
Core Course VI	Real Time Operating Systems	25	75	4	--	4
Core Elective III	Digital Signal Processors and Architectures System On Chip Architecture Embedded Networking	25	75	4	--	4
Core Elective IV	Design for Testability Semiconductor Memory Design and Testing Full Custom Design	25	75	4	--	4
Open Elective II	Image and Video Processing Adhoc Wireless Networks Sensors and Actuators	25	75	4	--	4
Laboratory II	Embedded Systems Laboratory	25	75	--	4	2
Seminar II	Seminar	50	--	--	4	2
Total Credits				24	8	28

Sem-II

R15

Regulation

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IMAGE AND VIDEO PROCESSING
(Open Elective –II)

UNIT –I:
Fundamentals of Image Processing and Image Transforms: Basic steps of Image Processing System Sampling and Quantization of an image, Basic relationship between pixels.
Image Segmentation: Segmentation concepts, Point, Line and Edge Detection, Thresholding, Region based segmentation.

UNIT –II:
Image Enhancement: Spatial domain methods: Histogram processing, Fundamentals of Spatial filtering, Smoothing spatial filters, Sharpening spatial filters.
Frequency domain methods: Basics of filtering in frequency domain, image smoothing, image sharpening, Selective filtering.

UNIT –III:
Image Compression: Image compression fundamentals - Coding Redundancy, Spatial and Temporal redundancy, Compression models: Lossy & Lossless, Huffman coding, , Bit plane coding, Transform coding, Predictive coding, Wavelet coding, Lossy Predictive coding, JPEG Standards.

UNIT -IV:
Basic Steps of Video Processing: Analog Video, Digital Video. Time-Varying Image Formation models: Three-Dimensional Motion Models, Geometric Image Formation, Photometric Image Formation, Sampling of Video signals, Filtering operations.

UNIT –V:
2-D Motion Estimation: Optical flow, General Methodologies, Pixel Based Motion Estimation, Block-Matching Algorithm, Mesh based Motion Estimation, Global Motion Estimation, Region based Motion Estimation, Multi resolution motion estimation, Waveform based coding, Block based transform coding, Predictive coding, Application of motion estimation in Video coding.

TEXT BOOKS:

1. Digital Image Processing – Gonzaleze and Woods, 3rd Ed., Pearson.
2. Video Processing and Communication – Yao Wang, Joem Ostermann and Ya–quin Zhang. 1st Ed., PH Int.

REFRENCE BOOKS:

1. Digital Image Processing and Analysis-Human and Computer Vision Application with CVIP Tools – Scotte Umbaugh, 2nd Ed, CRC Press, 2011.
2. Digital Video Processing – M. Tekalp, Prentice Hall International.
3. Digital Image Processing – S.Jayaraman, S.Esakkirajan, T.Veera Kumar – TMH, 2009.
4. Multidimentional Signal, Image and Video Processing and Coding – John Woods, 2nd Ed, Elsevier.
5. Digital Image Processing with MATLAB and Labview – Vipula Singh, Elsevier.
6. Video Demystified – A Hand Book for the Digital Engineer – Keith Jack, 5th Ed., Elsevier.

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M. Tech – I Year – I Sem. (ES & VLSID/VLSI & ES/EDT)

AD-HOC WIRELESS NETWORKS

(Open Elective –II)

UNIT - I:

Wireless Local Area Networks

Introduction, wireless LAN Topologies, Wireless LAN Requirements,
Physical Layer- Infrared Physical Layer, Microwave based Physical Layer Alternatives, Medium Access Control Layer- HIPERLAN 1 Sublayer, IEEE 802.11 MAC Sublayer and Latest Developments-802.11a, 802.11b, 802.11g
Personal Area Networks: Introduction to PAN technology and Applications, Bluetooth -specifications, Radio Channel, Piconets and Scatternets, Inquiry, Paging and Link Establishment, Packet Format, Link Types, Power Management, Security, Home RF -Physical and MAC Layer

UNIT - II:

MAC Protocols

Introduction, Issues in Designing a MAC protocol for Ad Hoc Wireless Networks, Design goals of a MAC Protocol for Ad Hoc Wireless Networks, Classifications of MAC Protocols, Contention - Based Protocols, Contention - Based Protocols with reservation Mechanisms, Contention – Based MAC Protocols with Scheduling Mechanisms, MAC Protocols that use Directional Antennas, Other MAC Protocols.

UNIT - III:

Routing Protocols

Introduction, Issues in Designing a Routing Protocol for Ad Hoc Wireless Networks, Classification of Routing Protocols, Table –Driven Routing Protocols, On – Demand Routing Protocols, Hybrid Routing Protocols, Routing Protocols with Efficient Flooding Mechanisms, Hierarchical Routing Protocols, Power – Aware Routing Protocols.

UNIT – IV:

Transport Layer Protocols

Introduction, Issues in Designing a Transport Layer Protocol for Ad Hoc Wireless Networks, Design Goals of a Transport Layer Protocol for Ad Hoc Wireless Networks, Classification of Transport Layer Solutions, TCP Over Ad Hoc Wireless Networks, Other Transport Layer Protocol for Ad Hoc Wireless Networks.

UNIT – V:

Quality of Service in Ad Hoc Wireless Networks:

Introduction, Real Time Traffic Support in Ad Hoc Wireless Networks, QoS Parameters in Ad Hoc Wireless Network, Issues and Challenges in providing QoS in Ad Hoc Wireless Networks, Classification of QoS Solutions: MAC Layer Solutions, Cluster TDMA, IEEE 802.11e, DBASE, Network Layer Solutions, QoS Routing Protocols, Ticket Based QoS Routing Protocol, Predictive Location Based QoS routing protocol, Trigger Based Distributed QoS Routing Protocol, QoS enabled AODV Routing Protocol, Bandwidth QoS Routing Protocol, On Demand QoS Routing Protocol, On Demand Link-State Multipath QoS Routing Protocol, Asynchronous Slot Allocation Strategies. QoS Frameworks for Ad Hoc Wireless Networks.

TEXT BOOKS:

1. Ad Hoc Wireless Networks: Architectures and Protocols - C. Siva Ram Murthy and B.S.Manoj, 2004, PHI.
2. Wireless Networks -P Nicopolitidis and M S Obaidat, Wiley India Edition 2003.

REFERENCE BOOKS

1. Wireless Communication Technology- Roy Blake, CENGAGE,2012
2. Wireless Ad- hoc and Sensor Networks: Protocols, Performance and Control - Jagannathan Sarangapani, CRC Press.

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M. Tech – I Year – I Sem. (ES & VLSID/VLSI & ES/EDT)

SENSORS AND ACTUATORS
(Open Elective –II)

UNIT -I:
Sensors / Transducers: Principles – Classification – Parameters – Characteristics - Environmental Parameters (EP) – Characterization
Mechanical and Electromechanical Sensors: Introduction – Resistive Potentiometer – Strain Gauge – Resistance Strain Gauge – Semiconductor Strain Gauges -Inductive Sensors: Sensitivity and Linearity of the Sensor –Types-Capacitive Sensors:– Electrostatic Transducer– Force/Stress Sensors Using Quartz Resonators – Ultrasonic Sensors

UNIT –II:
Thermal Sensors: Introduction – Gas thermometric Sensors – Thermal Expansion Type Thermometric Sensors – Acoustic Temperature Sensor – Dielectric Constant and Refractive Index thermosensors – Helium Low Temperature Thermometer – Nuclear Thermometer – Magnetic Thermometer – Resistance Change Type Thermometric Sensors –Thermoemf Sensors– Junction Semiconductor Types– Thermal Radiation Sensors –Quartz Crystal Thermoelectric Sensors – NQR Thermometry – Spectroscopic Thermometry – Noise Thermometry – Heat Flux Sensors
Magnetic sensors: Introduction – Sensors and the Principles Behind – Magneto-resistive Sensors – Anisotropic Magnetoresistive Sensing – Semiconductor Magnetoresistors– Hall Effect and Sensors – Inductance and Eddy Current Sensors– Angular/Rotary Movement Transducers – Synchros – Synchro-resolvers - Eddy Current Sensors – Electromagnetic Flowmeter – Switching Magnetic Sensors SQUID Sensors

UNIT -III:
Radiation Sensors: Introduction – Basic Characteristics – Types of Photosensistors/Photo detectors– X-ray and Nuclear Radiation Sensors– Fiber Optic Sensors
Electro analytical Sensors: Introduction – The Electrochemical Cell – The Cell Potential - Standard Hydrogen Electrode (SHE) – Liquid Junction and Other Potentials – Polarization – Concentration Polarization— Reference Electrodes - Sensor Electrodes – Electro ceramics in Gas Media .

UNIT -IV:
Smart Sensors: Introduction – Primary Sensors – Excitation – Amplification – Filters – Converters – Compensation– Information Coding/Processing - Data Communication – Standards for Smart Sensor Interface – The Automation
Sensors –Applications: Introduction – On-board Automobile Sensors (Automotive Sensors)– Home Appliance Sensors – Aerospace Sensors — Sensors for Manufacturing –Sensors for environmental Monitoring

UNIT -V:
Actuators: Pneumatic and Hydraulic Actuation Systems- Actuation systems – Pneumatic and hydraulic systems - Directional Control valves – Presure control valves – Cylinders - Servo and proportional control valves – Process control valves – Rotary actuators
Mechanical Actuation Systems- Types of motion – Kinematic chains – Cams – Gears – Ratchet and pawl – Belt and chain drives – Bearings – Mechanical aspects of motor selection
Electrical Actuation Systems-Electrical systems -Mechanical switches – Solid-state switches Solenoids – D.C. Motors – A.C. motors – Stepper motors

TEXT BOOKS:
1. D. Patranabis – "Sensors and Transducers" –PHI Learning Private Limited.
2. W. Bolton – "Mechatronics" –Pearson Education Limited.

REFERENCE BOOK:

1. Sensors and Actuators – D. Patranabis – 2nd Ed., PHI, 2013.

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M. Tech – I Year – I Sem. (ES & VLSID/VLSI & ES/EDT)

EMBEDDED SYSTEMS LABORATORY

Note:

- The following programs are to be implemented on ARM based Processors/Equivalent.
- Minimum of 10 programs from Part –I and 6 programs from Part -II are to be conducted.

Part -I:

The following Programs are to be implemented on ARM Processor

1. Simple Assembly Program for
   a. Addition | Subtraction | Multiplication | Division
   b. Operating Modes, System Calls and Interrupts
   c. Loops, Branches
2. Write an Assembly programs to configure and control General Purpose Input/Output (GPIO) port pins.
3. Write an Assembly programs to read digital values from external peripherals and execute them with the Target board.
4. Program for reading and writing of a file
5. Program to demonstrate Time delay program using built in Timer / Counter feature on IDE environment
6. Program to demonstrates a simple interrupt handler and setting up a timer
7. Program demonstrates setting up interrupt handlers. Press button to generate an interrupt and trace the program flow with debug terminal.
8. Program to Interface 8 Bit LED and Switch Interface
9. Program to implement Buzzer Interface on IDE environment
10. Program to Displaying a message in a 2 line x 16 Characters LCD display and verify the result in debug terminal.
11. Program to demonstrate I2C Interface on IDE environment
12. Program to demonstrate I2C Interface – Serial EEPROM
13. Demonstration of Serial communication. Transmission from Kit and reception from PC using Serial Port on IDE environment use debug terminal to trace the program.
14. Generation of PWM Signal
15. Program to demonstrate SD-MMC Card Interface.

Part -II:

Write the following programs to understand the use of RTOS with ARM Processor on IDE Environment using ARM Tool chain and Library:

1. Create an application that creates two tasks that wait on a timer whilst the main task loops.
2. Write an application that creates a task which is scheduled when a button is pressed, which illustrates the use of an event set between an ISR and a task
3. Write an application that Demonstrates the interruptible ISRs(Requires timer to have higher priority than external interrupt button)
4. a).Write an application to Test message queues and memory blocks.
   b).Write an application to Test byte queues
5. Write an application that creates two tasks of the same priority and sets the time slice period to illustrate time slicing.

Interfacing Programs:

6. Write an application that creates a two task to Blinking two different LEDs at different timings
7. Write an application that creates a two task displaying two different messages in LCD display in two lines.
8. Sending messages to mailbox by one task and reading the message from mailbox by another task.
9. Sending message to PC through serial port by three different tasks on priority Basis.
10. Basic Audio Processing on IDE environment.

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REAL TIME OPERATING SYSTEMS

UNIT – I:
Introduction
Introduction to UNIX/LINUX, Overview of Commands, File I/O.(open, create, close, lseek, read, write), Process Control (fork, vfork, exit, wait, waitpid, exec.)

UNIT - II:
Real Time Operating Systems
Brief History of OS, Defining RTOS, The Scheduler, Objects, Services, Characteristics of RTOS, Defining a Task, asks States and Scheduling, Task Operations, Structure, Synchronization, Communication and Concurrency.
Defining Semaphores, Operations and Use, Defining Message Queue, States, Content, Storage, Operations and Use

UNIT - III:
Objects, Services and I/O
Pipes, Event Registers, Signals, Other Building Blocks, Component Configuration, Basic I/O Concepts, I/O Subsystem

UNIT - IV:
Exceptions, Interrupts and Timers
Exceptions, Interrupts, Applications, Processing of Exceptions and Spurious Interrupts, Real Time Clocks, Programmable Timers, Timer Interrupt Service Routines (ISR), Soft Timers, Operations.

UNIT - V:
Case Studies of RTOS
RT Linux, MicroC/OS-II, Vx Works, Embedded Linux, Tiny OS, and Basic Concepts of Android OS.

TEXT BOOK:
Real Time Concepts for Embedded Systems – Qing Li, Elsevier, 2011

REFERENCE BOOKS:
1. Embedded Systems- Architecture, Programming and Design by Rajkamal, 2007, TMH.
2. Advanced UNIX Programming, Richard Stevens
3. Embedded Linux: Hardware, Software and Interfacing – Dr. Craig Hollabaugh

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M. Tech – I Year – II Sem. (ES & VLSID/VLSI & ES/EDT)

LOW POWER VLSI DESIGN

UNIT –I:
Fundamentals:
Need for Low Power Circuit Design, Sources of Power Dissipation – Switching Power Dissipation, Short Circuit Power Dissipation, Leakage Power Dissipation, Glitching Power Dissipation, Short Channel Effects –Drain Induced Barrier Lowering and Punch Through, Surface Scattering, Velocity Saturation, Impact Ionization, Hot Electron Effect.

UNIT –II:
Low-Power Design Approaches:
Low-Power Design through Voltage Scaling – VTCMOS circuits, MTCMOS circuits, Architectural Level Approach –Pipelining and Parallel Processing Approaches.
Switched Capacitance Minimization Approaches:
System Level Measures, Circuit Level Measures, Mask level Measures.

UNIT –III:
Low-Voltage Low-Power Adders:
Introduction, Standard Adder Cells, CMOS Adder's Architectures – Ripple Carry Adders, Carry Look-Ahead Adders, Carry Select Adders, Carry Save Adders, Low-Voltage Low-Power Design Techniques –Trends of Technology and Power Supply Voltage, Low-Voltage Low-Power Logic Styles.

UNIT –IV:
Low-Voltage Low-Power Multipliers:
Introduction, Overview of Multiplication, Types of Multiplier Architectures, Braun Multiplier, Baugh-Wooley Multiplier, Booth Multiplier, Introduction to Wallace Tree Multiplier.

UNIT –V:
Low-Voltage Low-Power Memories:
Basics of ROM, Low-Power ROM Technology, Future Trend and Development of ROMs, Basics of SRAM, Memory Cell, Precharge and Equalization Circuit, Low-Power SRAM Technologies, Basics of DRAM, Self-Refresh Circuit, Future Trend and Development of DRAM.

TEXT BOOKS:

1. CMOS Digital Integrated Circuits – Analysis and Design – Sung-Mo Kang, Yusuf Leblebici, TMH, 2011.
2. Low-Voltage, Low-Power VLSI Subsystems – Kiat-Seng Yeo, Kaushik Roy, TMH Professional Engineering.

REFERENCE BOOKS:

1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRC Press, 2011
2. Low Power CMOS Design – AnanthaChandrakasan, IEEE Press/Wiley International, 1998.
3. Low Power CMOS VLSI Circuit Design – Kaushik Roy, Sharat C. Prasad, John Wiley & Sons, 2000.
4. Practical Low Power Digital VLSI Design – Gary K. Yeap, Kluwer Academic Press, 2002.
5. Low Power CMOS VLSI Circuit Design – A. Bellamour, M. I. Elamasri, Kluwer Academic Press, 1995.
6. Leakage in Nanometer CMOS Technologies – Siva G. Narendran, AnathaChandrakasan, Springer, 2005.

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CMOS MIXED SIGNAL CIRCUIT DESIGN

UNIT -I:
Switched Capacitor Circuits:
Introduction to Switched Capacitor circuits- basic building blocks, Operation and Analysis, Non-ideal effects in switched capacitor circuits, Switched capacitor integrators first order filters, Switch sharing, biquad filters.

UNIT -II:
Phased Lock Loop (PLL):
Basic PLL topology, Dynamics of simple PLL, Charge pump PLLs-Lock acquisition, Phase/Frequency detector and charge pump, Basic charge pump PLL, Non-ideal effects in PLLs-PFD/CP non-idealities, Jitter in PLLs, Delay locked loops, applications

UNIT -III:
Data Converter Fundamentals:
DC and dynamic specifications, Quantization noise, Nyquist rate D/A converters- Decoder based converters, Binary-Scaled converters, Thermometer-code converters, Hybrid converters

UNIT -IV:
Nyquist Rate A/D Converters:
Successive approximation converters, Flash converter, Two-step A/D converters, Interpolating A/D converters, Folding A/D converters, Pipelined A/D converters, Time-interleaved converters.

UNIT -V:
Oversampling Converters:
Noise shaping modulators, Decimating filters and interpolating filters, Higher order modulators, Delta sigma modulators with multibit quantizers, Delta sigma D/A

TEXT BOOKS:
1. Design of Analog CMOS Integrated Circuits- Behzad Razavi, TMH Edition, 2002
2. CMOS Analog Circuit Design - Philip E. Allen and Douglas R. Holberg, Oxford University Press, International Second Edition/Indian Edition, 2010.
3. Analog Integrated Circuit Design- David A. Johns, Ken Martin, Wiley Student Edition, 2013

REFERENCE BOOKS:
1. CMOS Integrated Analog-to- Digital and Digital-to-Analog converters-Rudy Van De Plassche, Kluwer Academic Publishers, 2003
2. Understanding Delta-Sigma Data converters-Richard Schreier, Wiley Interscience, 2005.
3. CMOS Mixed-Signal Circuit Design - R. Jacob Baker, Wiley Interscience, 2009.

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (ES & VLSID/VLSI & ES/EDT)

DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES
(Core Elective –III)

UNIT-I:
Introduction to Digital Signal Processing:
Introduction, A digital Signal – Processing system, the sampling process, Discrete time sequences, Discrete Fourier Transform (DFT) and Fast Fourier Transform (FFT), linear time-invariant systems, Digital filters, Decimation and interpolation.
Architectures for Programmable DSP devices:
Basic Architectural features, DSP computational building blocks, Bus Architecture and Memory, Data addressing capabilities, Address generation UNIT, programmability and program execution, speed issues, features for external interfacing.

UNIT-II:
Programmable Digital Signal Processors:
Commercial Digital signal-processing Devices, Data Addressing modes of TMS320C54XX processors, memory space of TMS320C54XX processors, program control, TMS320C54XX instructions and programming, On-Chip peripherals, Interrupts of TMS320C54XX processors, Pipeline operation of TMS320C54XX processors.

UNIT-III:
Architecture of ARM Processors:
Introduction to the architecture, Programmer's model- operation modes and states, registers, special registers, floating point registers, Behaviour of the application program status register(APSR)-Integer status flags, Q status flag, GE bits, Memory system-Memory system features, memory map, stack memory, memory protection unit (MPU), Exceptions and Interrupts-what are exceptions?, nested vectored interrupt controller(NVIC), vector table, Fault handling, System control block (SCB), Debug, Reset and reset sequence.
Technical Details of ARM Processors:
General information about Cortex-M3 and cortex M4 processors-Processor type, processor architecture, instruction set, block diagram, memory system, interrupt and exception support, Features of the cortex-M3 and Cortex-M4 Processors-Performance, code density, low power, memory system, memory protection unit, interrupt handling, OS support and system level features, Cortex-M4 specific features, Ease of use, Debug support, Scalability, Compatibility.

UNIT-IV:
Instruction SET:
Background to the instruction set in ARM Cortex-M Processors, Comparison of the instruction set in ARM Cortex-M Processors, understanding the assembly language syntax, Use of a suffix in instructions, Unified assembly Language (UAL), Instruction set, Cortex-M4-specific instructions, Barrel shifter, Accessing special instructions and special registers in Programming.

UNIT-V:
Floating Point Operations:
About Floating Point Data,Cortex-M4 Floating Point Unit (FPU)- overview, FP registers overview, CPACR register, Floating point register bank, FPSCR, FPU->FPCCR, FPU-> FPCAR, FPU->FPDSCR, FPU->MVFR0, FPU->MVFR1.

TEXTBOOKS:

1. Digital Signal Processing- Avtar Singh and S. Srinivasan, CENGAGE Learning, 2004.
2. The Definitive Guide to ARM Cortex-M3 and Cortex-M4 Processors by Joseph Yiu, Elsevier Publications, Third edition.

REFERENCES:

1. ARM System Developer's Guide Designing and Optimizing System Software by Andrew N. SLOSS, Dominic SYMES, Chris WRIGHT, Elsevier Publications, 2004.

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M. Tech – I Year – II Sem. (ES & VLSID/VLSI & ES/EDT)

SYSTEM ON CHIP ARCHITECTURE
(Core Elective –III)

UNIT –I:
Introduction to the System Approach:
System Architecture, Components of the system, Hardware & Software, Processor Architectures, Memory and Addressing. System level interconnection, An approach for SOC Design, System Architecture and Complexity.

UNIT –II:
Processors:
Introduction , Processor Selection for SOC, Basic concepts in Processor Architecture, Basic concepts in Processor Micro Architecture, Basic elements in Instruction handling. Buffers: minimizing Pipeline Delays, Branches, More Robust Processors, Vector Processors and Vector Instructions extensions, VLIW Processors, Superscalar Processors.

UNIT –III:
Memory Design for SOC:
Overview of SOC external memory, Internal Memory, Size, Scratchpads and Cache memory, Cache Organization, Cache data, Write Policies, Strategies for line replacement at miss time, Types of Cache, Split – I, and D – Caches, Multilevel Caches, Virtual to real translation , SOC Memory System, Models of Simple Processor – memory interaction.

UNIT -IV:
Interconnect Customization and Configuration:
Inter Connect Architectures, Bus: Basic Architectures, SOC Standard Buses , Analytic Bus Models, Using the Bus model, Effects of Bus transactions and contention time. SOC Customization: An overview, Customizing Instruction Processor, Reconfiguration Technologies, Mapping design onto Reconfigurable devices, Instance- Specific design, Customizable Soft Processor, Reconfiguration - overhead analysis and trade-off analysis on reconfigurable Parallelism.

UNIT –V:
Application Studies / Case Studies:
SOC Design approach, AES algorithms, Design and evaluation, Image compression – JPEG compression.

TEXT BOOKS:

1. Computer System Design System-on-Chip - Michael J. Flynn and Wayne Luk, Wiely India Pvt. Ltd.
2. ARM System on Chip Architecture – Steve Furber –2nd Ed., 2000, Addison Wesley Professional.

REFERENCE BOOKS:

1. Design of System on a Chip: Devices and Components – Ricardo Reis, 1st Ed., 2004, Springer
2. Co-Verification of Hardware and Software for ARM System on Chip Design (Embedded Technology) – Jason Andrews – Newnes, BK and CDROM.
3. System on Chip Verification – Methodologies and Techniques –Prakash Rashinkar, Peter Paterson and Leena Singh L, 2001, Kluwer Academic Publishers.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (ES & VLSID/VLSI & ES/EDT)

EMBEDDED NETWORKING

(Core Elective –III)

UNIT –I:

Embedded Communication Protocols:

Embedded Networking: Introduction – Serial/Parallel Communication – Serial communication protocols -RS232 standard – RS485 – Synchronous Serial Protocols -Serial Peripheral Interface (SPI) – Inter Integrated Circuits (I2C) – PC Parallel port programming - ISA/PCI Bus protocols – Firewire.

UNIT –II:

USB and CAN Bus:

USB bus – Introduction – Speed Identification on the bus – USB States – USB bus communication: Packets –Data flow types –Enumeration –Descriptors –PIC 18 Microcontroller USB Interface – C Programs –CAN Bus – Introduction - Frames –Bit stuffing –Types of errors –Nominal Bit Timing – PIC microcontroller CAN Interface –A simple application with CAN.

UNIT –III:

Ethernet Basics:

Elements of a network – Inside Ethernet – Building a Network: Hardware options – Cables, Connections and network speed – Design choices: Selecting components –Ethernet Controllers – Using the internet in local and internet communications – Inside the Internet protocol.

UNIT –IV:

Embedded Ethernet:

Exchanging messages using UDP and TCP – Serving web pages with Dynamic Data – Serving web pages that respond to user Input – Email for Embedded Systems – Using FTP – Keeping Devices and Network secure.

UNIT –V:

Wireless Embedded Networking:

Wireless sensor networks – Introduction – Applications – Network Topology – Localization –Time Synchronization - Energy efficient MAC protocols –SMAC – Energy efficient and robust routing – Data Centric routing.

TEXT BOOKS:

1. Embedded Systems Design: A Unified Hardware/Software Introduction - Frank Vahid, Tony Givargis, John & Wiley Publications, 2002
2. Parallel Port Complete: Programming, interfacing and using the PCs parallel printer port - Jan Axelson, Penram Publications, 1996.

REFERENCE BOOKS:

1. Advanced PIC microcontroller projects in C: from USB to RTOS with the PIC18F series - Dogan Ibrahim, Elsevier 2008.
2. Embedded Ethernet and Internet Complete - Jan Axelson, Penram publications, 2003.
3. Networking Wireless Sensors - Bhaskar Krishnamachari", Cambridge press 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech – I Year – II Sem. (ES & VLSID/VLSI & ES/EDT)

DESIGN FOR TESTABILITY

(Core Elective –IV)

UNIT -I:

Introduction to Testing:

Testing Philosophy, Role of Testing, Digital and Analog VLSI Testing, VLSI Technology Trends affecting Testing, Types of Testing, Fault Modeling: Defects, Errors and Faults, Functional Versus Structural Testing, Levels of Fault Models, Single Stuck-at Fault.

UNIT -II:

Logic and Fault Simulation:

Simulation for Design Verification and Test Evaluation, Modeling Circuits for Simulation, Algorithms for True-value Simulation, Algorithms for Fault Simulation, ATPG.

UNIT -III:

Testability Measures:

SCOAP Controllability and Observability, High Level Testability Measures, Digital DFT and Scan Design: Ad-Hoc DFT Methods, Scan Design, Partial-Scan Design, Variations of Scan.

UNIT -IV:

Built-In Self-Test:

The Economic Case for BIST, Random Logic BIST: Definitions, BIST Process, Pattern Generation, Response Compaction, Built-In Logic Block Observers, Test-Per-Clock, Test-Per-Scan BIST Systems, Circular Self Test Path System, Memory BIST, Delay Fault BIST.

UNIT -V:

Boundary Scan Standard:

Motivation, System Configuration with Boundary Scan: TAP Controller and Port, Boundary Scan Test Instructions, Pin Constraints of the Standard, Boundary Scan Description Language: BDSL Description Components, Pin Descriptions.

TEXT BOOK:

1. Essentials of Electronic Testing for Digital, Memory and Mixed Signal VLSI Circuits - M.L. Bushnell, V. D. Agrawal, Kluwer Academic Pulishers.

REFERENCE BOOKS:

1. Digital Systems and Testable Design - M. Abramovici, M.A.Breuer and A.D Friedman, Jaico Publishing House.
2. Digital Circuits Testing and Testability - P.K. Lala, Academic Press.

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (ES & VLSID/VLSI & ES/EDT)

SEMICONDUCTOR MEMORY DESIGN AND TESTING
(Core Elective –IV)

UNIT -I:
Random Access Memory Technologies:
SRAM – SRAM Cell structures, MOS SRAM Architecture, MOS SRAM cell and peripheral circuit operation, Bipolar SRAM technologies, SOI technology, Advanced SRAM architectures and technologies, Application specific SRAMs, DRAM – DRAM technology development, CMOS DRAM, DRAM cell theory and advanced cell structures, BICMOS DRAM, soft error failure in DRAM, Advanced DRAM design and architecture, Application specific DRAM

UNIT -II:
Non-volatile Memories:
Masked ROMs, High density ROM, PROM, Bipolar ROM, CMOS PROMS, EPROM, Floating gate EPROM cell, One time programmable EPROM, EEPROM, EEPROM technology and architecture, Non-volatile SRAM, Flash Memories (EPROM or EEPROM), advanced Flash memory architecture

UNIT -III:
Memory Fault Modeling Testing and Memory Design for Testability and Fault Tolerance: RAM fault modeling, Electrical testing, Pseudo Random testing, Megabit DRAM Testing, non-volatile memory modeling and testing, IDDQ fault modeling and testing, Application specific memory testing, RAM fault modeling, BIST techniques for memory

UNIT -IV:
Semiconductor Memory Reliability and Radiation Effects:
General reliability issues RAM failure modes and mechanism, Non-volatile memory reliability, reliability modeling and failure rate prediction, Design for Reliability, Reliability Test Structures, Reliability Screening and qualification, Radiation effects, Single Event Phenomenon (SEP), Radiation Hardening techniques, Radiation Hardening Process and Design Issues, Radiation Hardened Memory characteristics, Radiation Hardness Assurance and Testing, Radiation Dosimetry, Water Level Radiation Testing and Test structures

UNIT -V:
Advanced Memory Technologies and High-density Memory Packing Technologies: Ferroelectric RAMs (FRAMs), GaAs FRAMs, Analog memories, magneto resistive RAMs (MRAMs), Experimental memory devices, Memory Hybrids and MCMs (2D), Memory Stacks and MCMs (3D), Memory MCM testing and reliability issues, Memory cards, High Density Memory Packaging Future Directions

TEXT BOOKS:
1. Semiconductor Memories Technology – Ashok K. Sharma, 2002, Wiley.
2. Advanced Semiconductor Memories – Architecture, Design and Applications - Ashok K. Sharma- 2002, Wiley.
3. Modern Semiconductor Devices for Integrated Circuits – Chenming C Hu, 1st Ed., Prentice Hall.

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech – I Year – II Sem. (ES & VLSID/VLSI & ES/EDT)

FULL CUSTOM DESIGN
(Core Elective –IV)

Unit I
Introduction: Schematic fundamentals, Layout design, Introduction to CMOS VLSI manufacturing processes, Layers and connectivity, Process design rules Significance of full custom IC design, layout design flows.

Unit II
Advanced techniques for specialized building blocks Standard cell libraries, Pad cells and Laser fuse cells, Advanced techniques for building blocks, Power grid Clock signals and

Unit III
Interconnect routing. Interconnect layout design, Special electrical requirements, Layout design techniques to address electrical characteristics.

Unit IV
Layout considerations due to process constraints Large metal via implementations, Step coverage rules, Special design rules, Latch-up and Guard rings, Constructing the pad ring, Minimizing Stress effects.

Unit V
Proper layout CAD tools for layout, Planning tools, Layout generation tools, Support tools.

TEXT BOOKS:
1. CMOS IC Layout Concepts Methodologies and Tools, Dan Clein, Newnes, 2000.
2. The Art of Analog Layout, 2nd Edition, Ray Alan Hastings, Prentice Hall, 2006

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. (ELECTRONICS AND COMMUNICATION ENGINEERING/ DIGITAL ELECTRONICS AND COMMUNICATION ENGINEERING/ DIGITAL ELECTRONICS AND COMMUNICATION SYSTEMS)
COURSE STRUCTURE AND SYLLABUS

I Year – I Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course I	Digital System Design	25	75	4	--	4
Core Course II	VLSI Technology and Design	25	75	4	--	4
Core Course III	Advanced Data Communications	25	75	4	--	4
Core Elective I	Detection and Estimation Theory Embedded System Design Advanced Digital Signal Processing	25	75	4	--	4
Core Elective II	Internetworking Advanced Computer Architecture Real Time Operating Systems	25	75	4	--	4
Open Elective I	Spread Spectrum Communication Advanced Communications and Networks Virtual instrumentation	25	75	4	--	4
Laboratory I	Digital Signal Processing Lab	25	75	--	4	2
Seminar I	Seminar	50	--	--	4	2
	Total Credits			24	8	28

I Year – II Semester

Category	Course Title	Int. marks	Ext. marks	L	P	C
Core Course IV	Coding Theory and Techniques	25	75	4	--	4
Core Course V	Digital Signal Processors and Architectures	25	75	4	--	4
Core Course VI	Wireless Communications and Networks	25	75	4	--	4
Core Elective III	Speech Signal Processing Biomedical Signal Processing Radar Signal Processing	25	75	4	--	4
Core Elective IV	Network Security And Cryptography Wireless MIMO Communications Optical Communications Technology	25	75	4	--	4
Open Elective II	Image and Video Processing Software Defined Radio Adhoc Wireless Networks	25	75	4	--	4
Laboratory II	Advanced Communications Lab	25	75	--	4	2
Seminar II	Seminar	50	--	--	4	2
Total Credits				24	8	28

II Year - I Semester

Course Title	Int. marks	Ext. marks	L	P	C
Comprehensive Viva-Voce	--	100	--	--	4
Project work Review I	50	--	--	24	12
Total Credits			--	24	16

II Year - II Semester

Course Title	Int. marks	Ext. marks	L	P	C
Project work Review II	50	--	--	8	4
Project Evaluation (Viva-Voce)	--	150	--	16	12
Total Credits			--	24	16

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. – I Year –I Sem (ECE /DECE/DECS)

DIGITAL SYSTEM DESIGN

UNIT -I:
Minimization and Transformation of Sequential Machines: The Finite State Model – Capabilities and limitations of FSM – State equivalence and machine minimization – Simplification of incompletely specified machines.
Fundamental mode model – Flow table – State reduction – Minimal closed covers – Races, Cycles and Hazards.

UNIT -II:
Digital Design: Digital Design Using ROMs, PALs and PLAs, BCD Adder, 32 – bit adder, State graphs for control circuits, Scoreboard and Controller, A shift and add multiplier, Array multiplier, Keypad Scanner, Binary divider.

UNIT -III:
SM Charts: State machine charts, Derivation of SM Charts, Realization of SM Chart, Implementation of Binary Multiplier, dice game controller.

UNIT -IV:
Fault Modeling & Test Pattern Generation: Logic Fault model – Fault detection & Redundancy- Fault equivalence and fault location –Fault dominance – Single stuck at fault model – Multiple stuck at fault models –Bridging fault model.
Fault diagnosis of combinational circuits by conventional methods – Path sensitization techniques, Boolean Difference method – Kohavi algorithm – Test algorithms – D algorithm, PODEM, Random testing, Transition count testing, Signature analysis and test bridging faults.

UNIT - V:
Fault Diagnosis in Sequential Circuits: Circuit Test Approach, Transition Check Approach – State identification and fault detection experiment, Machine identification, Design of fault detection experiment

TEXT BOOKS:

1. Fundamentals of Logic Design – Charles H. Roth, 5th Ed., Cengage Learning.
2. Digital Systems Testing and Testable Design – Miron Abramovici, Melvin A. Breuer and Arthur D. Friedman- John Wiley & Sons Inc.
3. Logic Design Theory – N. N. Biswas, PHI

REFERENCE BOOKS:

1. Switching and Finite Automata Theory – Z. Kohavi , 2nd Ed., 2001, TMH
2. Digital Design – Morris Mano, M.D.Ciletti, 4th Edition, PHI.
3. Digital Circuits and Logic Design – Samuel C. Lee , PHI

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. – I Year –I Sem (ECE /DECE/DECS)

VLSI TECHNOLOGY AND DESIGN

UNIT –I:
Review of Microelectronics and Introduction to MOS Technologies:
MOS, CMOS, BiCMOS Technology. Basic Electrical Properties of MOS, CMOS & BiCMOS Circuits: Ids – Vds relationships, Threshold Voltage VT, Gm, Gds and ωo, Pass Transistor, MOS, CMOS & Bi CMOS Inverters, Zpu/Zpd, MOS Transistor circuit model, Latch-up in CMOS circuits.

UNIT –II:
Layout Design and Tools:
Transistor structures, Wires and Vias, Scalable Design rules, Layout Design tools.
Logic Gates & Layouts:
Static Complementary Gates, Switch Logic, Alternative Gate circuits, Low power gates, Resistive and Inductive interconnect delays.

UNIT –III:
Combinational Logic Networks:
Layouts, Simulation, Network delay, Interconnect design, Power optimization, Switch logic networks, Gate and Network testing.

UNIT –IV:
Sequential Systems:
Memory cells and Arrays, Clocking disciplines, Design, Power optimization, Design validation and testing.

UNIT –V:
Floor Planning:
Floor planning methods, Global Interconnect, Floor Plan Design, Off-chip connections.

TEXT BOOKS:

1. Essentials of VLSI Circuits and Systems, K. Eshraghian Eshraghian. D, A. Pucknell, 2005, PHI.
2. Modern VLSI Design – Wayne Wolf, 3rd Ed., 1997, Pearson Education.

REFERENCE BOOKS:

1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRC Press, 2011.
2. Principals of CMOS VLSI Design – N.H.E Weste, K. Eshraghian, 2nd Ed., Addison Wesley.

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. – I Year –I Sem (ECE /DECE/DECS)

ADVANCED DATA COMMUNICATIONS

Unit I

Data Communications, Networks and Network Types, Internet History, Standards and Administration, Protocol Layering, TCP/IP protocol suite, OSI Model. Digital Data Transmission, DTE-DCE interface.

Data Link Layer

Introduction, Data Link Layer, Nodes and Links, Services, Categories of Links, sub layers, Link Layer Addressing, Address Resolution Protocol.

Unit II

Error Detection and Correction: Types of Errors, Redundancy, detection versus correction, Coding Block Coding: Error Detection, Vertical redundancy cheeks, longitudinal redundancy cheeks, Error Correction, Error correction single bit, Hamming code.

Cyclic Codes: Cyclic Redundancy Check, Polynomials, Cyclic Code Encoder Using Polynomials, Cyclic Code Analysis, Advantage of Cyclic Codes, Checksum

Data Link Control: DLC Services, Data Link Layer Protocols, HDLC, Point to Point Protocol

Unit III

Switching: Introduction to Switching, Circuit Switched Networks, Packet Switching, Structure of switch

Multiplexing : Multiplexing, Frequency Division Multiplexing, Time Division Multiplexing.

Connecting devices: Passive Hubs, Repeaters, Active Hubs, Bridges, Two Layer Switches, Routers, Three Layer Switches, Gateway, Backbone Networks.

Wired LANS: Ethernet Protocol, Standard Ethernet, Fast Ethernet, Gigabit Ethernet, 10 Giga bit Ethernet

Unit IV

Media Access Control (MAC) Sub Layer

Random Access, ALOHA, Carrier Sense Multiple Access (CSMA), Carrier Sense Multiple Access with Collision Detection (CSMA/CD), Carrier Sense Multiple Access with Collision Avoidance (CSMA/CA),Controlled Access- Reservation, Polling- Token Passing, Channelization - Frequency Division Multiple Access (FDMA), Time - Division Multiple Access (TDMA), Code - Division Multiple Access (CDMA).

Spectrum Spreading: Spread Spectrum-Frequency Hopping Spread Spectrum and Direct Sequence Spread Spectrum.

Unit V

Networks Layer: Packetizing, Routing and Forwarding, Packet Switching, Network Layer Performance, IPv4 Address, Address Space, Classful Addressing, Classless Addressing, Dynamic Host Configuration Protocol (DHCP), Network Address Resolution(NATF), Forwarding of IP Packets, Forwarding based on Destination Address, Forwarding based on Label, Routing as Packet Switches.

Unicast Routing : Introduction, **Routing Algorithms**-Distance Vector Routing, Link State Routing, Path Vector Routing, **Unicast Routing Protocols**- Routing Information Protocol(RIP), Open Short Path First Version 4.

TEXT BOOKS:

1. Data Communications and Networking - B. A. Forouzan, 5^{th} , 2013,TMH.
2. Data and Computer Communications - William Stallings, 8^{th} ed., 2007, PHI.

REFERENCE BOOKS:

1. Data Communications and Computer Networks - Prakash C. Gupta, 2006, PHI.
2. Data Communications and Networking - B. A. Forouzan, 2^{nd} , 2013, TMH.
3. Data Communications and Computer Networks- Brijendra Singh, 2^{nd} ed., 2005, PHI.

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. – I Year –I Sem (ECE /DECE/DECS)

DETECTION AND ESTIMATION THEORY
(Core Elective -I)

UNIT –I:

Random Processes: Discrete Linear Models, Markov Sequences and Processes, Point Processes, and Gaussian Processes.

UNIT –II:

Detection Theory: Basic Detection Problem, Maximum A posteriori Decision Rule, Minimum Probability of Error Classifier, Bayes Decision Rule, Multiple-Class Problem (Bayes)- minimum probability error with and without equal a priori probabilities, Neyman-Pearson Classifier, General Calculation of Probability of Error, General Gaussian Problem, Composite Hypotheses.

UNIT –III:

Linear Minimum Mean-Square Error Filtering: Linear Minimum Mean Squared Error Estimators, Nonlinear Minimum Mean Squared Error Estimators. Innovations, Digital Wiener Filters with Stored Data, Real-time Digital Wiener Filters, Kalman Filters.

UNIT –IV:

Statistics: Measurements, Nonparametric Estimators of Probability Distribution and Density Functions, Point Estimators of Parameters, Measures of the Quality of Estimators, Introduction to Interval Estimates, Distribution of Estimators, Tests of Hypotheses, Simple Linear Regression, Multiple Linear Regression.

UNIT –V:

Estimating the Parameters of Random Processes from Data: Tests for Stationarity and Ergodicity, Model-free Estimation, Model-based Estimation of Autocorrelation Functions, Power Special Density Functions.

TEXT BOOKS:

1. Random Signals: Detection, Estimation and Data Analysis - K. Sam Shanmugan & A.M. Breipohl, Wiley India Pvt. Ltd, 2011.
2. Random Processes: Filtering, Estimation and Detection - Lonnie C. Ludeman, Wiley India Pvt. Ltd., 2010.

REFERENCE BOOKS:

1. Fundamentals of Statistical Signal Processing: Volume I Estimation Theory– Steven.M.Kay, Prentice Hall, USA, 1998.
2. Fundamentals of Statistical Signal Processing: Volume I Detection Theory– Steven.M.Kay, Prentice Hall, USA, 1998.
3. Introduction to Statistical Signal Processing with Applications - Srinath, Rajasekaran, Viswanathan, 2003, PHI.
4. Statistical Signal Processing: Detection, Estimation and Time Series Analysis – Louis L.Scharf, 1991, Addison Wesley.
5. Detection, Estimation and Modulation Theory: Part – I – Harry L. Van Trees, 2001, John Wiley & Sons, USA.
6. Signal Processing: Discrete Spectral Analysis – Detection & Estimation – Mischa Schwartz, Leonard Shaw, 1975, Mc Graw Hill.

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. – I Year –I Sem (ECE /DECE/DECS)

EMBEDDED SYSTEM DESIGN
(Core Elective -I)

UNIT -I:
Introduction to Embedded Systems
Definition of Embedded System, Embedded Systems Vs General Computing Systems, History of Embedded Systems, Classification, Major Application Areas, Purpose of Embedded Systems, Characteristics and Quality Attributes of Embedded Systems.

UNIT -II:
Typical Embedded System:
Core of the Embedded System: General Purpose and Domain Specific Processors, ASICs, PLDs, Commercial Off-The-Shelf Components (COTS), Memory: ROM, RAM, Memory according to the type of Interface, Memory Shadowing, Memory selection for Embedded Systems, Sensors and Actuators, Communication Interface: Onboard and External Communication Interfaces.

UNIT -III:
Embedded Firmware:
Reset Circuit, Brown-out Protection Circuit, Oscillator Unit, Real Time Clock, Watchdog Timer, Embedded Firmware Design Approaches and Development Languages.

UNIT -IV:
RTOS Based Embedded System Design:
Operating System Basics, Types of Operating Systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Task Scheduling.

UNIT -V:
Task Communication: Shared Memory, Message Passing, Remote Procedure Call and Sockets, Task Synchronization: Task Communication/Synchronization Issues, Task Synchronization Techniques, Device Drivers, How to Choose an RTOS.

TEXT BOOKS:
1. Introduction to Embedded Systems - Shibu K.V, Mc Graw Hill.

REFERENCE BOOKS:
1. Embedded Systems - Raj Kamal, TMH.
2. Embedded System Design - Frank Vahid, Tony Givargis, John Wiley.
3. Embedded Systems – Lyla, Pearson, 2013
4. An Embedded Software Primer - David E. Simon, Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. – I Year –I Sem (ECE /DECE/DECS)

ADVANCED DIGITAL SIGNAL PROCESSING
(Core Elective -I)

UNIT –I:
Review of DFT, FFT, IIR Filters and FIR Filters: Multi Rate Signal Processing: Introduction, Decimation by a factor D, Interpolation by a factor I, Sampling rate conversion by a rational factor I/D, Multistage Implementation of Sampling Rate Conversion, Filter design & Implementation for sampling rate conversion.

UNIT –II:
Applications of Multi Rate Signal Processing: Design of Phase Shifters, Interfacing of Digital Systems with Different Sampling Rates, Implementation of Narrow Band Low Pass Filters, Implementation of Digital Filter Banks, Sub-band Coding of Speech Signals, Quadrature Mirror Filters, Trans-multiplexers, Over Sampling A/D and D/A Conversion.

UNIT -III:
Non-Parametric Methods of Power Spectral Estimation: Estimation of spectra from finite duration observation of signals, Non-parametric Methods: Bartlett, Welch & Blackman-Tukey methods, Comparison of all Non-Parametric methods

UNIT –IV:
Implementation of Digital Filters: Introduction to filter structures (IIR & FIR), Frequency sampling structures of FIR, Lattice structures, Forward prediction error, Backward prediction error, Reflection coefficients for lattice realization, Implementation of lattice structures for IIR filters, Advantages of lattice structures.

UNIT –V:
Parametric Methods of Power Spectrum Estimation: Autocorrelation & Its Properties, Relation between auto correlation & model parameters, AR Models - Yule-Walker & Burg Methods, MA & ARMA models for power spectrum estimation, Finite word length effect in IIR digital Filters – Finite word-length effects in FFT algorithms.

TEXT BOOKS:
1. Digital Signal Processing: Principles, Algorithms & Applications - J.G.Proakis & D. G. Manolakis, 4th Ed., PHI.
2. Discrete Time Signal Processing - Alan V Oppenheim & R. W Schaffer, PHI.
3. DSP – A Practical Approach – Emmanuel C. Ifeacher, Barrie. W. Jervis, 2 Ed., Pearson Education.

REFERENCE BOOKS:
1. Modern Spectral Estimation: Theory & Application – S. M .Kay, 1988, PHI.
2. Multi Rate Systems and Filter Banks – P.P.Vaidyanathan – Pearson Education.
3. Digital Signal Processing – S.Salivahanan, A.Vallavaraj, C.Gnanapriya, 2000,TMH
4. Digital Spectral Analysis – Jr. Marple

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. Tech. – I Year –I Sem (ECE /DECE/DECS)

INTERNETWORKING
(Core Elective –II)

UNIT -I:

Internetworking Concepts: Principles of Internetworking, Connectionless Internetworking, Application level Interconnections, Network level Interconnection, Properties of the Internet, Internet Architecture, Wired LANS, Wireless LANs, Point-to-Point WANs, Switched WANs, Connecting Devices, TCP/IP Protocol Suite.

IP Address: Classful Addressing: Introduction, Classful Addressing, Other Issues, Sub-netting and Super-netting

Classless Addressing: Variable length Blocks, Sub-netting, Address Allocation. Delivery, Forwarding, and Routing of IP Packets: Delivery, Forwarding, Routing, Structure of Router.

ARP and RARP: ARP, ARP Package, RARP.

UNIT -II:

Internet Protocol (IP): Datagram, Fragmentation, Options, Checksum, IP V.6.

Transmission Control Protocol (TCP): TCP Services, TCP Features, Segment, A TCP Connection, State Transition Diagram, Flow Control, Error Control, Congestion Control, TCP Times.

Stream Control Transmission Protocol (SCTP): SCTP Services, SCTP Features, Packet Format, Flow Control, Error Control, Congestion Control.

Mobile IP: Addressing, Agents, Three Phases, Inefficiency in Mobile IP.

Classical TCP Improvements: Indirect TCP, Snooping TCP, Mobile TCP, Fast Retransmit/ Fast Recovery, Transmission/ Time Out Freezing, Selective Retransmission, Transaction Oriented TCP.

UNIT -III:

Unicast Routing Protocols (RIP, OSPF, and BGP: Intra and Inter-domain Routing, Distance Vector Routing, RIP, Link State Routing, OSPF, Path Vector Routing, BGP.

Multicasting and Multicast Routing Protocols: Unicast - Multicast- Broadcast, Multicast Applications, Multicast Routing, Multicast Link State Routing: MOSPF, Multicast Distance Vector: DVMRP.

UNIT -IV:

Domain Name System (DNS): Name Space, Domain Name Space, Distribution of Name Space, and DNS in the internet.

Remote Login TELNET: Concept, Network Virtual Terminal (NVT).

File Transfer FTP and TFTP: File Transfer Protocol (FTP).

Electronic Mail: SMTP and POP.

Network Management-SNMP: Concept, Management Components, World Wide Web- HTTP Architecture.

UNIT -V:

Multimedia: Digitizing Audio and Video, Network security, security in the internet firewalls. Audio and Video Compression, Streaming Stored Audio/Video, Streaming Live Audio/Video, Real-Time Interactive Audio/Video, RTP, RTCP, Voice Over IP. Network Security, Security in the Internet, Firewalls.

TEXT BOOKS:

1. TCP/IP Protocol Suite- Behrouz A. Forouzan, Third Edition, TMH
2. Internetworking with TCP/IP Comer 6th Edition PHI, Volume -1.

REFERENCE BOOKS:

1. Mobile Communications, Jochen Schiller, 2nd edition, Pearson Education 2003.
2. Data Communications & Networking – B.A. Forouzan – 4th Edition – TMH
3. High Speed Networks and Internets- William Stallings, Pearson Education, 2002.
4. Data and Computer Communications, William Stallings, 7th Edition., PEI.
5. The Internet and Its Protocols – Adrin Farrel, Elsevier, 2005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. – I Year –I Sem (ECE /DECE/DECS)

ADVANCED COMPUTER ARCHITECTURE
(Core Elective –II)

UNIT -I:
Fundamentals of Computer Design: Fundamentals of Computer design, Changing faces of computing and task of computer designer, Technology trends, Cost price and their trends, Measuring and reporting performance, Quantitative principles of computer design, Amdahl's law.
Instruction set principles and examples- Introduction, Classifying instruction set- MEmory addressing- type and size of operands, Operations in the instruction set.

UNIT –II:
Pipelines: Introduction, Basic RISC instruction set, Simple implementation of RISC instruction set, Classic five stage pipe lined RISC processor, Basic performance issues in pipelining, Pipeline hazards, Reducing pipeline branch penalties.
Memory Hierarchy Design: Introduction, Review of ABC of cache, Cache performance, Reducing cache miss penalty, Virtual memory.

UNIT -III:
Instruction Level Parallelism the Hardware Approach: Instruction-Level parallelism, Dynamic scheduling, Dynamic scheduling using Tomasulo's approach, Branch prediction, high performance instruction delivery- hardware based speculation.
ILP Software Approach : Basic compiler level techniques, Static branch prediction, VLIW approach, Exploiting ILP, Parallelism at compile time, Cross cutting issues -Hardware verses Software.

UNIT –IV:
Multi Processors and Thread Level Parallelism: Multi Processors and Thread level Parallelism- Introduction, Characteristics of application domain, Systematic shared memory architecture, Distributed shared – memory architecture, Synchronization.

UNIT –V:
Inter Connection and Networks: Introduction, Interconnection network media, Practical issues in interconnecting networks, Examples of inter connection, Cluster, Designing of clusters.
Intel Architecture: Intel IA-64 ILP in embedded and mobile markets Fallacies and pit falls.

TEXT BOOKS:

1. John L. Hennessy, David A. Patterson - Computer Architecture: A Quantitative Approach, 3rd Edition, An Imprint of Elsevier.

REFERENCE BOOKS:

1. John P. Shen and Miikko H. Lipasti - Modern Processor Design : Fundamentals of Super Scalar Processors
2. Computer Architecture and Parallel Processing - Kai Hwang, Faye A.Brigs., MC Graw Hill.
3. Advanced Computer Architecture - A Design Space Approach - Dezso Sima, Terence Fountain, Peter Kacsuk , Pearson Ed.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. – I Year –I Sem (ECE /DECE/DECS)

REAL TIME OPERATING SYSTEMS
(Core Elective –II)

UNIT – I
Introduction: Introduction to UNIX/LINUX, Overview of Commands, File I/O,(open, create, close, lseek, read, write), Process Control (fork, vfork, exit, wait, waitpid, exec).

UNIT -II:
Real Time Operating Systems: Brief History of OS, Defining RTOS, The Scheduler, Objects, Services, Characteristics of RTOS, Defining a Task, asks States and Scheduling, Task Operations, Structure, Synchronization, Communication and Concurrency.
Defining Semaphores, Operations and Use, Defining Message Queue, States, Content, Storage, Operations and Use

UNIT -III:
Objects, Services and I/O: Pipes, Event Registers, Signals, Other Building Blocks, Component Configuration, Basic I/O Concepts, I/O Subsystem

UNIT -IV:
Exceptions, Interrupts and Timers: Exceptions, Interrupts, Applications, Processing of Exceptions and Spurious Interrupts, Real Time Clocks, Programmable Timers, Timer Interrupt Service Routines (ISR), Soft Timers, Operations.

UNIT -V:
Case Studies of RTOS: RT Linux, MicroC/OS-II, Vx Works, Embedded Linux, and Tiny OS, and Android OS.

TEXT BOOKS:

1. Real Time Concepts for Embedded Systems – Qing Li, Elsevier, 2011

REFERENCE BOOKS:

1. Embedded Systems- Architecture, Programming and Design - Rajkamal, 2007, TMH.
2. Advanced UNIX Programming - Richard Stevens
3. Embedded Linux: Hardware, Software and Interfacing – Dr. Craig Hollabaugh

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. – I Year –I Sem (ECE /DECE/DECS)

SPREAD SPECTRUM COMMUNICATION
(Open Elective I)

UNIT -I:
Introduction to Spread Spectrum Systems: Fundamental Concepts of Spread Spectrum Systems, Pseudo Noise Sequences, Direct Sequence Spread Spectrum, Frequency Hop Spread Spectrum, Hybrid Direct Sequence Frequency Hop Spread Spectrum, Code Division Multiple Access.
Binary Shift Register Sequences for Spread Spectrum Systems:
Introduction, Definitions, Mathematical Background and Sequence Generator Fundamentals, Maximal Length Sequences, Gold Codes.

UNIT -II:
Code Tracking Loops: Introduction, Optimum Tracking of Wideband Signals, Base Band Delay-Lock Tracking Loop, Tau-Dither Non- Coherent Tracking Loop, Double Dither Non-Coherent Tracking Loop.

UNIT -III:
Initial Synchronization of the Receiver Spreading Code: Introduction, Problem Definition and the Optimum Synchronizer, Serial Search Synchronization Techniques, Synchronization using a Matched Filter, Synchronization by Estimated the Received Spreading Code.

UNIT -IV:
Cellular Code Division Multiple Access (CDMA) Principles: Introduction, Wide Band Mobile Channel, The Cellular CDMA System, Single User Receiver in a Multi User Channel, CDMA System Capacity,
Multi-User Detection in CDMA Cellular Radio: Optimal Multi-User Detection, Linear Suboptimal Detectors, Interference Combat Detection Schemes, Interference Cancellation Techniques.

UNIT -V:
Performance of Spread Spectrum Systems in Jamming Environments: Spread Spectrum Communication System Model, Performance of Spread Spectrum Systems without Coding.
Performance of Spread Spectrum Systems with Forward Error Correction: Elementary Block Coding Concepts, Optimum Decoding Rule, Calculation of Error Probability, Elementary Convolution Coding Concepts, Viterbi Algorithm, Decoding and Bit-Error Rate.

TEXT BOOKS:
1. Rodger E Ziemer, Roger L. Peterson and David E Borth - "Introduction to Spread Spectrum Communication- Pearson, 1st Edition, 1995.
2. Mosa Ali Abu-Rgheff – "Introduction to CDMA Wireless Communications." Elsevier Publications, 2008.

REFERENCE BOOKS:
1. George R. Cooper, Clare D. Mc Gillem - "Modern Communication and Spread Spectrum," McGraw Hill, 1986.
2. Andrew j. Viterbi - "CDMA: Principles of spread spectrum communication," Pearson Education, 1st Edition, 1995.
3. Kamilo Feher - "Wireless Digital Communications," PHI, 2009.
4. Andrew Richardson - "WCDMA Design Handbook," Cambridge University Press, 2005.
5. Steve Lee - Spread Spectrum CDMA, McGraw Hill, 2002.

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. – I Year –I Sem (ECE /DECE/DECS)

ADVANCED COMMUNICATIONS AND NETWORKS
(Open Elective I)

UNIT I

Spread Spectrum Communications

Spreading sequences- Properties of Spreading Sequences, Pseudo- noise sequence, Gold sequences, Kasami sequences, Walsh Sequences, Orthogonal Variable Spreading Factor Sequences, Barker Sequence, Complementary Codes

Direct sequence spread spectrum – DS-CDMA Model, Conventional receiver, Rake Receiver, Synchronization in CDMA, Power Control, Soft handoff, Multiuser detection – Optimum multiuser detector, Liner multiuser detection.

UNIT II

Orthogonal Frequency Division Multiplexing

Basic Principles of Orthogonality, Single vs Multicarrier Systems, OFDM Block Diagram and Its Explanation, OFDM Signal Mathematical Representation, Selection parameter for Modulation, Pulse shaping in OFDM Signal and Spectral Efficiency, Window in OFDM Signal and Spectrum, Synchronization in OFDM, Pilot Insert in OFDM Transmission and Channel Estimation, Amplitude Limitations in OFDM, FFT Point Selection Constraints in OFDM, CDMA vs OFDM, Hybrid OFDM.

UNIT III

MIMO Systems

Introduction, Space Diversity and System Based on Space Diversity, Smart Antenna system and MIMO, MIMO Based System Architecture, MIMO Exploits Multipath, Space – Time Processing, Antenna Consideration for MIMO, MIMO Channel Modelling, MIMO Channel Measurement, MIMO Channel Capacity, Cyclic Delay Diversity (CDD), Space Time Coding, Advantages and Applications of MIMO in Present Context, MIMO Applications in 3G Wireless System and Beyond, MIMO-OFDM

UNIT IV

Wireless LANs/IEEE 802.11x: Introduction to IEEE802.11x Technologies, Evolution of wireless LANs, IEEE 802.11 Design Issues, IEEE 802.11 Services, IEEE 802.11 MAC Layer operations, IEEE 802.11 Layer1, IEEE 802.11 a/b/g Higher Rate Standards, Wireless LAN Security, Computing Wireless Technologies, Typical WLAN Hardware

UNIT V

Wireless PANs/IEEE 802.15x: Introduction to IEEE 802.15x Technologies,

Wireless PAN Applications and Architecture, IEEE 802.15.1 Physical Layer Details, Bluetooth Link Controllers Basics, Bluetooth Link Controllers Operational States, IEEE 802.15.1 Protocols and Host Control Interface, Evaluation of IEEE 802.15 Standards

Broad Band Wireless MANs/IEEE 802.16x

Introduction to WMAN/IEEE 802.16x Technology, IEEE 802.16Wireless MANs, IEEE 802.16 MAC Layer Details, IEEE 802.16 Physical Layer Details, IEEE 802.16 Physical Layer Details for 2-11 GHz, IEEE 802.16 Common System Operations.

Text Books

1. Introduction to Wireless Telecommunications Systems and Networks – Gary J. Mullett, CENGAGE
2. Wireless Communication-Upena Dalal, Oxford University Press, 2009

References

1. Wireless Communication System –Ke-Lin Du & M N S Swamy, Cambridge University Press, 2010
2. Mobile Cellular Communication - Gottapu Sasibhusan Rao, PEARSON

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. – I Year –I Sem (ECE /DECE/DECS)

VIRTUAL INSTRUMENTATION

UNIT- I
Virtual Instrumentation: An introduction
Historical perspective, advantages, block diagram and architecture of a virtual instrument, data-flow techniques, graphical programming in data flow, comparison with conventional programming. Development of Virtual Instrument using GUI,
Real-time systems.

UNIT- II
VI programming techniques:
VIs and sub-VIs, loops and charts, arrays, clusters and graphs, case and sequence structures, formula nodes, local and global variables, string and file I/O, Instrument Drivers, Publishing measurement data in the web.

UNIT- III
Data acquisition basics:
Introduction to data acquisition on PC, Sampling fundamentals, Input/Output techniques and buses. ADC, DAC, Digital I/O, counters and timers, DMA, Software and hardware installation, Calibration, Resolution, Data acquisition interface requirements.

UNIT -IV
VI Interface requirements:
Common Instrument Interfaces: Current loop, RS 232C/ RS485, GPIB. Bus Interfaces: USB, PCMCIA, VXI, SCSI, PCI, PXI, Firewire. PXI system controllers, Ethernet control of PXI. Networking basics for office & Industrial applications, VISA and IVI.

UNIT- V
VI toolsets:
Distributed I/O modules. Application of Virtual Instrumentation: Instrument Control, Development of process database management system, Simulation of systems using VI, Development of Control system, Industrial Communication, Image acquisition and processing, Motion control.

TEXTBOOKS
1. LabVIEW Graphical Programming , Gary Johnson, Second edition, McGraw Hill, Newyork, 1997.
2. LabVIEW based Advanced Instrumentation Systems, S. Sumathi and P. Surekha, Spinger.

REFERENCES
1. PC Interfacing and Data Acquisition: Techniques for Measurement, Instrumentation and Control, Kevin James, Newnes, 2000.
2. WEB RESOURCES: www.ni.com.
3.LabVIEW for everyone, Lisa K. wells & Jeffrey Travis Prentice Hall, New Jersey, 1997.

R17 - Regulation

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH IN EMBEDDED SYSTEMS & VLSI DESIGN/VLSI AND EMBEDDED SYSTEMS/ ELECTRONICS DESIGN TECHNOLOGY.

A.C- 2017-18

EFFECTIVE FROM ACADEMIC YEAR 2017- 18 ADMITTED BATCH

COURSE STRUCTURE AND SYLLABUS

Sem → I & II

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Embedded System Design	25	75	4	0	0	4
PC-2	VLSI Technology	25	75	4	0	0	4
PC-3	CMOS Analog Integrated Circuit Design	25	75	4	0	0	4
PE-1	Hardware Software Co-Design Digital System Design Advanced Computer Architecture	25	75	3	0	0	3
PE-2	VLSI DSP Architectures CMOS Digital Integrated Circuit Design CPLD and FPGA Architectures and Applicat	25	75	3	0	0	3
OE-1	*Open Elective – I	25	75	3	0	0	3
Laboratory I	VLSI Laboratory	25	75	0	0	3	2
Seminar I	Seminar	100	0	0	0	3	2
	Total	**275**	**525**	**21**	**0**	**6**	**25**

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Low Power VLSI Design	25	75	4	0	0	4
PC-5	CMOS Mixed Signal Circuit Design	25	75	4	0	0	4
PC-6	Real Time Operating Systems	25	75	4	0	0	4
PE-3	Advanced Digital Signal Processing System On Chip Architecture Embedded Networking	25	75	3	0	0	3
PE4	Design for Testability Physical Design Automation Scripting Languages	25	75	3	0	0	3
OE-2	*Open Elective – II	25	75	3	0	0	3
Laboratory II	Embedded Systems Laboratory	25	75	0	0	3	2
Seminar II	Seminar	100	0	0	0	3	2
	Total	**275**	**525**	**21**	**0**	**6**	**25**

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III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by OTHER departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI AND EMBEDDED SYSTEMS/ ELECTRONICS DESIGN TECHNOLOGY.

EMBEDDED SYSTEM DESIGN (PC-1)

UNIT -I
Introduction to Embedded Systems: Definition of Embedded System, Embedded Systems Vs General Computing Systems, History of Embedded Systems, Classification, Major Application Areas, Purpose of Embedded Systems, Characteristics and Quality Attributes of Embedded Systems.

UNIT -II
Typical Embedded System: Core of the Embedded System: General Purpose and Domain Specific Processors, ASICs, PLDs, Commercial Off-The-Shelf Components (COTS), Memory: ROM, RAM, Memory according to the type of Interface, Memory Shadowing, Memory selection for Embedded Systems, Sensors and Actuators, Communication Interface: Onboard and External Communication Interfaces.

UNIT -III
Embedded Firmware: Reset Circuit, Brown-out Protection Circuit, Oscillator Unit, Real Time Clock, Watchdog Timer, Embedded Firmware Design Approaches and Development Languages.

UNIT –IV
RTOS Based Embedded System Design: Operating System Basics, Types of Operating Systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Task Scheduling.

UNIT -V
Task Communication: Shared Memory, Message Passing, Remote Procedure Call and Sockets, Task Synchronization: Task Communication/Synchronization Issues, Task Synchronization Techniques, Device Drivers, How to Choose an RTOS.

TEXT BOOKS:

1. Shibu K.V, "Introduction to Embedded Systems", McGraw Hill.

REFERENCE BOOKS:

1. Raj Kamal, "Embedded Systems", TMH.
2. Frank Vahid, Tony Givargis, "Embedded System Design", John Wiley.
3. Lyla, "Embedded Systems", Pearson, 2013
4. David E. Simon, "An Embedded Software Primer", Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI AND EMBEDDED SYSTEMS/ ELECTRONICS DESIGN TECHNOLOGY.

VLSI TECHNOLOGY (PC-2)

UNIT –I
Review of Microelectronics and Introduction to MOS Technologies: MOS, CMOS, BiCMOS Technology. Basic Electrical Properties of MOS, CMOS & BiCMOS Circuits: Ids – Vds relationships, Threshold Voltage VT, Gm, Gds and ωo, Pass Transistor, MOS, CMOS & Bi CMOS Inverters, Zpu/Zpd, MOS Transistor circuit model, Latch-up in CMOS circuits.

UNIT –II
Layout Design and Tools: Transistor structures, Wires and Vias, Scalable Design rules, Layout Design tools.
Logic Gates & Layouts: Static Complementary Gates, Switch Logic, Alternative Gate circuits, Low power gates, Resistive and Inductive interconnect delays.

UNIT –III
Overview of semiconductor industry: Stages of Manufacturing, Process and product trends, Crystal growth, Basic wafer fabrication operations, process yields, Semiconductor material preparation, Basic wafer fabrication operations, Yield measurement, Contamination sources, Clean room construction, Oxidation and Photolithography, Doping and Depositions, Metallization.Ten step patterning process, Photoresists, physical properties of photoresists, Storage and control of photoresists, photo masking process, Hard bake, develop inspect, Dry etching Wet etching, resist stripping

UNIT –IV
Doping and depositions: Diffusion process steps, deposition, Drive-in oxidation, Ion implantation-1, Ion implantation-2, CVD basics, CVD process steps, Low pressure CVD systems, Plasma enhanced CVD systems, Vapor phase epitoxy, molecular beam epitaxy.

UNIT –V
Design rules and Scaling, BICMOS ICs: Choice of transistor types, pnp transistors, Resistors, capacitors,
Packaging: Chip characteristics, package functions, package operations

TEXT BOOKS:
1. Peter Van Zant, "Microchip fabrication", McGraw Hill, 1997.
2. C.Y. Chang and S.M. Sze, "ULSI technology", McGraw Hill, 2000

REFERENCE BOOKS:
1. Muhammad H Rashid, "Micro Electronics circuits Analysis and Design", 2nd Edition, CENAGE Learning2011.
2. Eugene D. Fabricius, "Introduction to VLSI design", McGraw Hill, 1999
3. Wani-Kai Chen (editor), "The VLSI Hand book", CRI/IEEE press, 2000
4. S.K. Gandhi, "VLSI Fabrication principles", John Wiley and Sons, NY, 1994

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI AND EMBEDDED SYSTEMS/ ELECTRONICS DESIGN TECHNOLOGY.

CMOS ANALOG INTEGRATED CIRCUIT DESIGN (PC-3)

UNIT -I
MOS Devices and Modeling: The MOS Transistor, Passive Components- Capacitor & Resistor, Integrated circuit Layout, CMOS Device Modeling - Simple MOS Large-Signal Model, Other Model Parameters, Small-Signal Model for the MOS Transistor, Computer Simulation Models, Sub-threshold MOS Model.

UNIT -II
Analog CMOS Sub-Circuits:MOS Switch, MOS Diode, MOS Active Resistor, Current Sinks and Sources, Current Mirrors-Current mirror with Beta Helper, Degeneration, Cascode current Mirror and Wilson Current Mirror, Current and Voltage References, Band gap Reference.

UNIT -III
CMOS Amplifiers: Inverters, Differential Amplifiers, Cascode Amplifiers, Current Amplifiers, Output Amplifiers, High Gain Amplifiers Architectures.

UNIT -IV
CMOS Operational Amplifiers: Design of CMOS Op Amps, Compensation of Op Amps, Design of Two-Stage Op Amps, Power- Supply Rejection Ratio of Two-Stage Op Amps, Cascode Op Amps, Measurement Techniques of OP Amp.

UNIT -V
Comparators: Characterization of Comparator, Two-Stage, Open-Loop Comparators, Other Open-Loop Comparators, Improving the Performance of Open-Loop Comparators, Discrete-Time Comparators.

TEXT BOOKS:
1. Philip E. Allen and Douglas R. Holberg, "CMOS Analog Circuit Design", Oxford University Press, International 2nd Edition/Indian Edition, 2010.
2. Paul R. Gray, Paul J. Hurst, S. Lewis and R. G. Meyer, "Analysis and Design of Analog Integrated Circuits", Wiley India, Fifth Edition, 2010.

REFERENCE BOOKS:
1. David A. Johns, Ken Martin, "Analog Integrated Circuit Design", Wiley Student Edn, 2013.
2. Behzad Razavi, "Design of Analog CMOS Integrated Circuits", TMH Edition.
3. Baker, Li and Boyce, "CMOS: Circuit Design, Layout and Simulation", PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI AND EMBEDDED SYSTEMS/ ELECTRONICS DESIGN TECHNOLOGY.

HARDWARE SOFTWARE CO-DESIGN (PE-1)

UNIT –I
Co- Design Issues: Co- Design Models, Architectures, Languages, A Generic Co-design Methodology.
Co- Synthesis Algorithms: Hardware software synthesis algorithms: hardware – software partitioning distributed system co-synthesis.

UNIT –II
Prototyping and Emulation: Prototyping and emulation techniques, prototyping and emulation environments, future developments in emulation and prototyping architecture specialization techniques, system communication infrastructure
Target Architectures: Architecture Specialization techniques, System Communication infrastructure, Target Architecture and Application System classes, Architecture for control dominated systems (8051-Architectures for High performance control), Architecture for Data dominated systems (ADSP21060, TMS320C60), and Mixed Systems.

UNIT –III
Compilation Techniques and Tools for Embedded Processor Architectures: Modern embedded architectures, embedded software development needs, compilation technologies, practical consideration in a compiler development environment.

UNIT –IV
Design Specification and Verification: Design, co-design, the co-design computational model, concurrency coordinating concurrent computations, interfacing components, design verification, implementation verification, verification tools, interface verification

UNIT –V
Languages for System – Level Specification and Design-I: System – level specification, design representation for system level synthesis, system level specification languages,
Languages for System – Level Specification and Design-II: Heterogeneous specifications and multi language co-simulation, the cosyma system and lycos system.

TEXT BOOKS:
1. Jorgen Staunstrup, Wayne Wolf, "Hardware / Software Co- Design Principles and Practice", 2009, Springer.
2. Giovanni De Micheli, Mariagiovanna Sami, "Hardware / Software Co- Design", 2002, Kluwer Academic Publishers

REFERENCE BOOKS:
1. Patrick R. Schaumont, "A Practical Introduction to Hardware/Software Co-design", 2010, Springer

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI AND EMBEDDED SYSTEMS/ ELECTRONICS DESIGN TECHNOLOGY.
DIGITAL SYSTEM DESIGN (PE-1)

UNIT –I
Minimization and Transformation of Sequential Machines: The Finite State Model – Capabilities and limitations of FSM – State equivalence and machine minimization – Simplification of incompletely specified machines.Fundamental mode model – Flow table – State reduction – Minimal closed covers – Races, Cycles and Hazards.

UNIT –II
Digital Design: Digital Design Using ROMs, PALs and PLAs, BCD Adder, 32 – bit adder, State graphs for control circuits, Scoreboard and Controller, A shift and add multiplier, Array multiplier, Keypad Scanner, Binary divider.

UNIT –III
SM Charts: State machine charts, Derivation of SM Charts, Realization of SM Chart, Implementation of Binary Multiplier, dice game controller.

UNIT –IV
Fault Modeling & Test Pattern Generation: Logic Fault model – Fault detection & Redundancy- Fault equivalence and fault location –Fault dominance – Single stuck at fault model – Multiple stuck at fault models –Bridging fault model.
Fault diagnosis of combinational circuits by conventional methods – Path sensitization techniques, Boolean Difference method – Kohavi algorithm – Test algorithms – D algorithm, PODEM, Random testing, Transition count testing, Signature analysis and test bridging faults.

UNIT –V
Fault Diagnosis in Sequential Circuits: Circuit Test Approach, Transition Check Approach – State identification and fault detection experiment, Machine identification, Design of fault detection experiment

TEXT BOOKS:
1. Charles H. Roth, "Fundamentals of Logic Design", 5th Ed., Cengage Learning.
2. Miron Abramovici, Melvin A Breuer and Arthur D. Friedman, "Digital Systems Testing and Testable Design", John Wiley & Sons Inc.
3. N. N. Biswas, "Logic Design Theory", PHI

REFERENCE BOOKS:
1. Z. Kohavi , "Switching and Finite Automata Theory", 2nd Ed., 2001, TMH
2. Morris Mano, M.D. Ciletti, "Digital Design", 4th Edition, PHI.
3. Samuel C. Lee , "Digital Circuits and Logic Design", PHI

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI AND EMBEDDED SYSTEMS/ ELECTRONICS DESIGN TECHNOLOGY.

ADVANCED COMPUTER ARCHITECTURE (PE-1)

UNIT- I

Fundamentals of Computer Design: Fundamentals of Computer design, Changing faces of computing and task of computer designer, Technology trends, Cost price and their trends, measuring and reporting performance, quantitative principles of computer design, Amdahl's law.
Instruction set principles and examples- Introduction, classifying instruction set- memory addressing- type and size of operands, operations in the instruction set.

UNIT – II

Pipelines: Introduction ,basic RISC instruction set ,Simple implementation of RISC instruction set, Classic five stage pipe line for RISC processor, Basic performance issues in pipelining , Pipeline hazards, Reducing pipeline branch penalties.
Memory Hierarchy Design: Introduction, review of ABC of cache, Cache performance, Reducing cache miss penalty, Virtual memory.

UNIT - III

Instruction Level Parallelism the Hardware Approach: Instruction-Level parallelism, Dynamic scheduling, Dynamic scheduling using Tomasulo's approach, Branch prediction, high performance instruction delivery- hardware based speculation.
ILP Software Approach: Basic compiler level techniques, static branch prediction, VLIW approach, Exploiting ILP, Parallelism at compile time, Cross cutting issues -Hardware verses Software.

UNIT – IV

Multi Processors and Thread Level Parallelism: Multi Processors and Thread level Parallelism- Introduction, Characteristics of application domain, Systematic shared memory architecture, Distributed shared – memory architecture, Synchronization.

UNIT – V

Inter Connection and Networks: Introduction, Interconnection network media, Practical issues in interconnecting networks, Examples of inter connection, Cluster, Designing of clusters.
Intel Architecture: Intel IA- 64 ILP in embedded and mobile markets Fallacies and pit falls

TEXT BOOKS:

1. John L. Hennessy, David A. Patterson, "Computer Architecture: A Quantitative Approach", 3rd Edition, An Imprint of Elsevier.

REFERENCE BOOKS:

1. John P. Shen and Miikko H. Lipasti, "Modern Processor Design: Fundamentals of Super Scalar Processors", 2002, Beta Edition, McGrawHill.
2. Kai Hwang, Faye A.Brigs., "Computer Architecture, and Parallel Processing", McGraw Hill.,
3. Dezso Sima, Terence Fountain, Peter Kacsuk, "Advanced Computer Architecture - A Design Space Approach", Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH. I YEAR I SEMESTER

EMBEDDED SYSTEMS & VLSI DESIGN/VLSI AND EMBEDDED SYSTEMS/ ELECTRONICS DESIGN TECHNOLOGY.

VLSI DSP ARCHITECTURES (PE-2)

UNIT - I

Essential feature of Instruction set architectures of CISC, RISC and DSP processors and their implications for implementation as VLSI Chips, Micro programming approaches for implementation of control part of the processor. Assessing understanding performance, Introduction, CPU performance and its factors, evaluating performance, real stuff: Two spec bench marks and performance of recent INTEL processors, fallacies and pitfalls

UNIT - II

Data Path and Control: Introduction, logic design conventions, building a data path, a simple implementation scheme, a multi cycle implementation, exceptions, micro programming: simplifying control design, an introduction to digital design using hardware description language, fallacies and pitfalls

UNIT - III

Enhancing performance with pipeline: An overview of pipelining, a pipe lined data path. Pipe lined control, data hazards and forwarding, data hazards and stalls, branch hazards using a hard ware description language to describe and model a pipe line, exceptions, advanced pipelining: extracting more performance, fallacies and pitfalls

UNIT - IV

Computational Accuracy in DSP Implementations: Introduction, number formats for signals and coefficients in DSP system, dynamic range and precision, sources of errors in DSP implementations, A/D conversion errors, DSP computational errors, D /A conversion errors

UNIT - V

Architectures for programmable digital signal processing devices: introduction, basic architectural features, DSP Computational building blocks, bus architecture and memory, data addressing capabilities, address generation unit, programmability, and program execution, speed issues, features for external interfacing.

TEXT BOOKS:

1. D.A. Patterson and J.L Hennessy, "Computer Organization and Design: Hard ware/ Software Interface", 4th Edition, Elsevier, 2011
2. A.S Tannenbaum, "Structural Computer organization", 4th Edition, Prentice-Hall, 1999

REFERENCE BOOKS:

1. W. Wolf, "Modern VLSI Design: System on Silicon", 2nd Edition, Person Education, 1998
2. Keshab Parhi, "VLSI Digital Signal Processing system design and implementations", Wiley 1999
3. Avatar sign, Srinivasan S, "Digital Signal Processing implementations using DSP microprocessors with examples", Thomson 4th reprint, 2004.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI AND EMBEDDED SYSTEMS/ ELECTRONICS DESIGN TECHNOLOGY.

CMOS DIGITAL INTEGRATED CIRCUIT DESIGN (PE-2)

UNIT –I
MOS Design: Pseudo NMOS Logic – Inverter, Inverter threshold voltage, Output high voltage, Output Low voltage, Gain at gate threshold voltage, Transient response, Rise time, Fall time, Pseudo NMOS logic gates, Transistor equivalency, CMOS Inverter logic.

UNIT –II
Combinational MOS Logic Circuits:MOS logic circuits with NMOS loads, Primitive CMOS logic gates – NOR & NAND gate, Complex Logic circuits design – Realizing Boolean expressions using NMOS gates and CMOS gates , AOI and OIA gates, CMOS full adder, CMOS transmission gates, Designing with Transmission gates.

UNIT –III
Sequential MOS Logic Circuits: Behavior of bistable elements, SR Latch, Clocked latch and flip flop circuits, CMOS D latch, and edge triggered flip-flop.

UNIT –IV
Dynamic Logic Circuits: Basic principle, Voltage Bootstrapping, Synchronous dynamic pass transistor circuits, Dynamic CMOS transmission gate logic, High performance Dynamic CMOS circuits.

UNIT –V
Semiconductor Memories: Types, RAM array organization, DRAM – Types, Operation, Leakage currents in DRAM cell and refresh operation, SRAM operation Leakage currents in SRAM cells, Flash Memory- NOR flash and NAND flash.

TEXT BOOKS:

1. Ken Martin, "Digital Integrated Circuit Design", Oxford University Press, 2011.
2. Sung-Mo Kang, Yusuf Leblebici, "CMOS Digital Integrated Circuits Analysis and Design", TMH, 3rd Edition, 2011.

REFERENCE BOOKS:

1. Ming-BO Lin, "Introduction to VLSI Systems: A Logic, Circuit and System Perspective", CRC Press, 2011
2. Jan M. Rabaey, Anantha Chandrakasan, Borivoje Nikolic, "Digital Integrated Circuits – A Design Perspective", 2nd Edition, PHI.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI AND EMBEDDED SYSTEMS/ ELECTRONICS DESIGN TECHNOLOGY.

CPLD AND FPGA ARCHITECTURES AND APPLICATIONS (PE-2)

UNIT-I
Introduction to Programmable Logic Devices: Introduction, Simple Programmable Logic Devices – Read Only Memories, Programmable Logic Arrays, Programmable Array Logic, Programmable Logic Devices/Generic Array Logic; Complex Programmable Logic Devices – Architecture of Xilinx Cool Runner XCR3064XL CPLD, CPLD Implementation of a Parallel Adder with Accumulation.

UNIT-II
Field Programmable Gate Arrays: Organization of FPGAs, FPGA Programming Technologies, Programmable Logic Block Architectures, Programmable Interconnects, Programmable I/O blocks in FPGAs, Dedicated Specialized Components of FPGAs, Applications of FPGAs.

UNIT –III
SRAM Programmable FPGAs: Introduction, Programming Technology, Device Architecture, The Xilinx XC2000, XC3000 and XC4000 Architectures.

UNIT –IV
Anti-Fuse Programmed FPGAs: Introduction, Programming Technology, Device Architecture, The Actel ACT1, ACT2 and ACT3 Architectures.

UNIT –V
Design Applications: General Design Issues, Counter Examples, A Fast Video Controller, A Position Tracker for a Robot Manipulator, A Fast DMA Controller, Designing Counters with ACT devices, Designing Adders and Accumulators with the ACT Architecture.

TEXT BOOKS:
1. Stephen M. Trimberger, "Field Programmable Gate Array Technology", Springer International Edition.
2. Charles H. Roth Jr, Lizy Kurian John, "Digital Systems Design", Cengage Learning.

REFERENCE BOOKS:
1. John V. Oldfield, Richard C. Dorf, "Field Programmable Gate Arrays", Wiley India.
2. Pak K. Chan/Samiha Mourad, "Digital Design Using Field Programmable Gate Arrays", Pearson Low Price Edition.
3. Ian Grout, "Digital Systems Design with FPGAs and CPLDs", Elsevier, Newnes.
4. Wayne Wolf, "FPGA based System Design", Prentice Hall Modern Semiconductor Design Series.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M. TECH. I YEAR I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI AND EMBEDDED SYSTEMS/ ELECTRONICS DESIGN TECHNOLOGY.

VLSI LAB

Note:

- Minimum of 10 programs from Part –I and 2 programs from Part -II are to be conducted.

Design and implementation of the following CMOS digital/analog circuits must be carried out using **Cadence / Mentor Graphics / Synopsys / Equivalent** CAD tools. The design shall include Gate-level design, Transistor-level design, Hierarchical design, Verilog HDL/VHDL design, Logic synthesis, Simulation and verification.

Part –I: VLSI Front End Design programs:
Programming can be done using any complier. Down load the programs on FPGA/CPLD boards and performance testing may be done using pattern generator (32 channels) and logic analyzer apart from verification by simulation with any of the front end tools.

1. HDL code to realize all the logic gates
2. Design and Simulation of adder, Serial Binary Adder, Multi Precession Adder, Carry
3. Look Ahead Adder.
4. Design of 2-to-4 decoder
5. Design of 8-to-3 encoder (without and with parity)
6. Design of 8-to-1 multiplexer
7. Design of 4 bit binary to gray converter
8. Design of Multiplexer/ Demultiplexer, comparator
9. Design of Full adder using 3 modeling styles
10. Design of flip flops: SR, D, JK, T
11. Design of 4-bit binary, BCD counters (synchronous/ asynchronous reset) or any sequence counter
12. Design of a N- bit Register of Serial- in Serial –out, Serial in parallel out, Parallel in
13. Serial out and Parallel in Parallel Out.
14. Design of Sequence Detector (Finite State Machine- Mealy and Moore Machines).
15. Design of 4- Bit Multiplier, Divider.
16. Design of ALU to Perform – ADD, SUB, AND-OR, 1's and 2's Compliment, Multiplication and Division.
17. Design of Finite State Machine.
18. Implementing the above designs on Xilinx/Altera/Cypress/equivalent based FPGA/CPLD kits.

Part –II: VLSI Back End Design programs:

Design and implementation of the following CMOS digital/analog circuits using **Cadence / Mentor Graphics / Synopsys / Equivalent** CAD tools. The design shall include Gate-level design/Transistor-level design/Hierarchical design/Verilog HDL or VHDL design, Logic synthesis, Simulation and verification, Scaling of CMOS Inverter for different technologies, study of secondary effects (temperature, power supply and process corners), Circuit optimization with respect to area, performance and/or power, Layout, Extraction of parasitic and back annotation, modifications in circuit parameters and layout consumption, DC/transient analysis, Verification of layouts (DRC, LVS).

R19 → 2020-21 & 2021 & 2022

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH. IN EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS
EFFECTIVE FROM ACADEMIC YEAR 2019-20 ADMITTED BATCH

R19 COURSE STRUCTURE AND SYLLABUS

I YEAR I – SEMESTER

Course Code	Course Title	L	T	P	Credits
Professional Core - I	RTL Simulation and Synthesis with PLDs	3	0	0	3
Professional Core - II	Microcontrollers & Programmable Digital Signal Processors	3	0	0	3
Professional Elective - I	1. Digital Signal & Image Processing 2. Programming Languages for Embedded Software 3. Memory Technologies	3	0	0	3
Professional Elective - II	1. Parallel Processing 2. Advanced Computer Architecture 3. CAD of Digital Systems	3	0	0	3
Lab - I	RTL Simulation and Synthesis with PLDs Lab	0	0	3	2
Lab - II	Microcontrollers & Programmable Digital Signal Processors Lab	0	0	3	2
	Research Methodology & IPR	2	0	0	2
Audit - I	Audit Course - I	2	0	0	0
	Total	**16**	**0**	**6**	**18**

I YEAR II – SEMESTER

Course Code	Course Title	L	T	P	Credits
Professional Core - III	Analog and Digital CMOS VLSI Design	3	0	0	3
Professional Core - IV	System Design with Embedded Linux	3	0	0	3
Professional Elective - III	1. Advanced Digital Signal Processing 2. SOC Design 3. Low Power VLSI Design	3	0	0	3
Professional Elective - IV	1. Communications Buses & Interfaces 2. Network Security & Cryptography 3. Physical Design Automation	3	0	0	3
Lab - III	Analog and Digital CMOS VLSI Design Lab	0	0	3	2
Lab - IV	System Design with Embedded Linux Lab	0	0	3	2
	Mini project with Seminar	0	0	4	2
Audit - II	Audit Course- II	2	0	0	0
	Total	**14**	**0**	**10**	**18**

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III – SEMESTER

Course Code	Course Title	L	T	P	Credits
Professional Elective - V	1. IOT and its Applications 2. AI and Machine Learning 3. Nano Materials and Nano Technology	3	0	0	3
Open Elective	Open Elective	3	0	0	3
Dissertation	Dissertation Work Review - II	0	0	12	6
	Total	**6**	**0**	**12**	**12**

II YEAR II - SEMESTER

Course Code	Course Title	L	T	P	Credits
Dissertation	Dissertation Work Review - III	0	0	12	6
Dissertation	Dissertation Viva-Voce	0	0	28	14
	Total	**0**	**0**	**40**	**20**

***For Dissertation Work Review - I, Please refer 7.8 in R19 Academic Regulations.**

Audit Course I & II:

1. English for Research Paper Writing
2. Disaster Management
3. Sanskrit for Technical Knowledge
4. Value Education
5. Constitution of India
6. Pedagogy Studies
7. Stress Management by yoga
8. Personality Development Through Life Enlightenment Skills

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH. I YEAR - I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

RTL SIMULATION AND SYNTHESIS WITH PLDs (PC – I)

Course Outcomes: At the end of the course, students will demonstrate the ability to:
1. Familiarity of Finite State Machines, RTL design using reconfigurable logic.
2. Design and develop IP cores and Prototypes with performance guarantees
3. Use EDA tools like Cadence, Mentor Graphics and Xilinx

UNIT-I
Top down approach to design, Design of FSMs (Synchronous and asynchronous), Static timing analysis, Meta-stability, Clock issues, Need and design strategies for multi-clock domain designs.

UNIT-II
Design entry by Verilog/VHDL/FSM, Verilog AMS.

UNIT-III
Programmable Logic Devices, Introduction to ASIC Design Flow, FPGA, SoC, Floor planning, Placement, Clock tree synthesis, Routing, Physical verification, Power analysis, ESD protection.

UNIT-IV
Design for performance, Low power VLSI design techniques. Design for testability.

UNIT-V
IP and Prototyping: IP in various forms: RTL Source, Encrypted Source, Soft IP, Netlist, Physical IP, Use of external hard IP during prototyping

TEXTBOOKS:

1. Richard S. Sandige, "Modern Digital Design", MGH, International Editions.
2. Donald D Givone, "Digital principles and Design", TMH

REFERENCES:

1. Charles Roth, Jr. and Lizy K John, "Digital System Design using VHDL", Cengage Learning.
2. Samir Palnitkar, "Verilog HDL, a guide to digital design and synthesis", Prentice Hall.
3. Doug Amos, Austin Lesea, Rene Richter, "FPGA based prototyping methodology manual", Xilinx.
4. Bob Zeidman, "Designing with FPGAs & CPLDs", CMP Books.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH. I YEAR - I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

MICROCONTROLLERS AND PROGRAMMABLE DIGITAL SIGNAL PROCESSORS (PC – II)

Course Outcomes: At the end of this course, students will be able to;

1. Compare and select ARM processor core based SoC with several features/peripherals based on requirements of embedded applications.
2. Identify and characterize architecture of Programmable DSP Processors
3. Develop small applications by utilizing the ARM processor core and DSP processor-based platform.

UNIT-I
ARM Cortex-M3 processor: Applications, Programming model – Registers, Operation - modes, Exceptions and Interrupts, Reset Sequence Instruction Set, Unified AssemblerLanguage, Memory Maps, Memory Access Attributes, Permissions, Bit-Band Operations, Unaligned and Exclusive Transfers. Pipeline, Bus Interfaces.

UNIT-II
Exceptions, Types, Priority, Vector Tables, Interrupt Inputs and Pending behaviour, Fault Exceptions, Supervisor and Pendable Service Call, Nested Vectored Interrupt Controller, Basic Configuration, SYSTICK Timer, Interrupt Sequences, Exits, Tail Chaining, Interrupt Latency.

UNIT-III
LPC 17xx microcontroller- Internal memory, GPIOs, Timers, ADC, UART and other serial interfaces, PWM, RTC, WDT.

UNIT-IV
Programmable DSP (P-DSP) Processors: Harvard architecture, Multi port memory, architectural structure of P-DSP- MAC unit, Barrel shifters, Introduction to TI DSP processor family

UNIT-V
VLIW architecture and TMS320C6000 series, architecture study, data paths, cross paths, Introduction to Instruction level architecture of C6000 family, Assembly Instructions memory addressing, for arithmetic, logical operations

TEXTBOOKS:

1. Joseph Yiu, "The definitive guide to ARM Cortex-M3", Elsevier, 2nd Edition
2. Venkatramani B. and Bhaskar M. "Digital Signal Processors: Architecture, Programming and Applications", TMH, 2nd Edition

REFERENCES:

1. Sloss Andrew N, Symes Dominic, Wright Chris, "ARM System Developer's Guide: Designing and Optimizing", Morgan Kaufman Publication.
2. Steve furber, "ARM System-on-Chip Architecture", Pearson Education
3. Frank Vahid and Tony Givargis, "Embedded System Design", Wiley
4. Technical references and user manuals on www.arm.com, NXP Semiconductor www.nxp.com and Texas Instruments www.ti.com

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M.TECH. I YEAR - I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

DIGITAL SIGNAL AND IMAGE PROCESSING (PE – I)

Course Outcomes: At the end of this course, students will be able to

1. Analyze discrete-time signals and systems in various domains
2. Design and implement filters using fixed point arithmetic targeted for embedded platforms
3. Compare algorithmic and computational complexities in processing and coding digital images.

UNIT-I
Review of Discrete Time signals and systems, Characterization in time and Z and Fourier – domain, Fast Fourier Transform algorithms – In-place computations, Butterfly computations, bit reversal's.

UNIT-II
Digital Filter design: FIR - Windowing and Frequency Sampling, IIR – Impulse invariance, bilinear Transformation.

UNIT-III
Fixed point implementation of filters – challenges and techniques.

UNIT-IV
Digital Image Acquisition, Enhancement, Restoration. Digital Image Coding and Compression – JPEG and JPEG 2000.

UNIT-V
Color Image processing – Handling multiple planes, computational challenges.

TEXTBOOKS:

1. J.G. Proakis, Manolakis "Digital Signal Processing", Pearson, 4th Edition
2. Gonzalez and Woods, "Digital Image Processing", PHI, 3rd Edition

REFERENCES:

1. S. K. Mitra. "Digital Signal Processing – A Computer based Approach", TMH, 3rd Edition, 2006
2. K. Jain, "Fundamentals of Digital Image Processing", Prentice Hall
3. Keshab Parhi, "VLSI Digital Signal Processing Systems – Design and Implementation", Wiley India

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH.- I YEAR- I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

PROGRAMMING LANGUAGES FOR EMBEDDED SOFTWARE (PE – I)

Course Outcomes: At the end of this course, students will be able to
1. Write an embedded C application of moderate complexity.
2. Develop and analyze algorithms in C++.
3. Differentiate interpreted languages from compiled languages.

UNIT-I

Embedded 'C' Programming
- Bitwise operations, Dynamic memory allocation, OS services
- Linked stack and queue, Sparse matrices, Binary tree
- Interrupt handling in C, optimization issues
- Writing LCD drives, LED drivers, Drivers for serial port communication
- Embedded Software Development Cycle and Methods (Waterfall, Agile)

UNIT -II

CPP Programming: 'cin', 'cout', formatting and I/O manipulators, new and delete operators, Defining a class, data members and methods, 'this' pointer, constructors, destructors, friend function, dynamic memory allocation

UNIT-III

Overloading and Inheritance: Need of operator overloading, overloading the assignment, overloading using friends, type conversions, single inheritance, base and derived classes, friend classes, types of inheritance, hybrid inheritance, multiple inheritance, virtual base class, polymorphism, virtual functions

UNIT-IV

Templates: Function template and class template, member function templates and template arguments, Exception Handling: syntax for exception handling: try-catch-throw, Multiple Exceptions.

UNIT-V

Scripting Languages Overview of Scripting Languages – PERL, CGI, VB Script, Java Script.
PERL: Operators, Statements Pattern Matching etc. Data Structures, Modules, Objects, Tied Variables,
Inter process Communication Threads, Compilation & Line Interfacing.

TEXTBOOKS:

1. Michael J. Pont, "Embedded C", Pearson Education, 2nd Edition, 2008
2. Randal L. Schwartz, "Learning Perl", O'Reilly Publications, 6th Edition 2011

REFERENCES:

1. A. Michael Berman, "Data structures via C++", Oxford University Press, 2002
2. Robert Sedgewick, "Algorithms in C++", Addison Wesley Publishing Company, 1999
3. Abraham Silberschatz, Peter B, Greg Gagne, "Operating System Concepts", John Willey & Sons, 2005

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH. I YEAR - I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

MEMORY TECHNOLOGIES (PE – I)

Course Outcomes: At the end of the course, students will be able to:

1. Select architecture and design semiconductor memory circuits and subsystems.
2. Identify various fault models, modes and mechanisms in semiconductor memories and their testing procedures.
3. Know, how of the state-of-the-art memory chip design

UNIT-I

Random Access Memory Technologies: Static Random-Access Memories (SRAMs), SRAM Cell Structures, MOS SRAM Architecture, MOS SRAM Cell and Peripheral Circuit, Bipolar SRAM, Advanced SRAM Architectures, Application Specific SRAMs.

UNIT-II

DRAMs, MOS DRAM Cell, BiCMOS DRAM, Error Failures in DRAM, Advanced DRAM Design and Architecture, Application Specific DRAMs. SRAM and DRAM Memory controllers.

UNIT-III

Non-Volatile Memories: Masked ROMs, PROMs, Bipolar & CMOS PROM, EEPROMs, Floating Gate EPROM Cell, OTP EPROM, EEPROMs, Non-volatile SRAM, Flash Memories.

UNIT-IV

Advanced Memory Technologies and High-density Memory Packing Technologies: Ferroelectric Random-Access Memories (FRAMs), Gallium Arsenide (GaAs) FRAMs, Analog Memories, Magneto Resistive Random-Access Memories (MRAMs), Experimental Memory Devices.

UNIT-V

Memory Hybrids (2D & 3D), Memory Stacks, Memory Testing and Reliability Issues, Memory Cards, High Density Memory Packaging.

TEXTBOOKS:

1. Ashok K Sharma, "Advanced Semiconductor Memories: Architectures, Designs and Applications", Wiley Interscience
2. Kiyoo Itoh, "VLSI memory chip design", Springer International Edition

REFERENCE:

1. Ashok K Sharma," Semiconductor Memories: Technology, Testing and Reliability, PHI

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M.TECH. I YEAR - I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

PARALLEL PROCESSING (PE – II)

Course Outcomes: At the end of this course, students will be able to
1. Identify limitations of different architectures of computer
2. Analysis quantitatively the perfomance parameters for different architectures
3. Investigate issues related to compilers and instruction set based on type of architectures.

UNIT-I
Overview of Parallel Processing and Pipelining, Performance analysis, Scalability, Principles and implementation of Pipelining, Classification of pipelining processors, Advanced pipelining techniques, Software pipelining

UNIT-II
VLIW processors
Case study: Superscalar Architecture- Pentium, Intel Itanium Processor, Ultra SPARC, MIPS on FPGA, Vector and Array Processor, FFT Multiprocessor Architecture

UNIT-III
Multithreaded Architecture, Multithreaded processors, Latency hiding techniques, Principles of multithreading, Issues and solutions.

UNIT-IV
Parallel Programming Techniques: Message passing program development, Synchronous and asynchronous message passing, Shared Memory Programming, Data Parallel Programming, Parallel Software Issues

UNIT-V
Operating systems for multiprocessors systems Customizing applications on parallel processing platforms

TEXTBOOKS:

1. Kai Hwang, Faye A. Briggs, "Computer Architecture and Parallel Processing", MGH International Edition
2. Kai Hwang, "Advanced Computer Architecture", TMH

REFERENCES:

1. V. Rajaraman, L. Sivaram Murthy, "Parallel Computers", PHI.
2. William Stallings, "Computer Organization and Architecture, Designing for performance" Prentice Hall, Sixth edition
3. Kai Hwang, Zhiwei Xu, "Scalable Parallel Computing", MGH
4. David Harris and Sarah Harris, "Digital Design and Computer Architecture", Morgan Kaufmann.
5. System Design with Embedded Circuits (PE-2.2)

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH.- I YEAR- I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

ADVANCED COMPUTER ARCHITECTURE (PE – II)

UNIT- I

Fundamentals of Computer Design: Fundamentals of Computer design, Changing faces of computing and task of computer designer, Technology trends, Cost price and their trends, measuring and reporting performance, quantitative principles of computer design, Amdahl's law.
Instruction set principles and examples- Introduction, classifying instruction set- memory addressing- type and size of operands, operations in the instruction set.

UNIT – II

Pipelines: Introduction, basic RISC instruction set, Simple implementation of RISC instruction set, Classic five stage pipe line for RISC processor, Basic performance issues in pipelining, Pipeline hazards, Reducing pipeline branch penalties.
Memory Hierarchy Design: Introduction, review of ABC of cache, Cache performance, Reducing cache miss penalty, Virtual memory.

UNIT - III

Instruction Level Parallelism the Hardware Approach: Instruction-Level parallelism, Dynamic scheduling, Dynamic scheduling using Tomasulo's approach, Branch prediction, high performance instruction delivery- hardware based speculation.
ILP Software Approach: Basic compiler level techniques, static branch prediction, VLIW approach, Exploiting ILP, Parallelism at compile time, Cross cutting issues -Hardware verses Software.

UNIT – IV

Multi Processors and Thread Level Parallelism: Multi Processors and Thread level Parallelism- Introduction, Characteristics of application domain, Systematic shared memory architecture, Distributed shared – memory architecture, Synchronization.

UNIT – V

Inter Connection and Networks: Introduction, Interconnection network media, Practical issues in interconnecting networks, Examples of inter connection, Cluster, Designing of clusters.
Intel Architecture: Intel IA- 64 ILP in embedded and mobile markets Fallacies and pit falls

TEXT BOOKS:

1. John L. Hennessy, David A. Patterson, "Computer Architecture: A Quantitative Approach", 3rd Edition, Elsevier.

REFERENCE BOOKS

1. John P. Shen and Miikko H. Lipasti, "Modem Processor Design: Fundamentals of Super Scalar Processors", 2002, Beta Edition, McGraw-Hill
2. Kai Hwang, Faye A.Brigs., "Computer Architecture and Parallel Processing", Mc Graw Hill.
3. Dezso Sima, Terence Fountain, Peter Kacsuk, "Advanced Computer Architecture - A Design Space Approach", Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH.- I YEAR- I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

CAD FOR DIGITAL SYSTEMS (PE – II)

Course Outcomes: At the end of this course, students will be able to

1. Fundamentals of CAD tools for modelling, design, test and verification of VLSI systems.
2. Study of various phases of CAD, including simulation, physical design, test and verification.
3. Demonstrate knowledge of computational algorithms and tools for CAD.

UNIT-I
Introduction to VLSI Methodologies –Design and Fabrication of VLSI Devices, Fabrication Process and its impact on Design.

UNIT-II
VLSI design automation tools – Data structures and basic algorithms, graph theory and computational complexity, tractable and intractable problems.

UNIT-III
General purpose methods for combinational optimization – partitioning, floor planning and pin assignment, placement, routing.

UNIT-IV
Simulation – logic synthesis, verification, high level Synthesis.

UNIT-V
MCMS-VHDL-Verilog-implementation of simple circuits using VHDL

REFERENCES:

1. N.A. Sherwani, "Algorithms for VLSI Physical Design Automation".
2. S.H. Gerez, "Algorithms for VLSI Design Automation".

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH.- I YEAR- I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

RTL SIMULATION AND SYNTHESIS WITH PLDs LAB (Lab – I)

Course Outcomes: At the end of the laboratory work, students will be able to:

1. Identify, formulate, solve and implement problems in signal processing, communication systems etc using RTL design tools.
2. Use EDA tools like Cadence, Mentor Graphics and Xilinx or equivalent tools

List of Experiments:

1. Verilog implementation of 8:1 Mux/Demux, Full Adder, 8-bit Magnitude comparator, Enr/der, Priority enr, D-FF, 4-bit Shift registers (SISO, SIPO, PISO, bidirectional), 3-bit Synchronous Counters, Binary to Gray converter, Parity generator.
2. Sequence generator/detectors, Synchronous FSM – Mealy and Moore machines.
3. Vending machines - Traffic Light controller, ATM, elevator control.
4. PCI Bus & arbiter and downloading on FPGA.
5. UART/ USART implementation in Verilog.
6. Realization of single port SRAM in Verilog.
7. Verilog implementation of Arithmetic circuits like serial adder/ subtractor, parallel adder/subtractor, serial/parallel multiplier.
8. Discrete Fourier transform/Fast Fourier Transform algorithm in Verilog.

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH.- I YEAR- I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

MICROCONTROLLERS AND PROGRAMMABLE DIGITAL SIGNAL PROCESSORS LAB (Lab – II)

Course Outcomes: At the end of the laboratory work, students will be able to:

1. Install, configure and utilize tool sets for developing applications based on ARM processor core SoC and DSP processor.
2. Develop prototype s using commonly available on and off chip peripherals on the Cortex M3 and DSP development boards.

List of Assignments:

Part A) Experiments to be carried out on Cortex-M3 development boards and using GNU tool- chain

1. Blink an LED with software delay, delay generated using the SysTick timer.
2. System clock real time alteration using the PLL modules.
3. Control intensity of an LED using PWM implemented in software and hardware.
4. Control an LED using switch by polling method, by interrupt method and flash the LED once every five switch presses.
5. UART Echo Test.
6. Take analog readings on rotation of rotary potentiometer connected to an ADC channel.
7. Temperature indication on an RGB LED.
8. Mimic light intensity sensed by the light sensor by varying the blinking rate of an LED.
9. Evaluate the various sleep modes by putting core in sleep and deep sleep modes.
10. System reset using watchdog timer in case something goes wrong.
11. Sample sound using a microphone and display sound levels on LEDs.

Part B) Experiments to be carried out on DSP C6713 evaluation kits and using Code Composer Studio (CCS)

12. To develop an assembly and C to compute Euclidian distance between any two points
13. To develop assembly and study the impact of parallel, serial and mixed execution
14. To develop assembly and C for implementation of convolution operation
15. To design and implement filters in C to enhance the features of given input sequence/signal

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH. I YEAR - I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

RESEARCH METHODOLOGY AND IPR

Prerequisite: None

Course Objectives:
- To understand the research problem
- To know the literature studies, plagiarism and ethics
- To get the knowledge about technical writing
- To analyze the nature of intellectual property rights and new developments
- To know the patent rights

Course Outcomes: At the end of this course, students will be able to
- Understand research problem formulation.
- Analyze research related information
- Follow research ethics
- Understand that today's world is controlled by Computer, Information Technology, but tomorrow world will be ruled by ideas, concept, and creativity.
- Understanding that when IPR would take such important place in growth of individuals & nation, it is needless to emphasis the need of information about Intellectual Property Right to be promoted among students in general & engineering in particular.
- Understand that IPR protection provides an incentive to inventors for further research work and investment in R & D, which leads to creation of new and better products, and in turn brings about, economic growth and social benefits.

UNIT-I:
Meaning of research problem, Sources of research problem, Criteria Characteristics of a good research problem, Errors in selecting a research problem, Scope and objectives of research problem. Approaches of investigation of solutions for research problem, data collection, analysis, interpretation, Necessary instrumentations

UNIT-II:
Effective literature studies approaches, analysis, Plagiarism, Research ethics

UNIT-III:
Effective technical writing, how to write report, Paper Developing a Research Proposal, Format of research proposal, a presentation and assessment by a review committee

UNIT-IV:
Nature of Intellectual Property: Patents, Designs, Trade and Copyright. Process of Patenting and Development: technological research, innovation, patenting, development. International Scenario: International cooperation on Intellectual Property. Procedure for grants of patents, Patenting under PCT.

UNIT-V:
Patent Rights: Scope of Patent Rights. Licensing and transfer of technology. Patent information

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and databases. Geographical Indications. New Developments in IPR: Administration of Patent System. New developments in IPR; IPR of Biological Systems, Computer Software etc. Traditional knowledge Case Studies, IPR and IITs.

TEXT BOOKS:

1. Stuart Melville and Wayne Goddard, "Research methodology: an introduction for science & engineering students'"
2. Wayne Goddard and Stuart Melville, "Research Methodology: An Introduction"

REFERENCES:

1. Ranjit Kumar, 2nd Edition, "Research Methodology: A Step by Step Guide for beginners"
2. Halbert, "Resisting Intellectual Property", Taylor & Francis Ltd ,2007.
3. Mayall, "Industrial Design", McGraw Hill, 1992.
4. Niebel, "Product Design", McGraw Hill, 1974.
5. Asimov, "Introduction to Design", Prentice Hall, 1962.
6. Robert P. Merges, Peter S. Menell, Mark A. Lemley, "Intellectual Property in New Technological Age", 2016.
7. T. Ramappa, "Intellectual Property Rights Under WTO", S. Chand, 2008

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH.- I YEAR- II SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

ANALOG AND DIGITAL CMOS VLSI DESIGN (PC – III)

Course Outcomes: At the end of this course, students will be able to
1. Analyze, design, optimize and simulate analog and digital circuits using CMOS constrained by the design metrics.
2. Connect the individual gates to form the building blocks of a system.
3. Use EDA tools like Cadence, Mentor Graphics and other open source software tools like Ngspice

Digital CMOS Design:
UNIT-I:
Review: Basic MOS structure and its static behavior, Quality metrics of a digital design: Cost, Functionality, Robustness, Power, and Delay, Stick diagram and Layout, Wire delay models. Inverter: Static CMOS inverter, Switching threshold and noise margin concepts and their evaluation, Dynamic behavior, Power consumption.

UNIT-II
Physical design flow: Floor planning, Placement, Routing, CTS, Power analysis and IR drop estimation-static and dynamic, ESD protection-human body model, Machine model.
Combinational logic: Static CMOS design, Logic effort, Ratioed logic, Pass transistor logic, Dynamic logic, Speed and power dissipation in dynamic logic, Cascading dynamic gates, CMOS transmission gate logic.

UNIT-III
Sequential logic: Static latches and registers, Bi-stability principle, MUX based latches, Static SR flip-flops, Master-slave edge-triggered register, Dynamic latches and registers, Concept of pipelining, Pulse registers, Non-bistable sequential circuit.
Advanced technologies: Giga-scale dilemma, Short channel effects, High–k, Metal Gate Technology, FinFET, TFET etc.

Analog CMOS Design:
UNIT-IV
Single Stage Amplifier: CS stage with resistance load, Divide connected load, Current source load, Triode load, CS stage with source degeneration, Source follower, Common-gate stage, Cascade stage, Choice of device models. Differential Amplifiers: Basic difference pair, Common mode response, Differential pair with MOS loads, Gilbert cell.

UNIT-V
Passive and active current mirrors: Basic current mirrors, Cascade mirrors, Active current mirrors. Frequency response of CS stage: Source follower, Common gate stage, Cascade stage and difference pair, Noise

TEXTBOOKS:

1. J P Rabaey, A P Chandrakasan, B Nikolic, "Digital Integrated circuits: A design perspective", Prentice Hall electronics and VLSI series, 2nd Edition.
2. Baker, Li, Boyce, "CMOS Circuit Design, Layout, and Simulation", Wiley, 2nd Edition.

REFERENCES:

1. BehzadRazavi , "Design of Analog CMOS Integrated Circuits", TMH, 2007.
2. Phillip E. Allen and Douglas R. Holberg, "CMOS Analog Circuit Design", Oxford, 3rd Edition.
3. R J Baker, "CMOS circuit Design, Layout and Simulation", IEEE Inc., 2008.
4. Kang, S. and Leblebici, Y., "CMOS Digital Integrated Circuits, Analysis and Design", TMH, 3rd Edition.
5. Pucknell, D.A. and Eshraghian, K., "Basic VLSI Design", PHI, 3rd Edition

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M.TECH.- I YEAR- II SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

SYSTEM DESIGN WITH EMBEDDED LINUX (PC – IV)

Course Outcomes: At the end of this course, students will be able to

1. Familiarity of the embedded Linux development model.
2. Write, debug, and profile applications and drivers in embedded Linux.
3. Understand and create Linux BSP for a hardware platform

UNIT - I
Introduction to Real Time Operating Systems: Characteristics of RTOS, Tasks Specifications and types, Real-Time Scheduling Algorithms, Concurrency, Inter-process Communication and Synchronization mechanisms, Priority Inversion, Inheritance and Ceiling.
Embedded Linux Vs Desktop Linux, Embedded Linux Distributions, System calls, Static and dynamic libraries, Cross tool chains

UNIT - II
Embedded Linux Architecture, Kernel Architecture – HAL, Memory manager, Scheduler, File System, I/O and Networking subsystem, IPC, User space, Start-up sequence

UNIT - III
Board Support Package Embedded Storage: MTD, Architecture, Drivers, Embedded File System Embedded Device Drivers: Communication between user space and kernel space drivers, Character and Block Device Drivers, Interrupt handling, Kernel modules
Embedded Drivers: Serial, Ethernet, I2 C, USB, Timer, Kernel Modules

UNIT - IV
Porting Applications Real-Time Linux: Linux and Real time, Programming, Hard Real-time Linux

UNIT - V
Building and Debugging: Bootloaders, Kernel, Root file system, Device Tree

TEXT BOOKS:

1. Chris Simmonds, "Mastering Embedded Linux Programming" - Second Edition, PACKT Publications Limited.
2. Karim Yaghmour, "Building Embedded Linux Systems", O'Reilly & Associates
3. P Raghvan, Amol Lad, Sriram Neelakandan, "Embedded Linux System Design and Development", Auerbach Publications

REFERENCES:

1. Christopher Hallinan, "Embedded Linux Primer: A Practical Real-World Approach", Prentice Hall, 2nd Edition, 2010.
2. Derek Molloy, "Exploring Beagle Bone: Tools and Techniques for Building with Embedded Linux", Wiley, 1st Edition, 2014

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

M.TECH.- I YEAR- II SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

ADVANCED DIGITAL SIGNAL PROCESSING (PE – III)

Course Outcomes: At the end of this course, students will be able to

1. To understand theory of different filters and algorithms
2. To understand theory of multirate DSP, solve numerical problems and write algorithms
3. To understand theory of prediction and solution of normal equations
4. To know applications of DSP at block level.

UNIT-I

Overview of DSP, Characterization in time and frequency, FFT Algorithms, Digital filter design and structures: Basic FIR/IIR filter design &structures, design techniques of linear phase FIR filters,IIR filters by impulse invariance, bilinear transformation, FIR/IIR Cascaded lattice structures, parallel realization of IIR.

UNIT-II

Multi rate DSP, Decimators and Interpolators, Sampling rate conversion, multistage decimator & interpolator, poly phase filters, QMF, digital filter banks, Applications in subband coding.

UNIT-III

Linear prediction & optimum linear filters, stationary random process, forward-backward linear prediction filters, solution of normal equations, AR Lattice and ARMA Lattice-Ladder Filters, Wiener Filters for Filtering and Prediction.

UNIT-IV

Adaptive Filters, Applications, Gradient Adaptive Lattice, Minimum mean square criterion, LMS algorithm, Recursive Least Square algorithm

UNIT-V

Estimation of Spectra from Finite-Duration Observations of Signals. Nonparametric Methods for Power Spectrum Estimation, Parametric Methods for Power Spectrum Estimation, Minimum-Variance Spectral Estimation, Eigen analysis Algorithms for Spectrum Estimation.

TEXTBOOKS:

1. J. G. Proakis and D.G. Manolakis, "Digital signal processing: Principles, Algorithm and Applications", 4th Edition, Prentice Hall, 2007.
2. N. J. Fliege, "Multirate Digital Signal Processing: Multirate Systems -Filter Banks – Wavelets", 1st Edition, John Wiley and Sons Ltd, 1999.

REFERENCES:

1. Bruce W. Suter, "Multirate and Wavelet Signal Processing",1st Edition, Academic Press, 1997.
2. M. H. Hayes, "Statistical Digital Signal Processing and Modeling", John Wiley & Sons Inc., 2002.
3. S. Haykin, "Adaptive Filter Theory", 4th Edition, Prentice Hall, 2001.
4. D. G. Manolakis, V. K. Ingle and S. M. Kogon, "Statistical and Adaptive Signal Processing", McGraw Hill, 2000

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M.TECH.- I YEAR- II SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS
SOC DESIGN (PE – III)

Course Outcomes: At the end of the course, students will be able to:

1. Identify and formulate a given problem in the framework of SoC based design approaches
2. Design SoC based system for engineering applications
3. Realize impact of SoC on electronic design philosophy and Macro-electronics thereby incline towards entrepreneurship & skill development.

UNIT-I

ASIC: Overview of ASIC types, design strategies, CISC, RISC and NISC approaches for SOC architectural issues and its impact on SoC design methodologies, Application Specific Instruction Processor (ASIP) concepts.

UNIT-II

NISC: NISC Control Words methodology, NISC Applications and Advantages, Architecture Description Languages (ADL) for design and verification of Application Specific Instruction- set Processors (ASIP), No-Instruction-Set-computer (NISC)- design flow, modeling NISC architectures and systems, use of Generic Netlist Representation - A formal language for specification, compilation and synthesis of embedded processors.

UNIT-III

Simulation: Different simulation modes, behavioural, functional, static timing, gate level, switch level, transistor/circuit simulation, design of verification vectors, Low power FPGA, Reconfigurable systems, SoC related modeling of data path design and control logic, Minimization of interconnects impact, clock tree design issues.

UNIT-IV

Low power SoC design / Digital system, Design synergy, Low power system perspective- power gating, clock gating, adaptive voltage scaling (AVS), Static voltage scaling, Dynamic clock frequency and voltage scaling (DCFS), building block optimization, building block memory, power down techniques, power consumption verification.

UNIT-V

Synthesis: Role and Concept of graph theory and its relevance to synthesizable constructs, Walks, trails paths, connectivity, components, mapping/visualization, nodal and admittance graph. Technology independent and technology dependent approaches for synthesis, optimization constraints, Synthesis report analysis Single core and Multi core systems, dark silicon issues, HDL coding techniques for minimization of power consumption, Fault tolerant designs

TEXTBOOKS:

1. Hubert Kaeslin, "Digital Integrated Circuit Design: From VLSI Architectures to CMOS Fabrication", Cambridge University Press, 2008.
2. B. Al Hashimi, "System on chip-Next generation electronics", The IET, 2006

REFERENCES:

1. Rochit Rajsuman, "System-on- a-chip: Design and test", Advantest America R & D Center, 2000
2. P Mishra and N Dutt, "Processor Description Languages", Morgan Kaufmann, 2008
3. Michael J. Flynn and Wayne Luk, "Computer System Design: System-on-Chip". Wiley, 2011

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M.TECH.- I YEAR- II SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

COMMUNICATION BUSES AND INTERFACES (PE – IV)

Course Outcomes: At the end of the course, students will be able to:
1. Select a particular serial bus suitable for a particular application.
2. Develop APIs for configuration, reading and writing data onto serial bus.
3. Design and develop peripherals that can be interfaced to desired serial bus.

UNIT-I
Serial Busses - Physical interface, Data and Control signals, features, limitations and applications of RS232, RS485, I^2C, SPI

UNIT-II
CAN - Architecture, Data transmission, Layers, Frame formats, applications

UNIT-III
PCIe - Revisions, Configuration space, Hardware protocols, applications

UNIT-IV
USB - Transfer types, enumeration, Descriptor types and contents, Device driver

UNIT-V
Data Streaming Serial Communication Protocol - Serial Front Panel Data Port (SFPDP) using fibre optic and copper cable

TEXTBOOKS:
1. Jan Axelson, "Serial Port Complete - COM Ports, USB Virtual Com Ports, and Ports for Embedded Systems", Lakeview Research, 2nd Edition
2. Jan Axelson, "USB Complete", Penram Publications
3. Mike Jackson, Ravi Budruk, "PCI Express Technology", Mindshare Press
4. Wilfried Voss, "A Comprehensible Guide to Controller Area Network", Copperhill Media Corporation, 2nd Edition, 2005.
5. Serial Front Panel Draft Standard VITA 17.1 – 200x
6. Technical references on www.can-cia.org, www.pcisig.com, www.usb.org

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M.TECH.- I YEAR- II SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

NETWORK SECURITY AND CRYPTOGRAPHY (PE – IV)

Course Outcomes: At the end of the course, students will be able to:
1. Identify and utilize different forms of cryptography techniques.
2. Incorporate authentication and security in the network applications.
3. Distinguish among different types of threats to the system and handle the same.

UNIT-I
Security: Need, security services, Attacks, OSI Security Architecture, one-time passwords, Model for Network security, Classical Encryption Techniques like substitution ciphers, Transposition ciphers, Cryptanalysis of Classical Encryption Techniques.

UNIT-II
Number Theory: Introduction, Fermat's and Euler's Theorem, The Chinese Remainder Theorem, Euclidean Algorithm, Extended Euclidean Algorithm, and Modular Arithmetic.

UNIT-III
Private-Key (Symmetric) Cryptography: Block Ciphers, Stream Ciphers, RC4 Stream cipher, Data Encryption Standard (DES), Advanced Encryption Standard (AES), Triple DES, RC5, IDEA, Linear and Differential Cryptanalysis.

UNIT-IV
Public-Key (Asymmetric) Cryptography: RSA, Key Distribution and Management, Diffie-Hellman Key Exchange, Elliptic Curve Cryptography, Message Authentication Code, hash functions, message digest algorithms: MD4 MD5, Secure Hash algorithm, RIPEMD-160, HMAC.

UNIT-V
Authentication and System Security: IP and Web Security Digital Signatures, Digital Signature Standards, Authentication Protocols, Kerberos, IP security Architecture, Encapsulating Security Payload, Key Management, Web Security Considerations, Secure Socket Layer, Secure Electronic Transaction Intruders, Intrusion Detection, Password Management, Worms, viruses, Trojans, Virus Countermeasures, Firewalls, Trusted Systems.

TEXT BOOKS:

1. William Stallings, "Cryptography and Network Security, Principles and Practices", Pearson Education, 3rd Edition.
2. Charlie Kaufman, Radia Perlman and Mike Speciner, "Network Security, Private Communication in a Public World", Prentice Hall, 2nd Edition

REFERENCES:

1. Christopher M. King, Ertem Osmanoglu, Curtis Dalton, "Security Architecture, Design Deployment and Operations", RSA Pres,
2. Stephen Northcutt, Leny Zeltser, Scott Winters, Karen Kent, and Ronald W. Ritchey, "Inside Network Perimeter Security", Pearson Education, 2nd Edition
3. Richard Bejtlich, "The Practice of Network Security Monitoring: Understanding Incident Detection and Response", William Pollock Publisher, 2013.

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EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

PHYSICAL DESIGN AUTOMATION (PE – IV)

Course Outcomes: At the end of the course, students will be able to:

1. Study automation process for VLSI System design.
2. Understanding of fundamentals for various physical design CAD tools.
3. Develop and enhance the existing algorithms and computational techniques for physical design process of VLSI systems.

UNIT -I

Introduction to VLSI Physical Design Automation: Design Representation, VLSI Design Styles, and VLSI Physical Design automation.

UNIT -II

Partitioning, Floor planning, Pin Assignment, Standard cell, Perfomance issues in circuit layout, delay models, Layout styles.

UNIT -III

Placement: Problem formulation, classification, Simulation based placement algorithms, Partitioning based placement algorithms, Time driven and performance driven placement.

UNIT -IV

Global routing: Problem formulation, classification of global routing, Maze routing algorithms, Line-Probe algorithms, and shortest path based algorithms, Steiner Tree based algorithms, Integer programming based approach, Performance driven routing.

Detailed Routing: Problem formulation, classification, Single layer, two layer, three layer and Multi-Layer channel routing, Algorithms, Switch box routing.

UNIT -V

Over the Cell Routing - Single layer and two-layer routing: Over the cell routing, Two Layer, Three Layer and Multi-Layer OTC Routing.

Via Minimization: Constraint and Unconstrained via minimization.

Clock and Power Routing: Clocking schemes, design considerations for the clock , Problem formulation, Clock routing algorithms, Skew and Delay reduction by Pin Assignment, Multiple clock routing, Power and Ground Routing

TEXT BOOKS

1. Algorithms for VLSI Physical Design Automation – Naveed Sherwani, 3rd Ed., 2005,
2. Algorithms for VLSI Design Automation, S.H.Gerez, 1999, WILEY Student Edition, John wiley & Sons (Asia) Pvt. Ltd.

REFERENCE BOOKS:

1. Computer Aided Logical Design with Emphasis on VLSI – Hill & Peterson, 1993, Wiley.
2. Modern VLSI Design: Systems on silicon – Wayne Wolf, 2nd ed., 1998, Pearson Education Asia

4) Perform the following

a) Draw small signal voltage gain of the minimum-size inverter in 0.18um and 0.13um technology as a function of input DC voltage. Determine the small signal voltage gain at the switching point using Ngspice and compare the values for 0.18um and 0.13um process.

b) Consider a simple CS amplifier with active load, as explained in the lecture, with NMOS transistor MN as driver and PMOS transistor MP as load, in 0.18um technology. (W/L)MN=5, (W/L)MP=10 and L=0.5um for both transistors.

i. Establish a test bench, as explained in the lecture, to achieve VDSQ=VDD/2.

ii. Calculate input bias voltage if bias current=50uA.

iii. Use Ngspice and obtain the bias current. Compare its value with 50uA.

iv. Determine small signal voltage gain, -3dB BW and GBW of the amplifier using small signal analysis in Ngspice (consider 30fF load capacitance).

v. Plot step response of the amplifier for input pulse amplitude of 0.1V. Derive time constant of the output and compare it with the time constant resulted from -3dB BW

vi. Use Ngspice to determine input voltage range of the amplifier

5) Three OPAMP INA. Vdd=1.8V Vss=0V, CAD tool: Mentor Graphics DA.

Note: Adjust accuracy options of the simulator (setup->options in GUI).

Use proper values of resistors to get a three OPAMP INA with differential-mode voltage gain=10. Consider voltage gain=2 for the first stage and voltage gain=5 for the second stage.

i. Draw the schematic of op-amp macro model.

ii. Draw the schematic of INA.

iii. Obtain parameters of the op-amp macro model such that

a. low-frequency voltage gain = 5x104,

b. unity gain BW (fu) = 500KHz,

c. input capacitance=0.2pF,

d. output resistance = ,

e. CMRR=120dB

iv. Draw schematic diagram of CMRR simulation setup.

v. Simulate CMRR of INA using AC analysis (it's expected to be around 6dB below CMRR of OPAMP).

vi. Plot CMRR of the INA versus resistor mismatches (for resistors of second stage only) changing from -5% to +5% (use AC analysis). Generate a separate plot for mismatch in each resistor pair. Explain how CMRR of OPAMP changes with resistor mismatches.

vii. Repeat (iii) to (vi) by considering CMRR of all OPAMPs to be 90dB.

6) Technology: UMC 0.18um, VDD=1.8V. Use MAGIC or Microwind.

a) Draw layout of a minimum size inverter in UMC 0.18um technology using MAGIC Station layout editor. Use that inverter as a cell and lay out three cascaded minimum-sized inverters. Use M1 as interconnect line between inverters.

b) Run DRC, LVS and RC extraction. Make sure there is no DRC error. Extract the netlist.

c) Use extracted netlist and obtain tPHLtPLH for the middle inverter using Eldo.

d) Use interconnect length obtained and connect the second and third inverter.
Extract the new netlist and obtain tPHL and tPLH of the middle inverter. Compare new values of delay times with corresponding values obtained in part 'c'

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M.TECH.- I YEAR- II SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

SYSTEM DESIGN WITH EMBEDDED LINUX LAB (Lab – IV)

List of Experiments

1. **Functional Testing Of Devices:** Flashing the OS on to the device into a stable functional state by porting desktop environment with necessary packages.
2. **Exporting Display On To Other Systems:** Making use of available laptop/desktop displays as a display for the device using SSH client & X11 display server.
3. **GPIO Programming:** Programming of available GPIO pins of the corresponding device using native programming language. Interfacing of I/O devices like LED/Switch etc., and testing the functionality.
4. **Interfacing Chronos eZ430:** Chronos device is a programmable texas instruments watch which can be used for multiple purposes like PPT control, Mouse operations etc., Exploit the features of the device by interfacing with devices.
5. **ON/OFF Control Based On Light Intensity:** Using the light sensors, monitor the surrounding light intensity & automatically turn ON/OFF the high intensity LED's by taking some pre-defined threshold light intensity value.
6. **Battery Voltage Range Indicator:** Monitor the voltage level of the battery and indicating the same using multiple LED's (for ex: for 3V battery and 3 led's, turn on 3 led's for 2-3V, 2 led's for 1-2V, 1 led for 0.1-1V & turn off all for 0V)
7. **Dice Game Simulation:** Instead of using the conventional dice, generate a random value similar to dice value and display the same using a 16X2 LCD. A possible extension could be to provide the user with option of selecting single or double dice game.
8. **Displaying RSS News Feed On Display Interface:** Displaying the RSS news feed headlines on a LCD display connected to device. This can be adapted to other websites like twitter or other information websites. Python can be used to acquire data from the internet.
9. **Porting Openwrt To the Device:** Attempt to use the device while connecting to a wifi network using a USB dongle and at the same time providing a wireless access point to the dongle.
10. **Hosting a website on Board:** Building and hosting a simple website(static/dynamic) on the device and make it accessible online. There is a need to install server (eg: Apache) and thereby host the website.
11. **Webcam Server:** Interfacing the regular usb webcam with the device and turn it into fully functional IP webcam & test the functionality.
12. **FM Transmission:** Transforming the device into a regular fm transmitter capable of transmitting audio at desired frequency (generally 88-108 Mhz)

Note: Devices mentioned in the above lists include Arduino, Raspbery Pi, Beaglebone

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M.TECH.- II YEAR- I SEMESTER
EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

IOT AND ITS APPLICATIONS (PE – V)

Course Outcomes: At the end of this course, students will be able to
1. Understand the concept of IOT and M2M
2. Study IOT architecture and applications in various fields
3. Study the security and privacy issues in IOT.

UNIT-I
IoT & Web Technology The Internet of Things Today, Time for Convergence, Towards the IoT Universe, Internet of Things Vision, IoT Strategic Research and Innovation Directions, IoT Applications, Future Internet Technologies, Infrastructure, Networks and Communication, Processes, Data Management, Security, Privacy & Trust, Device Level Energy Issues, IoT Related Standardization, Recommendations on Research Topics.

UNIT-II
M2M to IoT – A Basic Perspective– Introduction, Some Definitions, M2M Value Chains, IoT Value Chains, An emerging industrial structure for IoT, The international driven global value
chain and global information monopolies. M2M to IoT-An Architectural Overview– Building an architecture, Main design principles and needed capabilities, An IoT architecture outline, standards considerations.

UNIT-III
IoT Architecture -State of the Art – Introduction, State of the art, Architecture Reference Model-Introduction, Reference Model and architecture, IoT reference Model, IoT Reference Architecture-Introduction, Functional View, Information View, Deployment and Operational View, Other Relevant architectural views.

UNIT-IV
IoT Applications for Value Creations Introduction, IoT applications for industry: Future Factory Concepts, Brownfield IoT, Smart Objects, Smart Applications, Four Aspects in your Business to Master IoT, Value Creation from Big Data and Serialization, IoT for Retailing Industry, IoT For Oil and Gas Industry, Opinions on IoT Application and Value for Industry, Home Management, eHealth.

UNIT-V
Internet of Things Privacy, Security and Governance Introduction, Overview of Governance, Privacy and Security Issues,

TEXTBOOKS

1. Vijay Madisetti and Arshdeep Bahga, "Internet of Things (A Hands-on-Approach)", 1st Edition, VPT, 2014.
2. Francis daCosta, "Rethinking the Internet of Things: A Scalable Approach to Connecting Everything", 1st Edition, Apress Publications, 2013.
3. Cuno Pfister, "Getting Started with the Internet of Things", O Reilly Media, 2011.

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AI AND MACHINE LEARNING (PE – V)

UNIT- I
Supervised Learning (Regression/Classification)
Basic methods: Distance-based methods, Nearest-Neighbours, Decision Trees, Naive Bayes Linear models: Linear Regression, Logistic Regression, Generalized Linear Models Support Vector Machines, Nonlinearity and Kernel Methods
Beyond Binary Classification: Multi-class/Structured Outputs, Ranking

UNIT- II
Unsupervised Learning
Clustering: K-means/Kernel K-means
Dimensionality Reduction: PCA and kernel PCA
Matrix Factorization and Matrix Completion
Generative Models (mixture models and latent factor models)

UNIT- III
Evaluating Machine Learning algorithms and Model Selection, Introduction to Statistical Learning Theory, Ensemble Methods (Boosting, Bagging, Random Forests)

UNIT- IV
Biological foundations to intelligent Systems: Artificial Neural Networks.
Single layer and Multilayer Feed Forward NN, LMS and Back Propagation. Algorithm, Feedback networks and Radial Basis Function Networks

UNIT- V
Fuzzy Logic, Knowledge Representation and Inference Mechanism, Defuzzification Methods Fuzzy Neural Networks and some algorithms to learn the parameters of the network like GA

TEXTBOOKS:

1. Kevin Murphy, Machine Learning: A Probabilistic Perspective, MIT Press, 2012
2. Trevor Hastie, Robert Tibshirani, Jerome Friedman, The Elements of Statistical Learning, Springer 2009 (freely available online)
3. Christopher Bishop, Pattern Recognition and Machine Learning, Springer, 2007.
4. J M Zurada , "An Introduction to ANN",Jaico Publishing House
5. Simon Haykins, "Neural Networks", Prentice Hall

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EMBEDDED SYSTEMS & VLSI DESIGN/VLSI & EMBEDDED SYSTEMS

NANOMATERIALS AND NANOTECHNOLOGY (PE - V)

Course Outcomes: At the end of the course, students will be able to:
1. To understand the basic science behind the design and fabrication of nano scale systems.
2. To understand and formulate new engineering solutions for current problems and competing technologies for future applications.
3. To be able make inter disciplinary projects applicable to wide areas by clearing and fixing the boundaries in system development.
4. To gather detailed knowledge of the operation of fabrication and characterization devices to achieve precisely designed systems

UNIT - I
Introduction of nano materials and nanotechnologies, Features of nanostructures, Applications of nano materials and technologies. Nano dimensional Materials 0D, 1D, 2D structures – Size Effects – Fraction of Surface Atoms – specific Surface Energy and Surface Stress – Effect on the Lattice Parameter – Phonon Density of States – the General Methods available for the Synthesis of Nanostrutures – precipitative – reactive –hydrothermal/solvo thermal methods – suitability of such methods for scaling – potential Uses.

UNIT - II
Fundamentals of nanomaterials, Classification, Zero-dimensional nanomaterials, One-dimensional nanomaterials, Two-dimensional nanomaterials, Three dimensional nanomaterials. Low-Dimensional Nanomaterials and its Applications, Synthesis, Properties, and Applications of Low-Dimensional Carbon-Related Nanomaterials.

UNIT - III
Micro- and Nanolithography Techniques, Emerging Applications Introduction to Micro electro mechanical Systems (MEMS), Advantages and Challenges of MEMS, Fabrication Technologies, Surface Micromachining, Bulk Micromachining, Molding. Introduction to Nano Phonics.

UNIT - IV
Introduction, Synthesis of CNTs - Arc-discharge, Laser-ablation, Catalytic growth, Growth mechanisms of CNT's - Multi-walled nanotubes, Single-walled nanotubes Optical properties of CNT's, Electrical transport in perfect nanotubes, Applications as case studies. Synthesis and Applications of CNT's.

UNIT - V
Ferroelectric materials, coating, molecular electronics and nanoelectronics, biological and environmental, membrane based application, polymer based application.

TEXT BOOKS

1. Kenneth J. Klabunde and Ryan M. Richards, "Nanoscale Materials in Chemistry", 2 edition, John Wiley and Sons, 2009.
2. I Gusev and A A Rempel, "Nanocrystalline Materials", Cambridge International Science Publishing, 1 st Indian edition by Viva Books Pvt. Ltd. 2008.
3. B. S. Murty, P. Shankar, Baldev Raj, B. B. Rath, James Murday, "Nanoscience and

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4. Nanotechnology", Tata McGraw Hill Education 2012.

REFERENCE BOOKS

1. Bharat Bhushan, "Springer Handbook of Nanotechnology", Springer, 3 rd edition, 2010.
2. Kamal K. Kar, "Carbon Nanotubes: Synthesis, Characterization and Applications", Research Publishing Services; 1 st edition, 2011, ISBN-13: 978-9810863975.

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. (ES & VLSI DESIGN/VLSI & ES)

ENGLISH FOR RESEARCH PAPER WRITING (Audit Course - I & II)

Prerequisite: None

Course objectives: Students will be able to:

- Understand that how to improve your writing skills and level of readability
- Learn about what to write in each section
- Understand the skills needed when writing a Title Ensure the good quality of paper at very first-time submission

UNIT-I:
Planning and Preparation, Word Order, Breaking up long sentences, Structuring Paragraphs and Sentences, Being Concise and Removing Redundancy, Avoiding Ambiguity and Vagueness

UNIT-II:
Clarifying Who Did What, Highlighting Your Findings, Hedging and Criticizing, Paraphrasing and Plagiarism, Sections of a Paper, Abstracts. Introduction

UNIT-III:
Review of the Literature, Methods, Results, Discussion, Conclusions, The Final Check.

UNIT-IV:
key skills are needed when writing a Title, key skills are needed when writing an Abstract, key skills are needed when writing an Introduction, skills needed when writing a Review of the Literature,

UNIT-V:
skills are needed when writing the Methods, skills needed when writing the Results, skills are needed when writing the Discussion, skills are needed when writing the Conclusions. useful phrases, how to ensure paper is as good as it could possibly be the first- time submission

TEXT BOOKS/ REFERENCES:

1. Goldbort R (2006) Writing for Science, Yale University Press (available on Google Books)
2. Day R (2006) How to Write and Publish a Scientific Paper, Cambridge University Press
3. Highman N (1998), Handbook of Writing for the Mathematical Sciences, SIAM. Highman's book.
4. Adrian Wallwork, English for Writing Research Papers, Springer New York Dordrecht Heidelberg London, 2011

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. (ES & VLSI DESIGN/VLSI & ES)

DISASTER MANAGEMENT (Audit Course - I & II)

Prerequisite: None

Course Objectives: Students will be able to

- learn to demonstrate a critical understanding of key concepts in disaster risk reduction and humanitarian response.
- critically evaluate disaster risk reduction and humanitarian response policy and practice from multiple perspectives.
- develop an understanding of standards of humanitarian response and practical relevance in specific types of disasters and conflict situations.
- critically understand the strengths and weaknesses of disaster management approaches,
- planning and programming in different countries, particularly their home country or the countries they work in

UNIT-I:
Introduction:
Disaster: Definition, Factors and Significance; Difference Between Hazard and Disaster; Natural and Manmade Disasters: Difference, Nature, Types and Magnitude.
Disaster Prone Areas in India:
Study of Seismic Zones; Areas Prone to Floods and Droughts, Landslides and Avalanches; Areas Prone to Cyclonic and Coastal Hazards with Special Reference to Tsunami; Post-Disaster Diseases and Epidemics

UNIT-II:
Repercussions of Disasters and Hazards:
Economic Damage, Loss of Human and Animal Life, Destruction of Ecosystem. Natural Disasters: Earthquakes, Volcanisms, Cyclones, Tsunamis, Floods, Droughts and Famines, Landslides and Avalanches, Man-made disaster: Nuclear Reactor Meltdown, Industrial Accidents, Oil Slicks and Spills, Outbreaks of Disease and Epidemics, War and Conflicts.

UNIT-III:
Disaster Preparedness and Management:
Preparedness: Monitoring of Phenomena Triggering A Disaster or Hazard; Evaluation of Risk: Application of Remote Sensing, Data from Meteorological and Other Agencies, Media Reports: Governmental and Community Preparedness.

UNIT-IV:
Risk Assessment Disaster Risk:
Concept and Elements, Disaster Risk Reduction, Global and National Disaster Risk Situation. Techniques of Risk Assessment, Global Co-Operation in Risk Assessment and Warning, People's Participation in Risk Assessment. Strategies for Survival.

UNIT-V:
Disaster Mitigation:
Meaning, Concept and Strategies of Disaster Mitigation, Emerging Trends In Mitigation. Structural Mitigation and Non-Structural Mitigation, Programs of Disaster Mitigation in India.

TEXT BOOKS/ REFERENCES:

1. R. Nishith, Singh AK, "Disaster Management in India: Perspectives, issues and strategies "New Royal book Company.
2. Sahni, Pardeep Et. Al. (Eds.)," Disaster Mitigation Experiences and Reflections", Prentice Hall of India, New Delhi.
3. Goel S. L., Disaster Administration and Management Text and Case Studies", Deep &Deep Publication Pvt. Ltd., New Delhi.

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M. Tech. (ES & VLSI DESIGN/VLSI & ES)

SANSKRIT FOR TECHNICAL KNOWLEDGE (Audit Course - I & II)

Prerequisite: None

Course Objectives:
- To get a working knowledge in illustrious Sanskrit, the scientific language in the world
- Learning of Sanskrit to improve brain functioning
- Learning of Sanskrit to develop the logic in mathematics, science & other subjects enhancing the memory power
- The engineering scholars equipped with Sanskrit will be able to explore the huge knowledge from ancient literature

Course Outcomes: Students will be able to
- Understanding basic Sanskrit language
- Ancient Sanskrit literature about science & technology can be understood
- Being a logical language will help to develop logic in students

UNIT-I:
Alphabets in Sanskrit,

UNIT-II:
Past/Present/Future Tense, Simple Sentences

UNIT-III:
Order, Introduction of roots,

UNIT-IV:
Technical information about Sanskrit Literature

UNIT-V:
Technical concepts of Engineering-Electrical, Mechanical, Architecture, Mathematics

TEXT BOOKS/ REFERENCES:

1. "Abhyaspustakam" – Dr. Vishwas, Samskrita-Bharti Publication, New Delhi
2. "Teach Yourself Sanskrit" Prathama Deeksha-Vempati Kutumbshastri, Rashtriya Sanskrit Sansthanam, New Delhi Publication
3. "India's Glorious Scientific Tradition" Suresh Soni, Ocean books (P) Ltd., New Delhi.

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M. Tech. (ES & VLSI DESIGN/VLSI & ES)

VALUE EDUCATION (Audit Course - I & II)

Prerequisite: None

Course Objectives: Students will be able to
- Understand value of education and self- development
- Imbibe good values in students
- Let the should know about the importance of character

Course outcomes: Students will be able to
- Knowledge of self-development
- Learn the importance of Human values
- Developing the overall personality

UNIT-I:
Values and self-development –Social values and individual attitudes. Work ethics, Indian vision of humanism. Moral and non- moral valuation. Standards and principles. Value judgements

UNIT-II:
Importance of cultivation of values. Sense of duty. Devotion, Self-reliance. Confidence, Concentration. Truthfulness, Cleanliness. Honesty, Humanity. Power of faith, National Unity. Patriotism. Love for nature, Discipline

UNIT-III:
Personality and Behavior Development - Soul and Scientific attitude. Positive Thinking. Integrity and discipline, Punctuality, Love and Kindness.

UNIT-IV:
Avoid fault Thinking. Free from anger, Dignity of labour. Universal brotherhood and religious tolerance. True friendship. Happiness Vs suffering, love for truth. Aware of self-destructive habits. Association and Cooperation. Doing best for saving nature

UNIT-V:
Character and Competence –Holy books vs Blind faith. Self-management and Good health. Science of reincarnation, Equality, Nonviolence, Humility, Role of Women. All religions and same message. Mind your Mind, Self-control. Honesty, Studying effectively

TEXT BOOKS/ REFERENCES:

1. Chakroborty, S.K. "Values and Ethics for organizations Theory and practice", Oxford University Press, New Delhi

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CONSTITUTION OF INDIA (Audit Course - I & II)

Prerequisite: None

Course Objectives: Students will be able to:
- Understand the premises informing the twin themes of liberty and freedom from a civil rights perspective.
- To address the growth of Indian opinion regarding modern Indian intellectuals' constitutional role and entitlement to civil and economic rights as well as the emergence of nationhood in the early years of Indian nationalism.
- To address the role of socialism in India after the commencement of the Bolshevik Revolution in 1917 and its impact on the initial drafting of the Indian Constitution.

Course Outcomes: Students will be able to:
- Discuss the growth of the demand for civil rights in India for the bulk of Indians before the arrival of Gandhi in Indian politics.
- Discuss the intellectual origins of the framework of argument that informed the conceptualization of social reforms leading to revolution in India.
- Discuss the circumstances surrounding the foundation of the Congress Socialist Party [CSP] under the leadership of Jawaharlal Nehru and the eventual failure of the proposal of direct elections through adult suffrage in the Indian Constitution.
- Discuss the passage of the Hindu Code Bill of 1956.

UNIT-I:
History of Making of the Indian Constitution: History Drafting Committee, (Composition & Working), **Philosophy of the Indian Constitution:** Preamble, Salient Features.

UNIT-II:
Contours of Constitutional Rights & Duties: Fundamental Rights Right to Equality, Right to Freedom, Right against Exploitation, Right to Freedom of Religion, Cultural and Educational Rights, Right to Constitutional Remedies, Directive Principles of State Policy, Fundamental Duties.

UNIT-III:
Organs of Governance: Parliament, Composition, Qualifications and Disqualifications, Powers and Functions, Executive, President, Governor, Council of Ministers, Judiciary, Appointment and Transfer of Judges, Qualification, Powers and Functions.

UNIT-IV:
Local Administration: District's Administration head: Role and Importance, Municipalities: Introduction, Mayor and role of Elected Representative, CEO of Municipal Corporation. Pachayati raj: Introduction, PRI: Zila Pachayat. Elected officials and their roles, CEO Zila Pachayat: Position and role. Block level: Organizational Hierarchy (Different departments), Village level: Role of Elected and Appointed officials, Importance of grass root democracy.

UNIT-V:
Election Commission: Election Commission: Role and Functioning. Chief Election Commissioner and Election Commissioners. State Election Commission: Role and Functioning. Institute and Bodies for the welfare of SC/ST/OBC and women.

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TEXT BOOKS/ REFERENCES:

1. The Constitution of India, 1950 (Bare Act), Government Publication.
2. Dr. S. N. Busi, Dr. B. R. Ambedkar framing of Indian Constitution, 1st Edition, 2015.
3. M. P. Jain, Indian Constitution Law, 7th Edn., Lexis Nexis, 2014.
4. D.D. Basu, Introduction to the Constitution of India, Lexis Nexis, 2015.

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PEDAGOGY STUDIES (Audit Course - I & II)

Prerequisite: None

Course Objectives: Students will be able to:

- Review existing evidence on the review topic to inform programme design and policy making undertaken by the DfID, other agencies and researchers.
- Identify critical evidence gaps to guide the development.

Course Outcomes: Students will be able to understand:

- What pedagogical practices are being used by teachers in formal and informal classrooms in developing countries?
- What is the evidence on the effectiveness of these pedagogical practices, in what conditions, and with what population of learners?
- How can teacher education (curriculum and practicum) and the school curriculum and guidance materials best support effective pedagogy?

UNIT-I:
Introduction and Methodology: Aims and rationale, Policy background, Conceptual framework and terminology Theories of learning, Curriculum, Teacher education. Conceptual framework, Research questions. Overview of methodology and Searching.

UNIT-II:
Thematic overview: Pedagogical practices are being used by teachers in formal and informal classrooms in developing countries. Curriculum, Teacher education.

UNIT-III:
Evidence on the effectiveness of pedagogical practices, Methodology for the indepth stage: quality assessment of included studies. How can teacher education (curriculum and practicum) and the scho curriculum and guidance materials best support effective pedagogy? Theory of change. Strength and nature of the body of evidence for effective pedagogical practices. Pedagogic theory and pedagogical approaches. Teachers' attitudes and beliefs and Pedagogic strategies.

UNIT-IV:
Professional development: alignment with classroom practices and follow-up support, Peer support, Support from the head teacher and the community. Curriculum and assessment, Barriers to learning: limited resources and large class sizes

UNIT-V:
Research gaps and future directions: Research design, Contexts, Pedagogy, Teacher education, Curriculum and assessment, Dissemination and research impact.

TEXT BOOKS/ REFERENCES:

1. Ackers J, Hardman F (2001) Classroom interaction in Kenyan primary schools, Compare, 31 (2): 245-261.
2. Agrawal M (2004) Curricular reform in schools: The importance of evaluation, Journal of Curriculum Studies, 36 (3): 361-379.
3. Akyeampong K (2003) Teacher training in Ghana - does it count? Multi-site teacher education research project (MUSTER) country report 1. London: DFID.

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4. Akyeampong K, Lussier K, Pryor J, Westbrook J (2013) Improving teaching and learning of basic maths and reading in Africa: Does teacher preparation count? International Journal Educational Development, 33 (3): 272–282.
5. Alexander RJ (2001) Culture and pedagogy: International comparisons in primary education. Oxford and Boston: Blackwell.
6. Chavan M (2003) Read India: A mass scale, rapid, 'learning to read' campaign.
7. www.pratham.org/images/resource%20working%20paper%202.pdf.

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M. Tech. (ES & VLSI DESIGN/VLSI & ES)

STRESS MANAGEMENT BY YOGA (Audit Course - I & II)

Prerequisite: None

Course Objectives:
- To achieve overall health of body and mind
- To overcome stress

Course Outcomes: Students will be able to:
- Develop healthy mind in a healthy body thus improving social health also
- Improve efficiency

UNIT-I:
Definitions of Eight parts of yog. (Ashtanga)

UNIT-II:
Yam and Niyam.

UNIT-III:
Do`s and Don't's in life.
i) Ahinsa, satya, astheya, bramhacharya and aparigraha
ii) Shaucha, santosh, tapa, swadhyay, ishwarpranidhan

UNIT-IV:
Asan and Pranayam

UNIT-V:
i) Various yog poses and their benefits for mind & body
ii) Regularization of breathing techniques and its effects-Types of pranayam

TEXT BOOKS/ REFERENCES:

1. 'Yogic Asanas for Group Tarining-Part-I": Janardan Swami Yogabhyasi Mandal, Nagpur
2. "Rajayoga or conquering the Internal Nature" by Swami Vivekananda, Advaita Ashrama (Publication Department), Kolkata

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M. Tech. (ES & VLSI DESIGN/VLSI & ES)

PERSONALITY DEVELOPMENT THROUGH LIFE ENLIGHTENMENT SKILLS
(Audit Course - I & II)

Prerequisite: None

Course Objectives:

- To learn to achieve the highest goal happily
- To become a person with stable mind, pleasing personality and determination
- To awaken wisdom in students

Course Outcomes: Students will be able to

- Study of Shrimad-Bhagwad-Geeta will help the student in developing his personality and achieve the highest goal in life
- The person who has studied Geeta will lead the nation and mankind to peace and prosperity
- Study of Neetishatakam will help in developing versatile personality of students

UNIT-I:

Neetisatakam-Holistic development of personality

- Verses- 19,20,21,22 (wisdom)
- Verses- 29,31,32 (pride & heroism)
- Verses- 26,28,63,65 (virtue)

UNIT-II:

Neetisatakam-Holistic development of personality

- Verses- 52,53,59 (dont's)
- Verses- 71,73,75,78 (do's)

UNIT-III:

Approach to day to day work and duties.

- Shrimad Bhagwad Geeta: Chapter 2-Verses 41, 47,48,
- Chapter 3-Verses 13, 21, 27, 35, Chapter 6-Verses 5,13,17, 23, 35,
- Chapter 18-Verses 45, 46, 48.

UNIT-IV:

Statements of basic knowledge.

- Shrimad Bhagwad Geeta: Chapter2-Verses 56, 62, 68
- Chapter 12 -Verses 13, 14, 15, 16,17, 18
- Personality of Role model. Shrimad Bhagwad Geeta:

UNIT-V:

- Chapter2-Verses 17, Chapter 3-Verses 36,37,42,
- Chapter 4-Verses 18, 38,39
- Chapter18 – Verses 37,38,63

TEXT BOOKS/ REFERENCES:

1. "Srimad Bhagavad Gita" by Swami Swarupananda Advaita Ashram (Publication Department), Kolkata.
2. Bhartrihari's Three Satakam (Niti-sringar-vairagya) by P.Gopinath, Rashtriya Sanskrit Sansthanam, New Delhi.